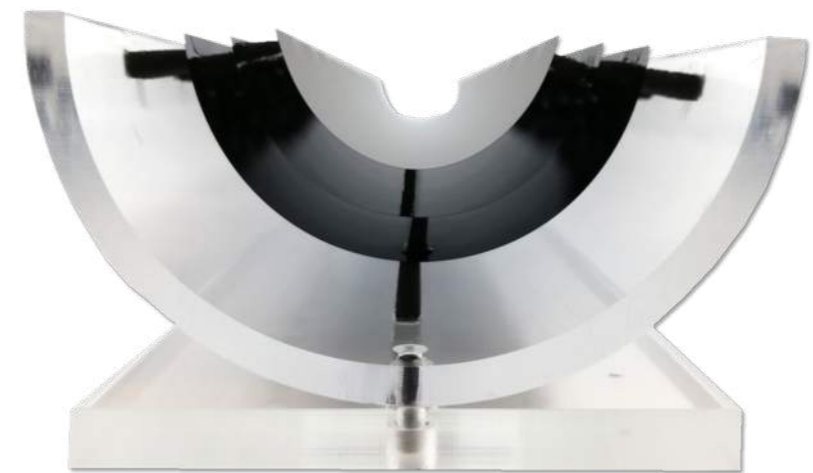




**ALICE**

## **ALICE ITS3** the first truly cylindrical inner tracker

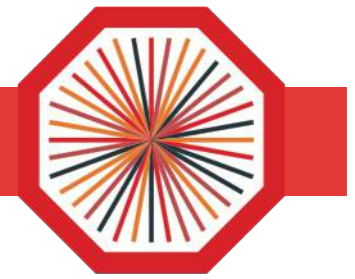


Cosimo Pastore  
INFN Bari

on behalf of the ALICE Collaboration



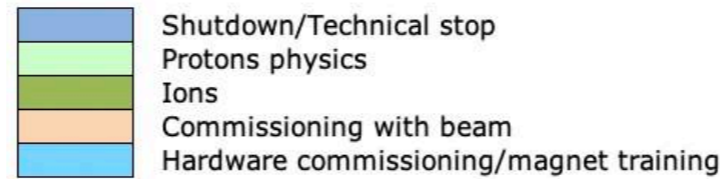
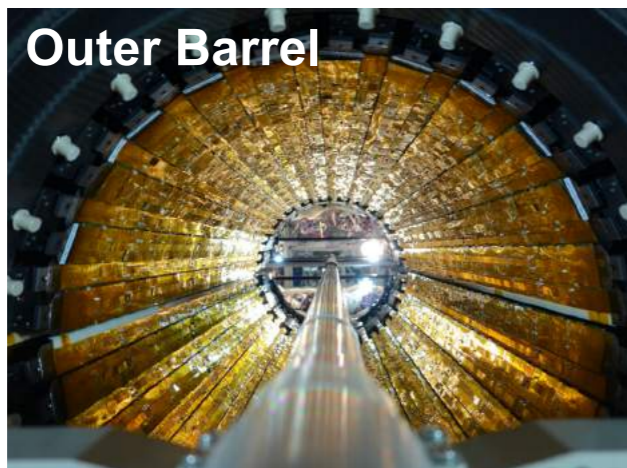
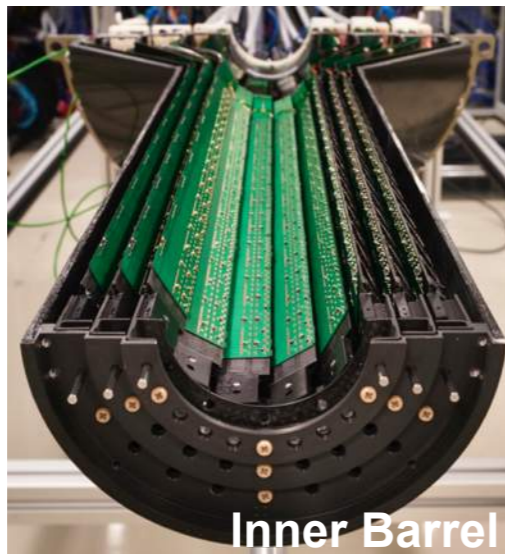
© Klaus Barth



# ALICE Inner Tracking upgrade roadmap



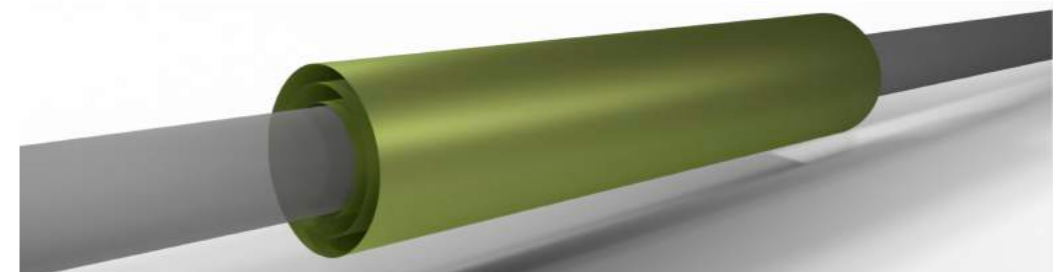
## ITS2 for LHC Run 3



## ITS3 for LHC Run 4

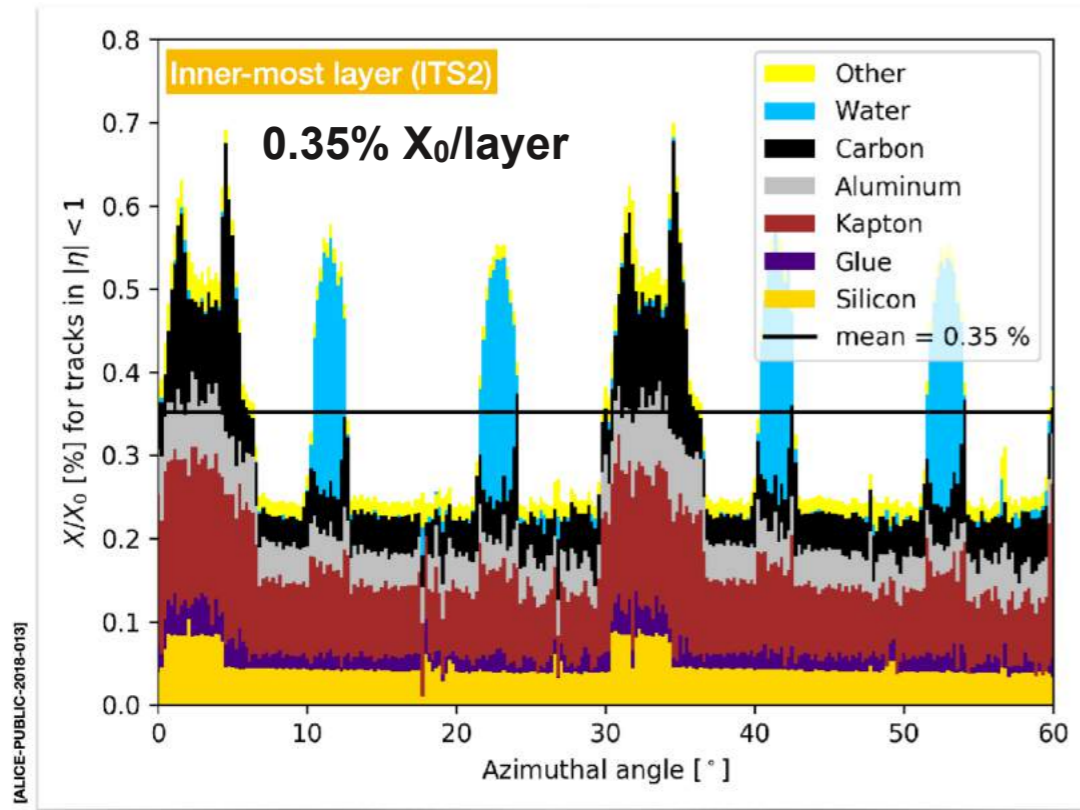
Can we get closer to the IP?  
Can we reduce the material budget?

**The way:** replace detector staves (3 innermost layers) by wafer-scale sensors bent around the beam pipe



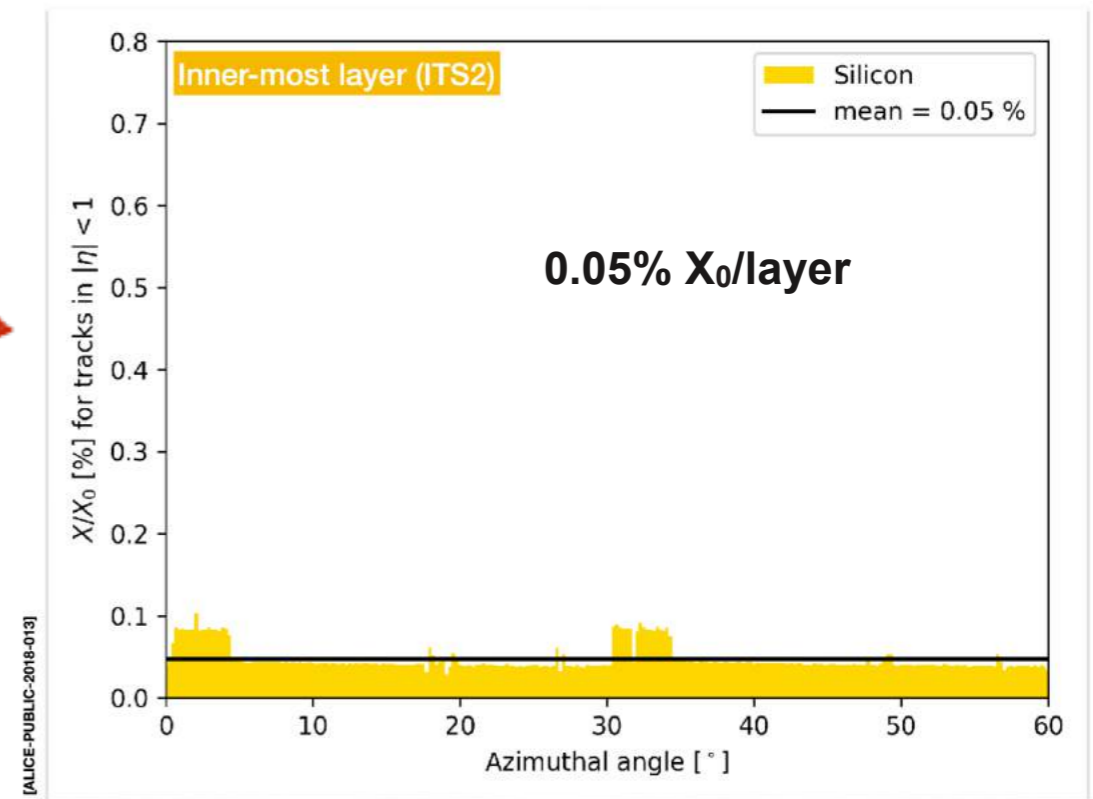
ITS2 installed and under commissioning

# Motivation for ITS3



## Observations

- » Silicon makes only about 15% of total material
- » Irregularities due to support/cooling and overlap



## Improvements

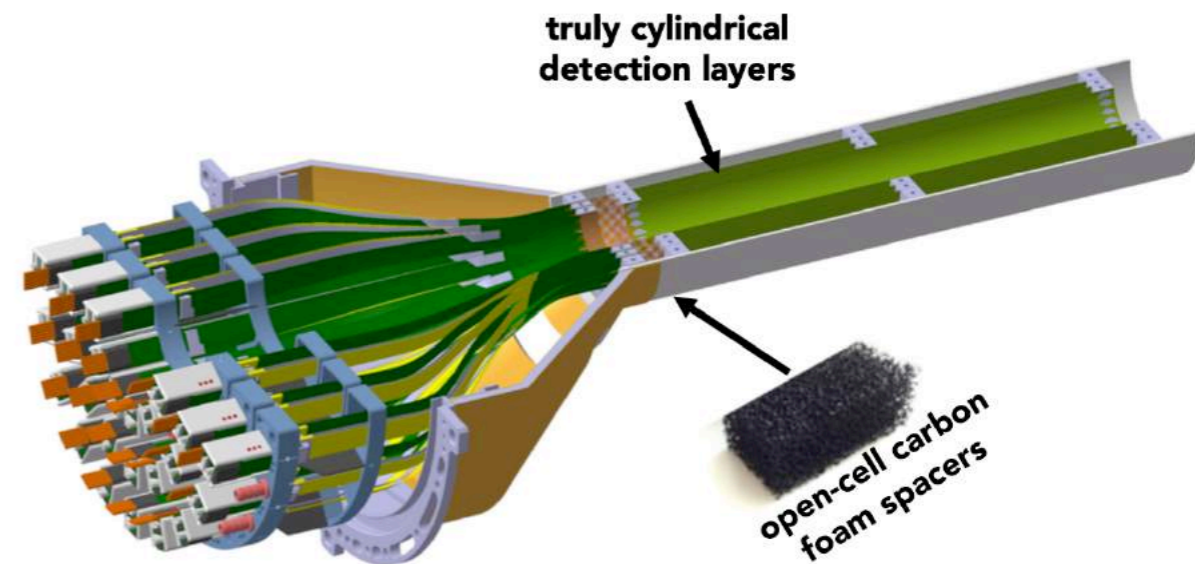
- » Removal of water cooling
  - **possible** if power consumption stays below 20 mW/cm<sup>2</sup>
  - move to (low flow) air cooling system
- » Removal circuit board (power+data)
  - **possible** if integrated on chip
- » Removal of mechanical support
  - **benefit** from increased stiffness by rolling Si wafers



# ITS3 detector concept

## Key ingredients

- » Wafer-scale chips (up to  $\sim 28 \times 10$  cm), fabricated using stitching
- » Sensor thickness 20-40  $\mu\text{m}$
- » Chips bent in cylindrical shape at target radii
- » Si MAPS sensor based on 65 nm technology
- » Carbon foam structures
- » Smaller beam pipe diameter and wall thickness ( $0.14\% X_0$ )



**The whole detector will comprise six chips (current ITS IB: 432) and barely anything else!**

## Key benefits

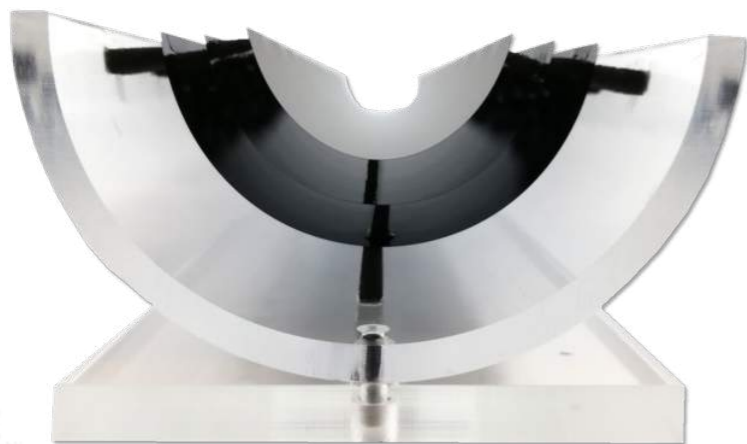
- » Extremely low material budget:  $0.02-0.04\% X_0$
- » Homogeneous material distribution: negligible systematic error from material distribution

<b>Beam pipe inner/outer radius (mm)</b>	16.0/16.5		
<b>IB Layer Parameters</b>	Layer 0	Layer 1	Layer 2
<b>Radial position (mm)</b>	18.0	24.0	30.0
<b>Length of sensitive area (mm)</b>	300.0		
<b>Pseudo-rapidity coverage</b>	$\pm 2.5$	$\pm 2.3$	$\pm 2.0$
<b>Active-area (cm<sup>2</sup>)</b>	610	816	1016
<b>Pixel sensor dimension (mm<sup>2</sup>)</b>	280 × 56.5	280 × 75.5	280 × 94
<b>Number of sensors per layer</b>	2		
<b>Pixel size (<math>\mu\text{m}^2</math>)</b>	O (10 × 10)		



## Detector Integration

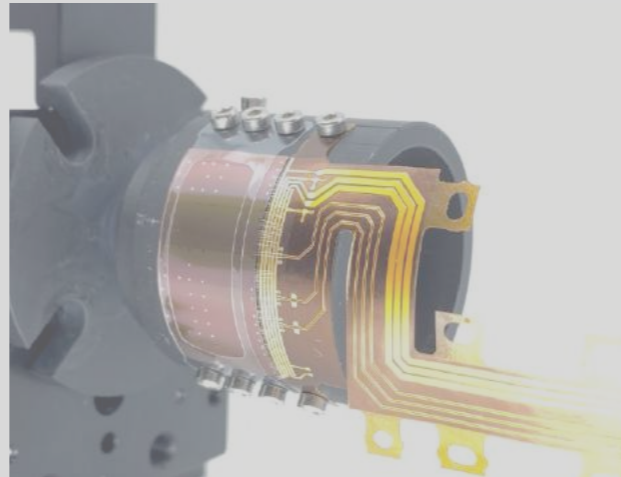
Tests with wafer-scale dummy chips for mechanical integration



↑  
**This talk!**

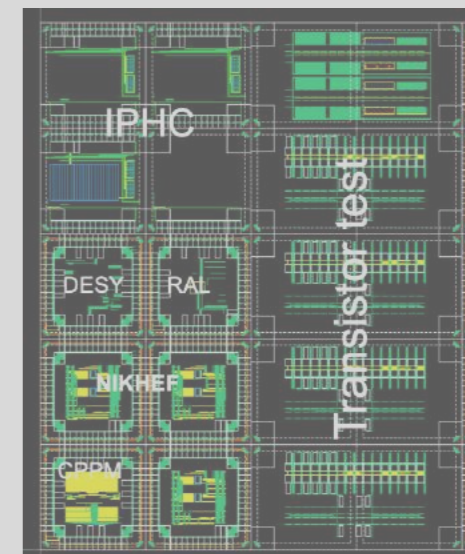
## Sensor performance

Tests with existing bent ALPIDE chips (ITS2) for (in-beam) performance assessment

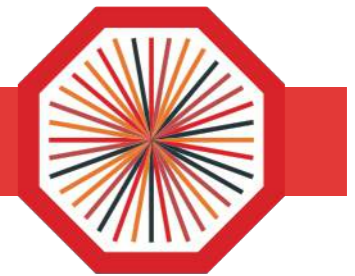


## Chip design

New, stitched sensor in 65 nm technology on 300 mm wafers

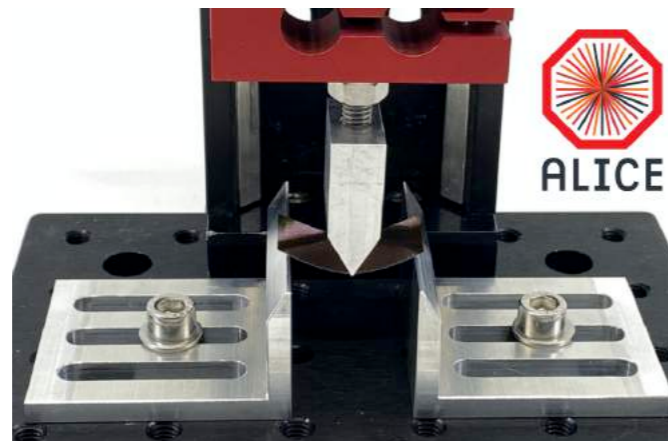


# ITS3 R&D lines - Detector Integration

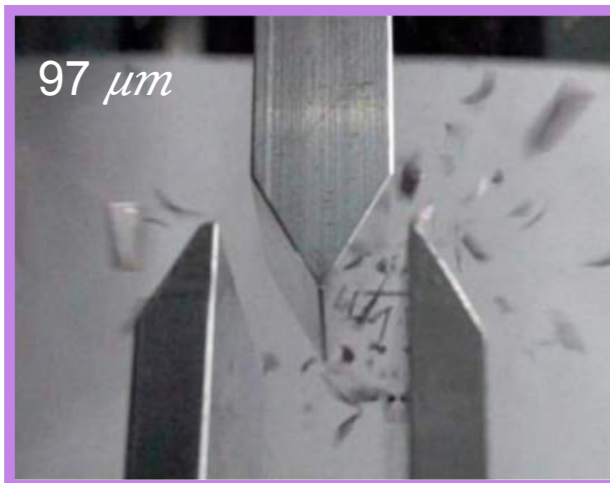
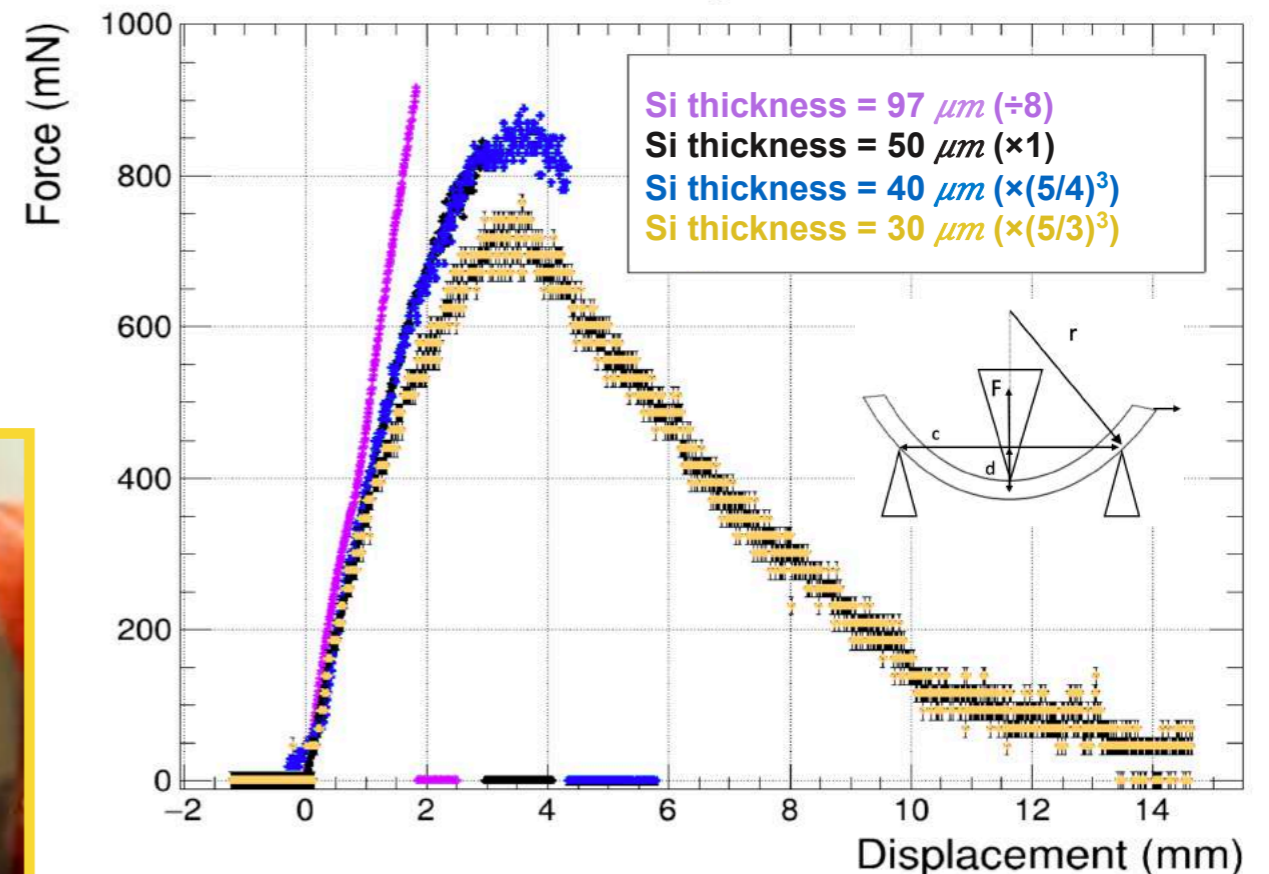


## ALPIDE CHIP BENDING

- » MAPS at thickness used in current detectors ( $\sim 50 \mu\text{m}$ ) are quite flexible
- » Large benefit from going even a bit thinner: the bending force scales with thickness to the third power
- » The breaking point moves to smaller bending radii when going thinner
- » Project goal thicknesses and desired bending radii are in a “not breaking” regime



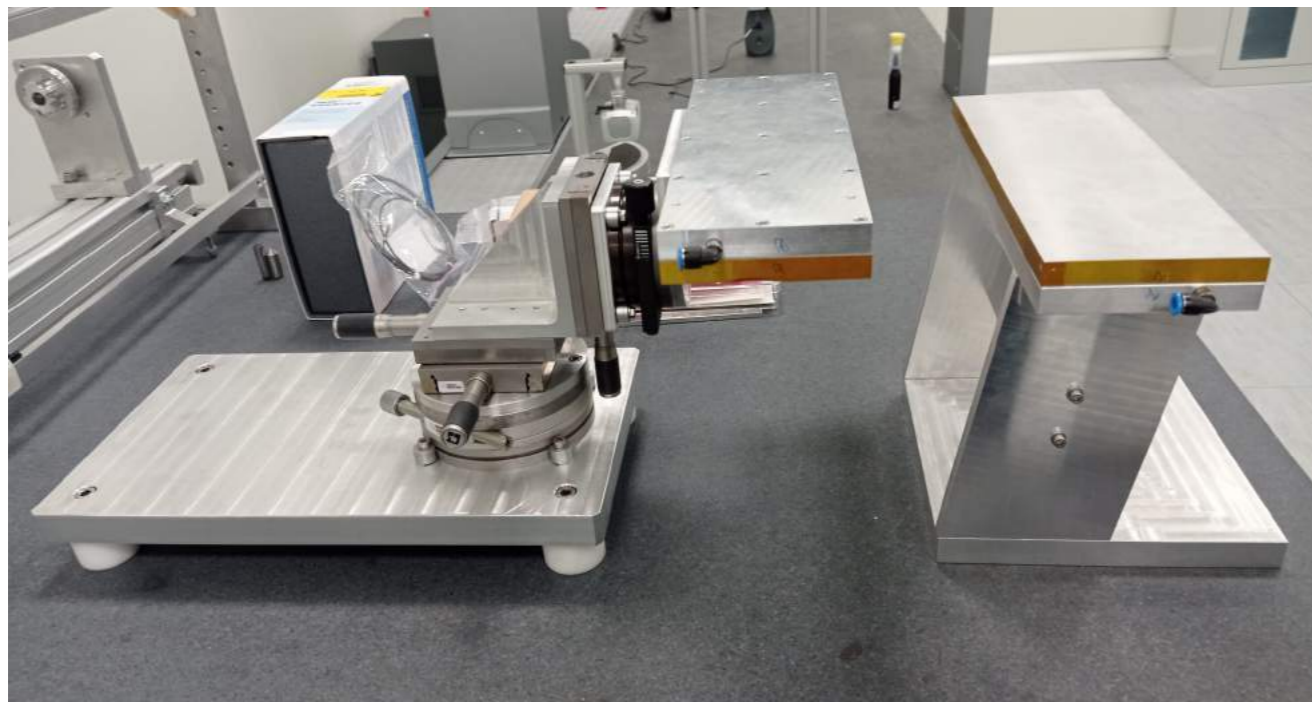
ALICE ITS3 Bending test



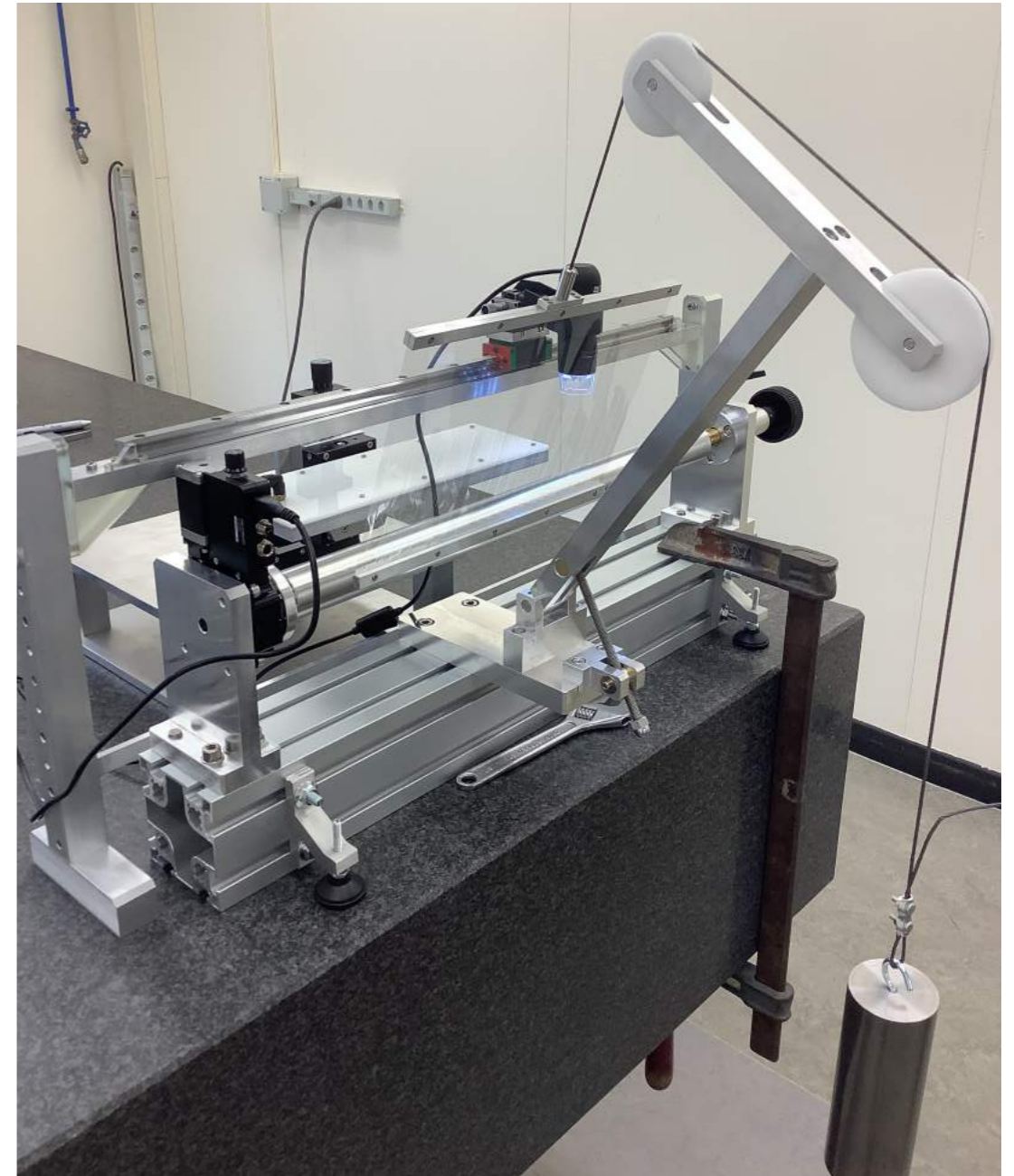


## WAFER-SCALE CHIPS BENDING

- » Developed procedure allows silicon bending in a repeatable reliable way
- » Bending tool: tensioned mylar foil wrapping around a cylindrical mandrel



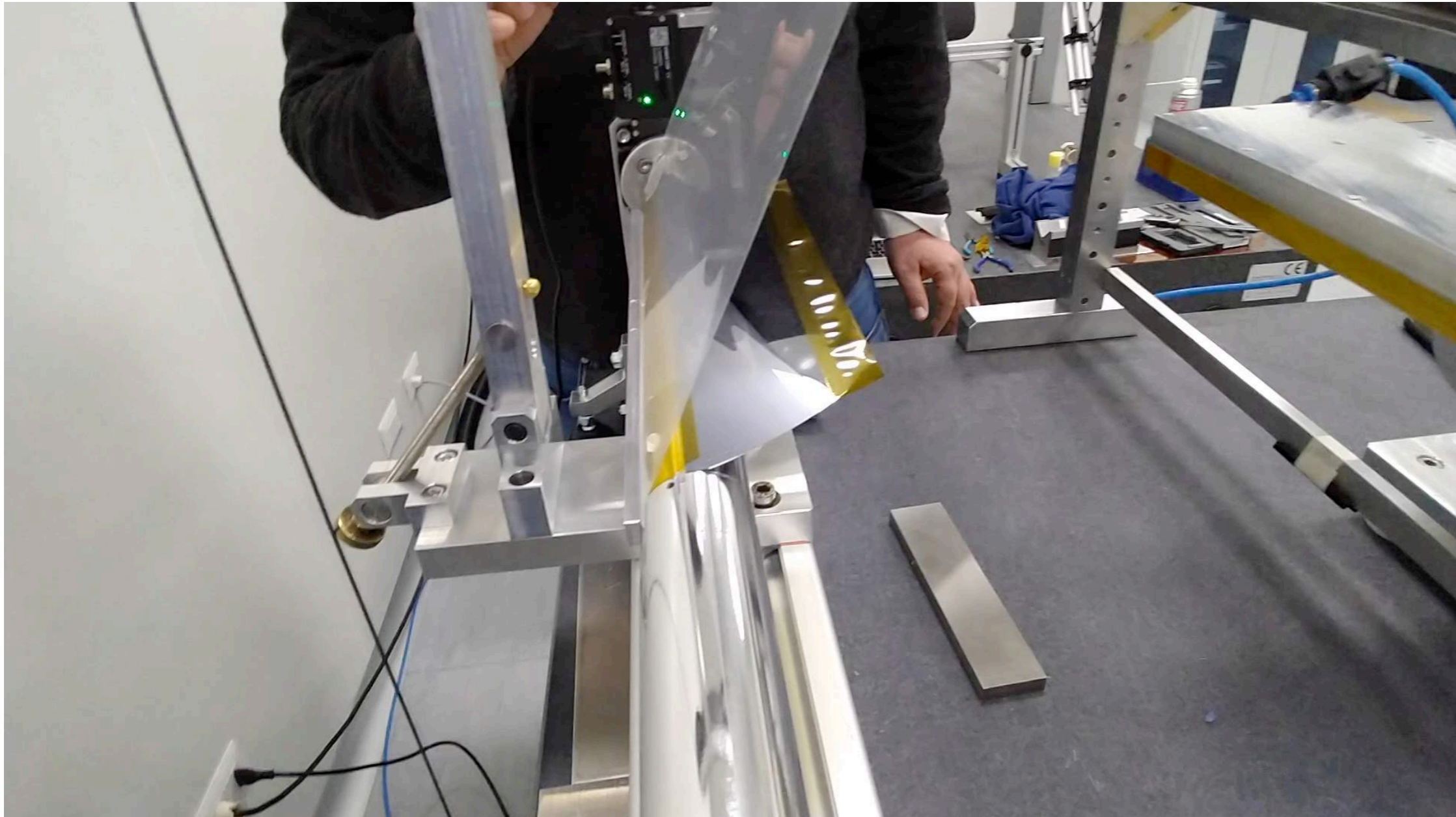
**Vacuum tools to handle and align large-size silicon piece**



**Bending tool equipped with: cylindrical mandrel, rotary motor, arm with weight to tension mylar foil, camera for alignment**

# ITS3 R&D lines - Detector Integration

## WAFER-SCALE CHIPS BENDING



**Bent silicon piece kept in position with Kapton adhesive tape**



# ITS3 R&D lines - Detector Integration

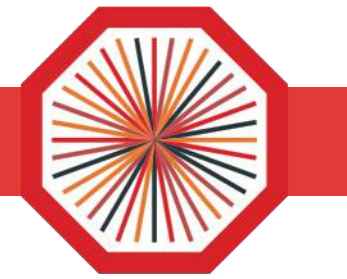
## WAFER-SCALE CHIPS BENDING



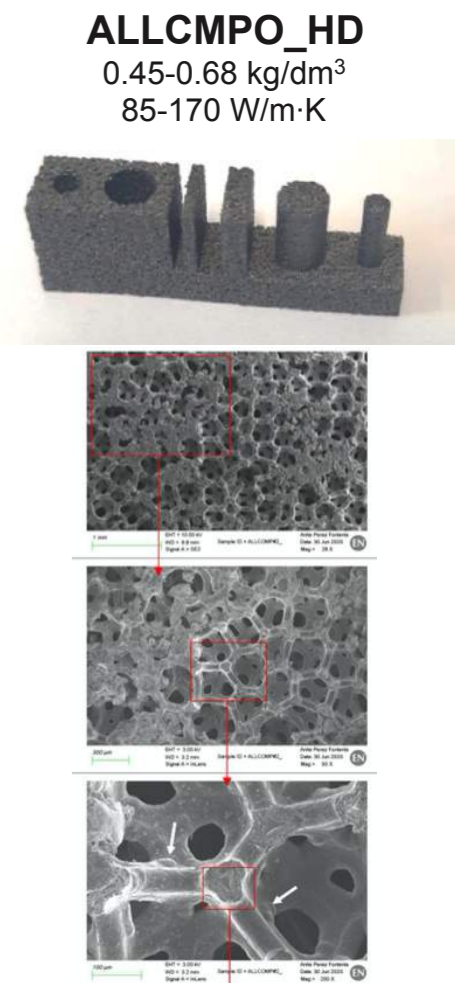
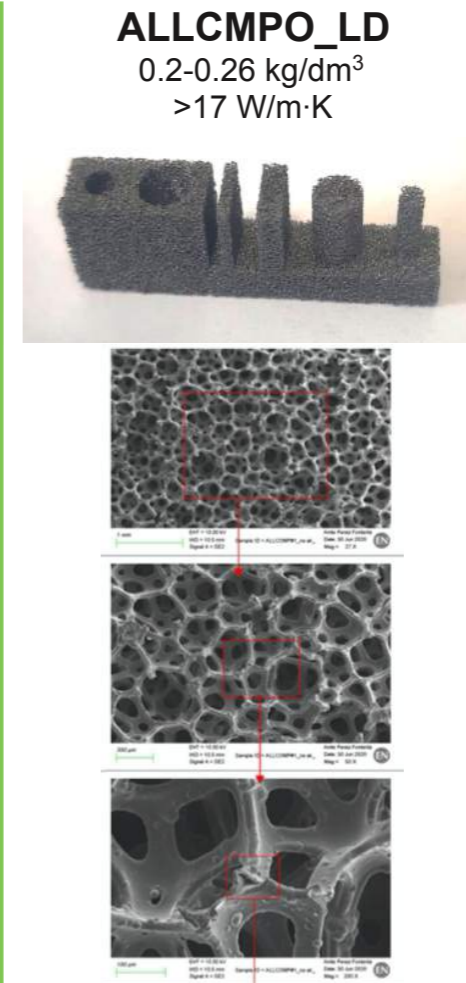
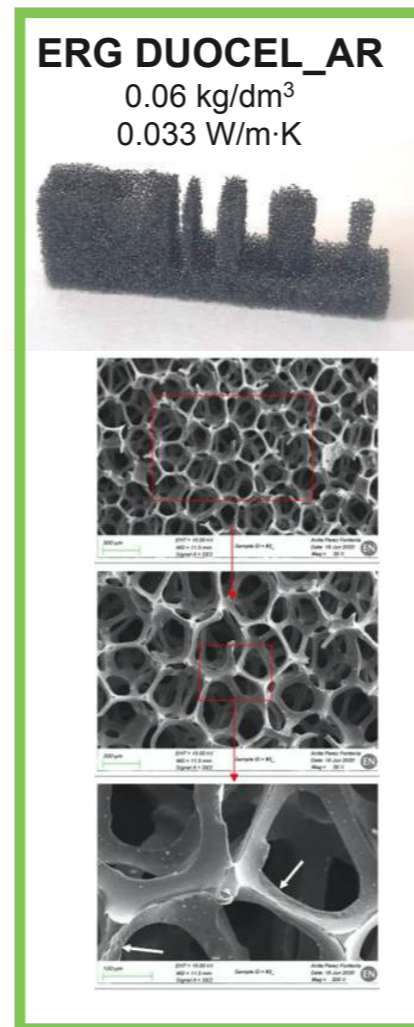
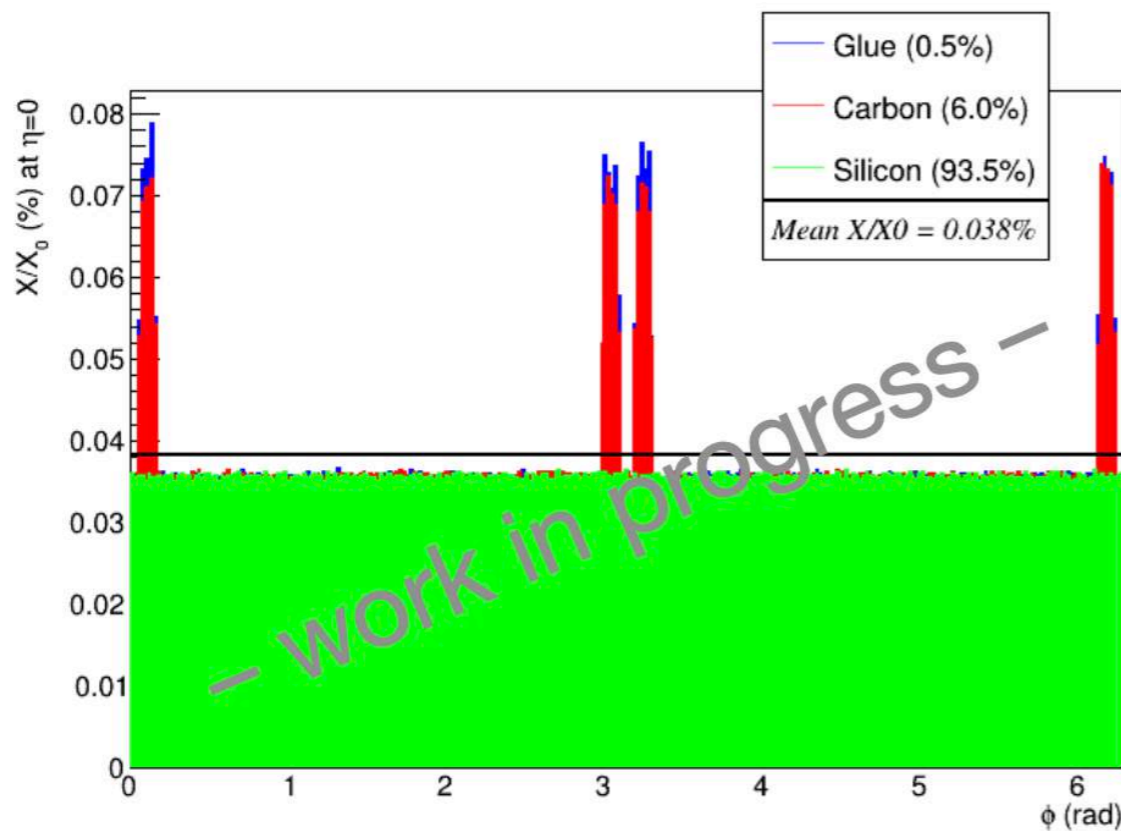
**Bent silicon piece kept in position with Kapton adhesive tape**

# ITS3 R&D lines - Detector Integration

## CARBON FOAM SUPPORT STRUCTURE

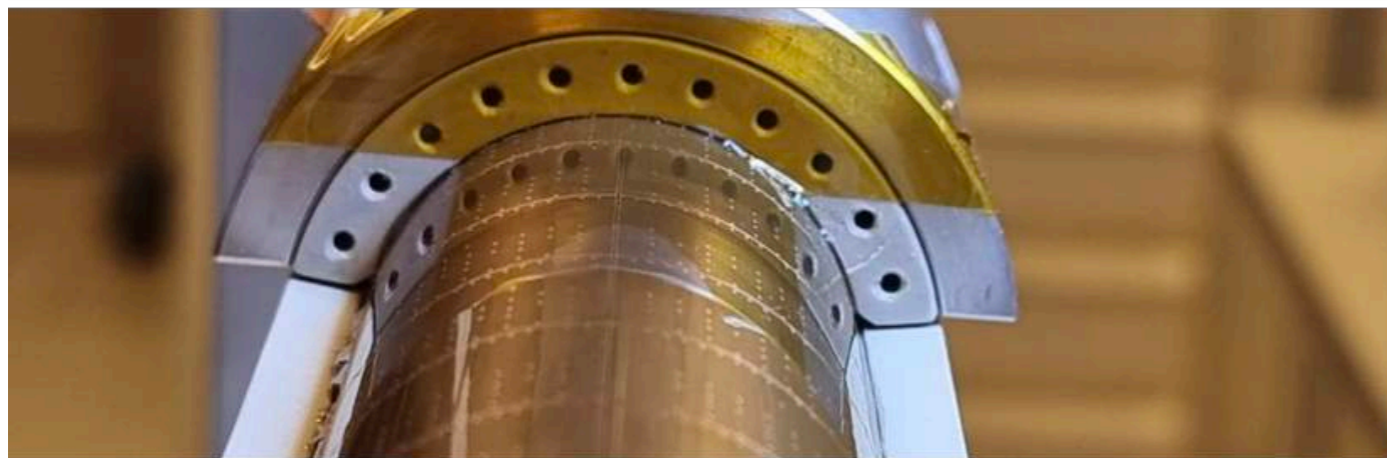
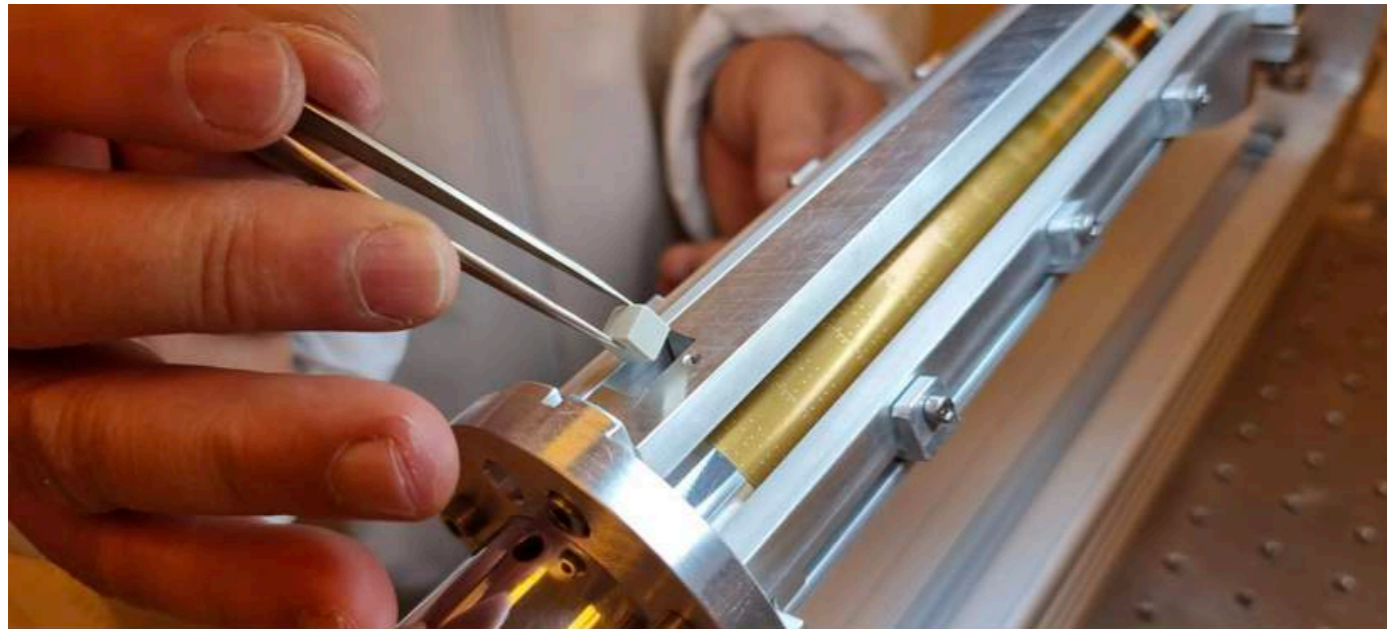


- » Different foams characterised for machinability and thermal properties
- » Baseline is ERG DUOCEL\_AR, which also features the largest radiation length

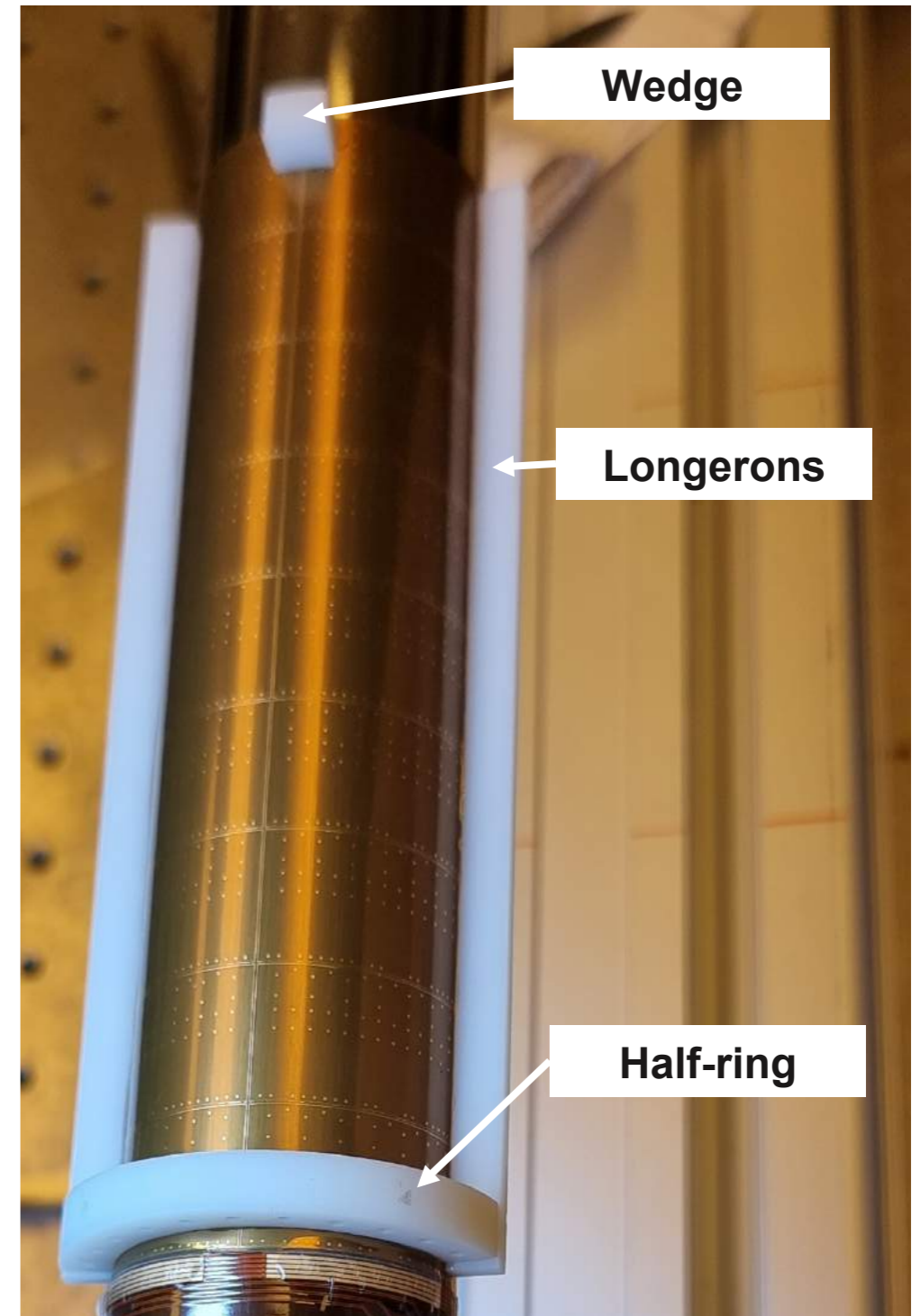


# ITS3 R&D lines - Detector Integration

## PEEK SUPPORT STRUCTURE

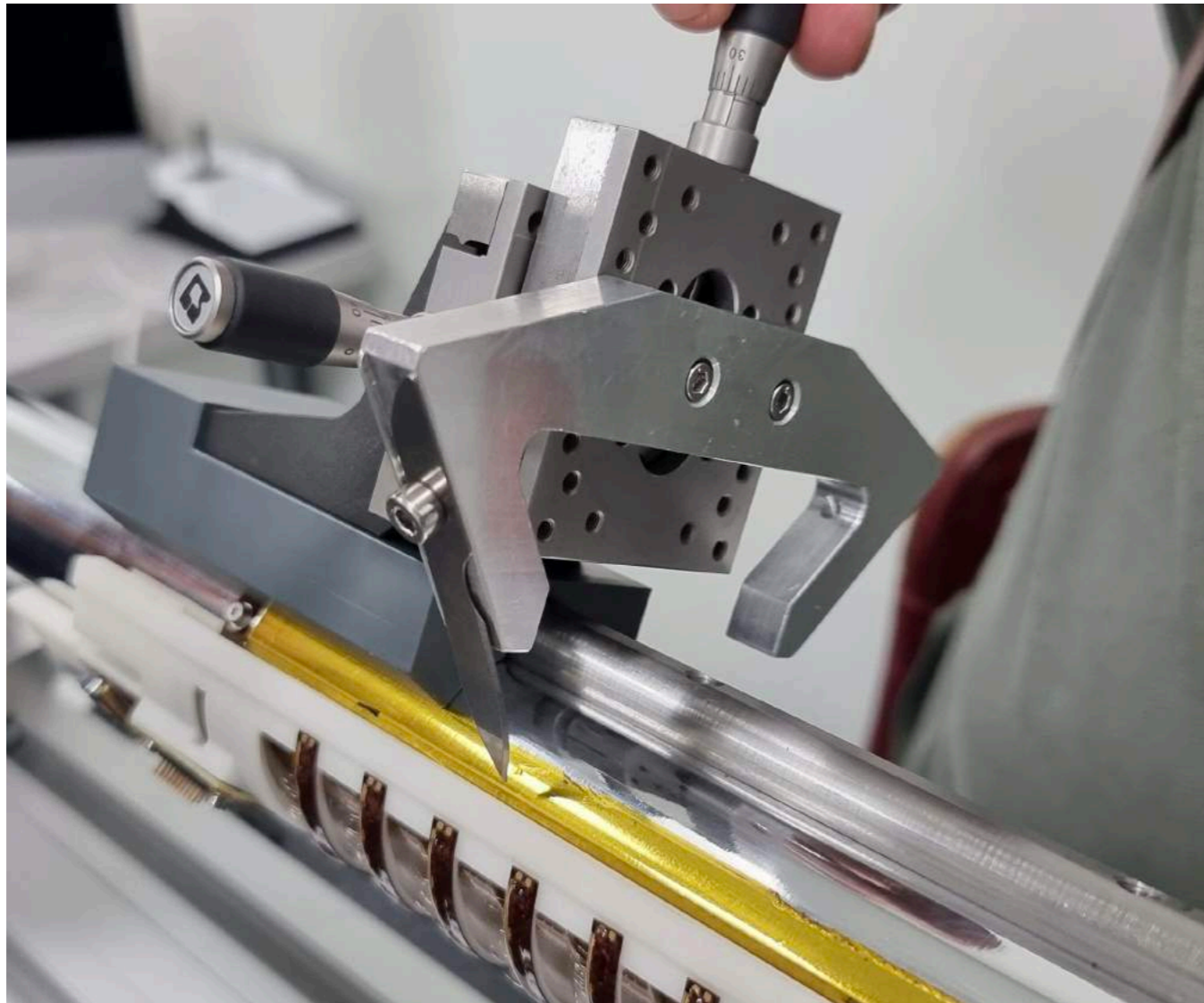


Alignment/gluing tools for support structures

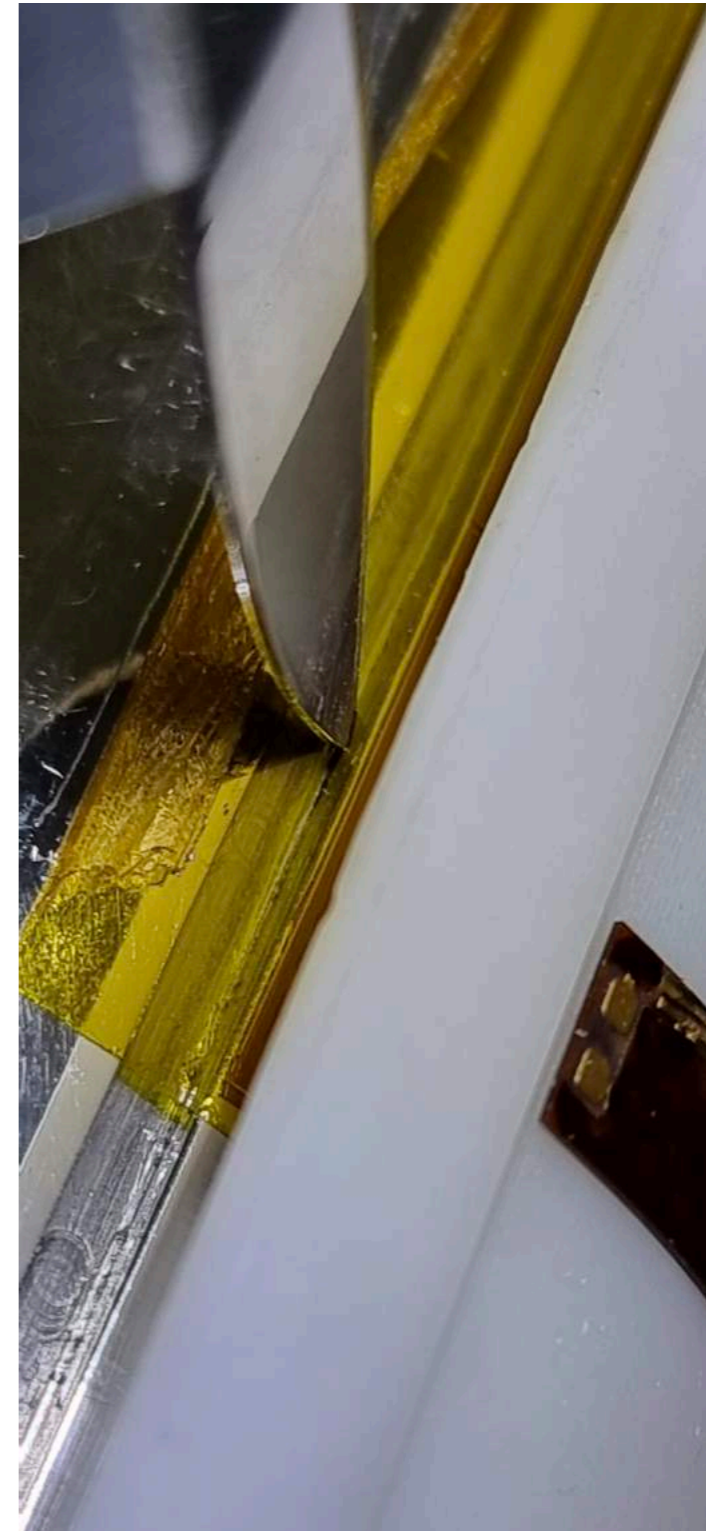


# ITS3 R&D lines - Detector Integration

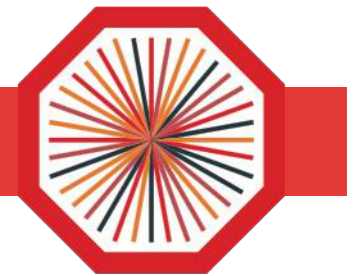
## ADHESIVE TAPE CUT TOOL



**Precision and controlled cut close to the edge of the silicon**

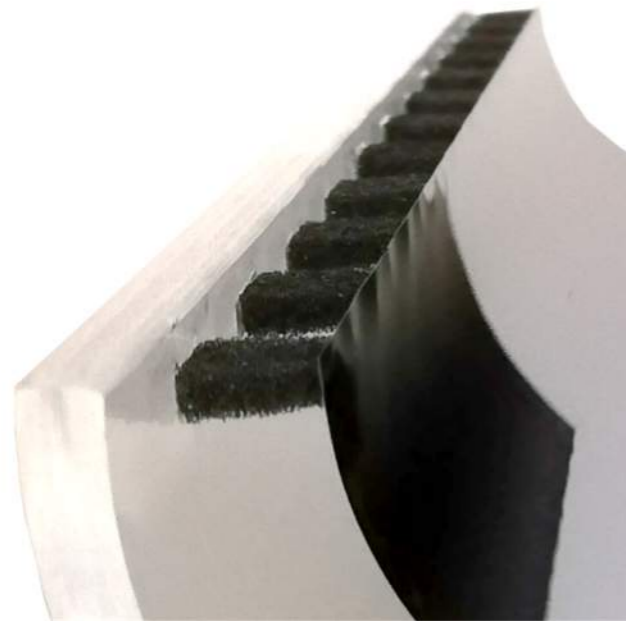
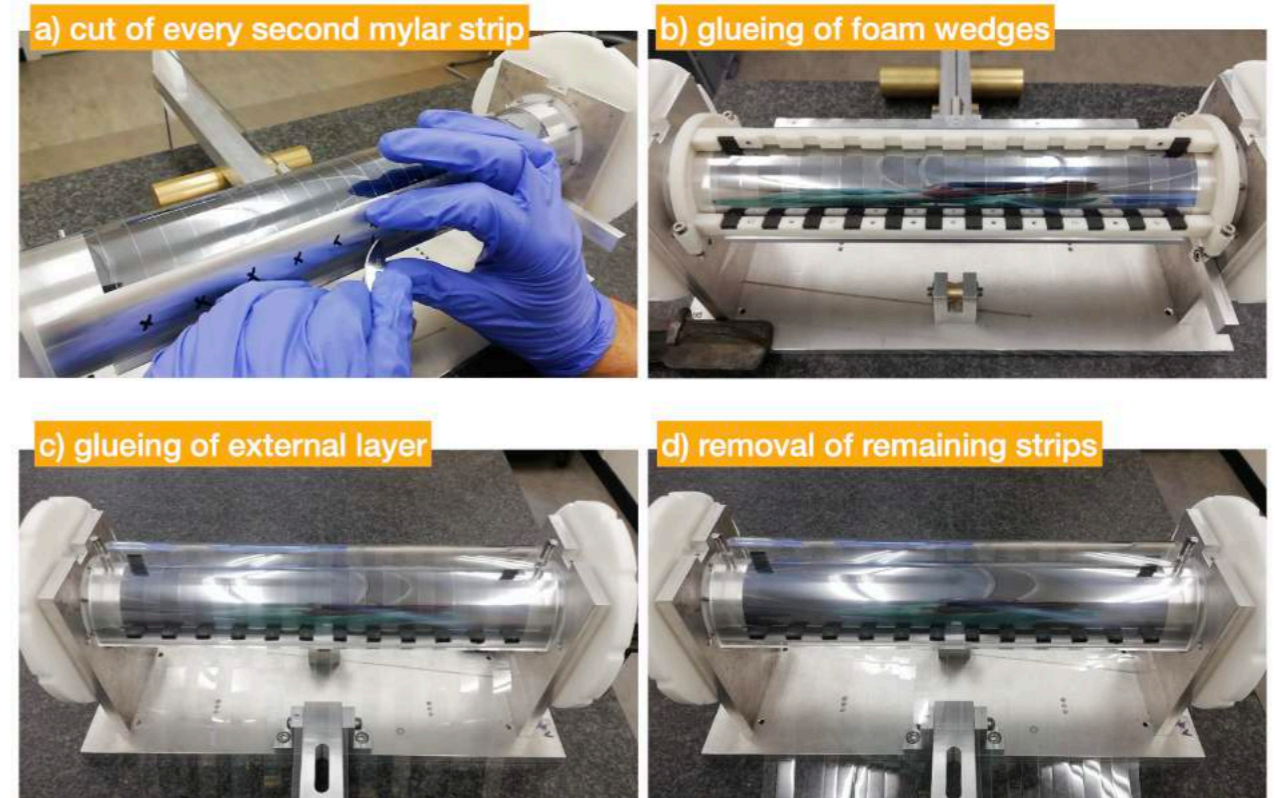


# ITS3 R&D lines - Detector Integration



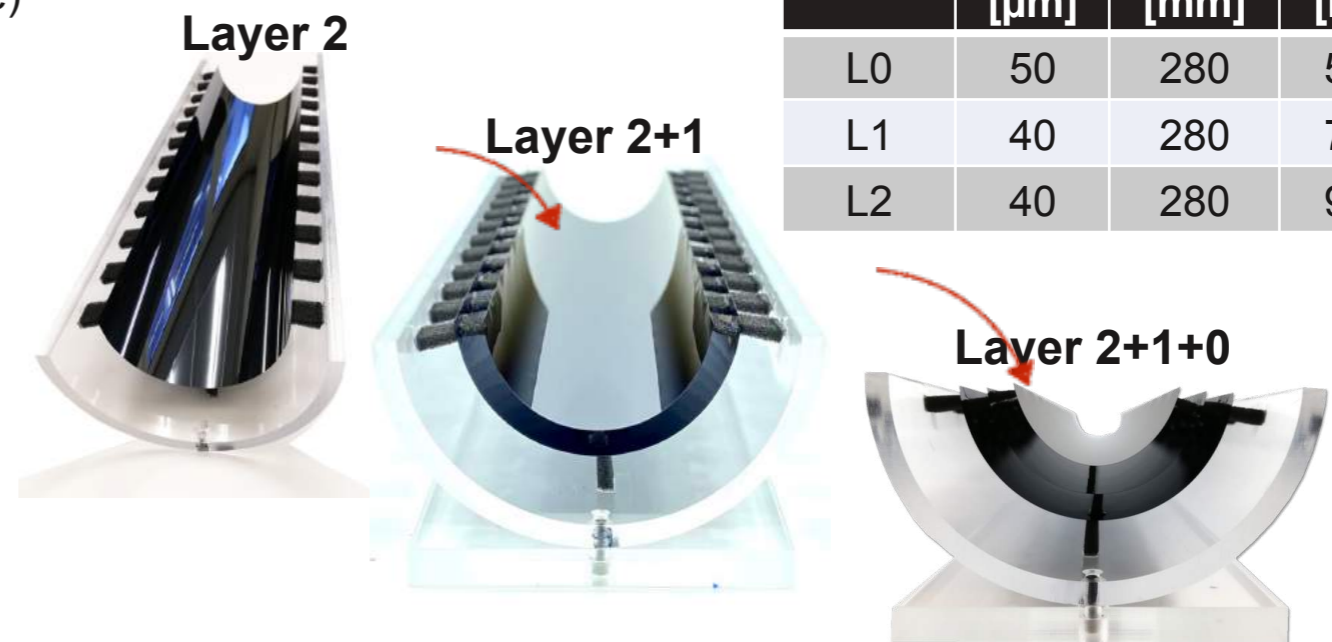
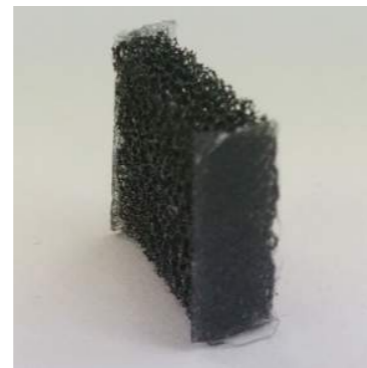
## LAYER ASSEMBLY PROCEDURE

- » Different options under study (including vacuum clamping)
- » Currently working solution based on segmented mylar foil



footprint effect  
+ bending between wedges

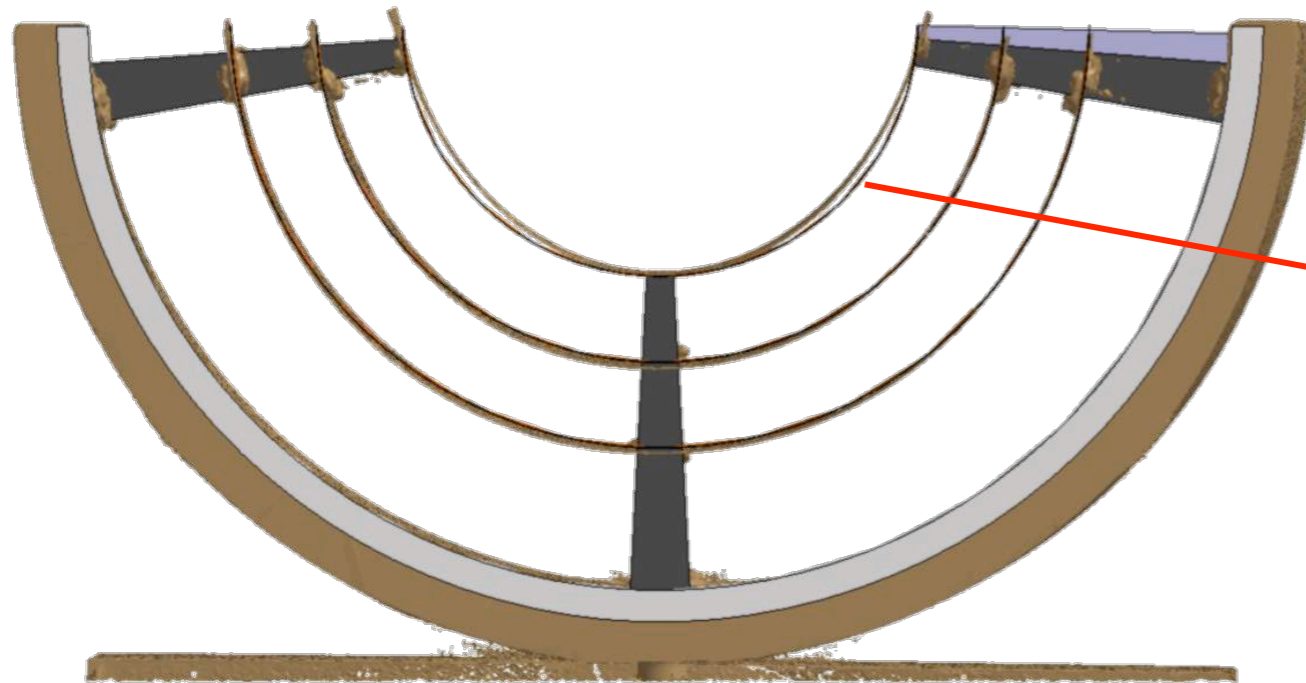
Carbon foam wedges  
+ fleece (to reduce glue)



Items	Th. [μm]	L [mm]	Circ. [mm]
L0	50	280	56.5
L1	40	280	74.4
L2	40	280	93.2



Engineering model1 → **geometrical accuracy-global**

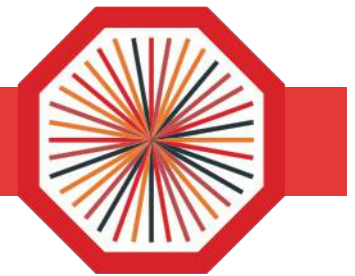


~0.7 mm largest out of tolerance on L0

Tomography VS CAD model

**Non-cylindricity** →

Optimize wedge geometry



## From Engineering model1 → Engineering model2

Fleece impregnated with glue allows for glue control

Carbon Veil (fleece)

Mould for Surface finishing

Minimized glue penetration in the foam wedge

Supporting base

Interface

Silicon

ERG Duocel

Fleece

Glue

Silicon

13 mm

1858%

Improve carbon foam glued interface

Duocel ERG Foam

replace wedges with continuous longeron

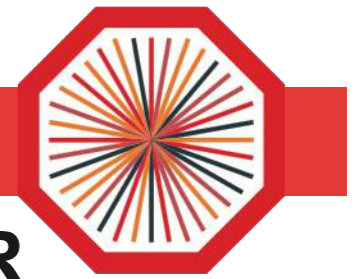
ALLCOMP LD Foam

Optimize wedge geometry

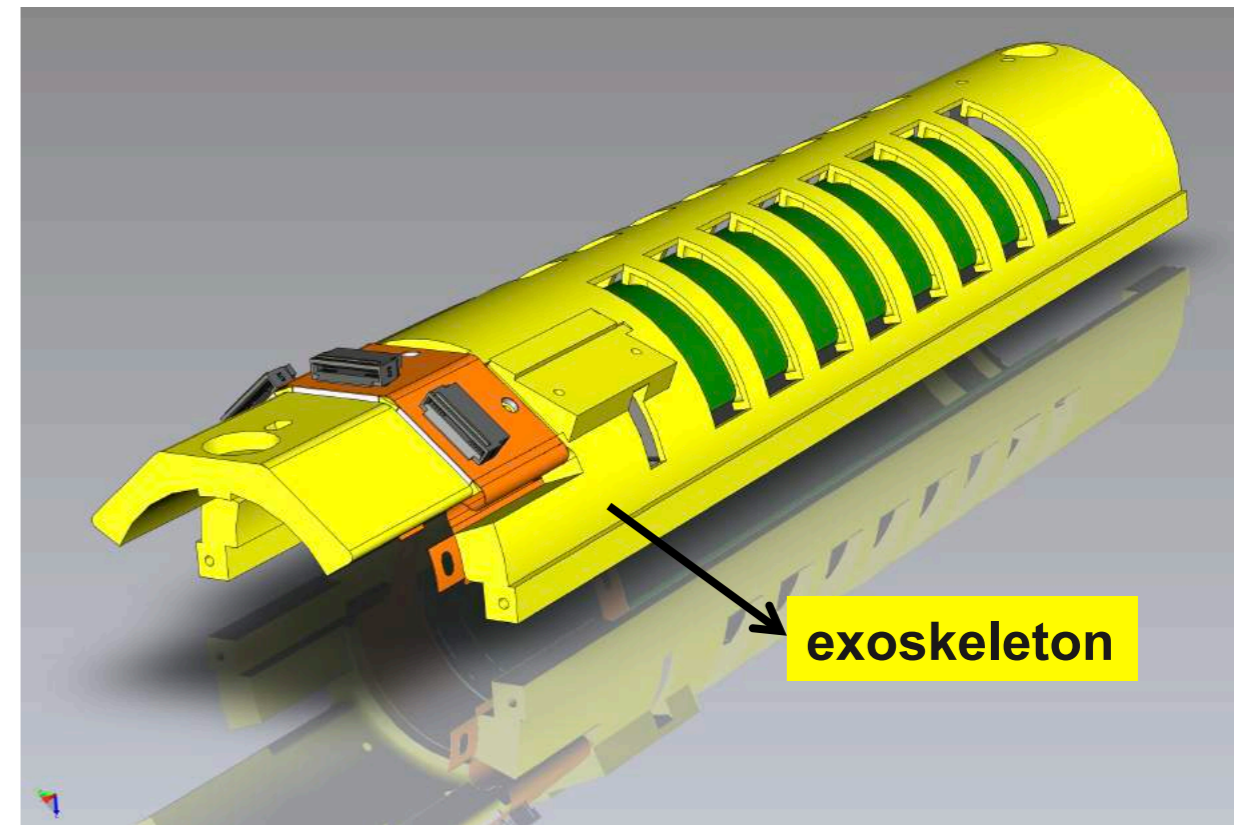
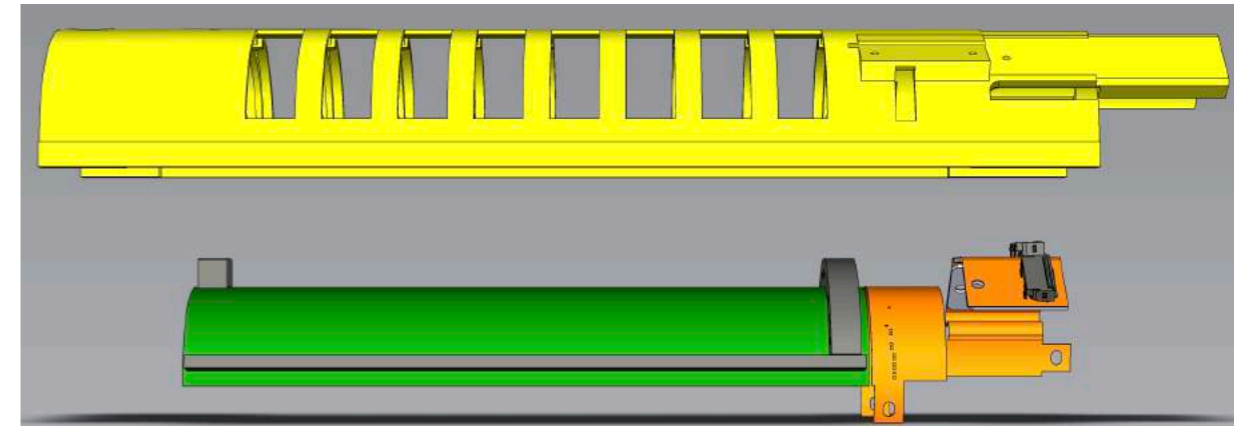
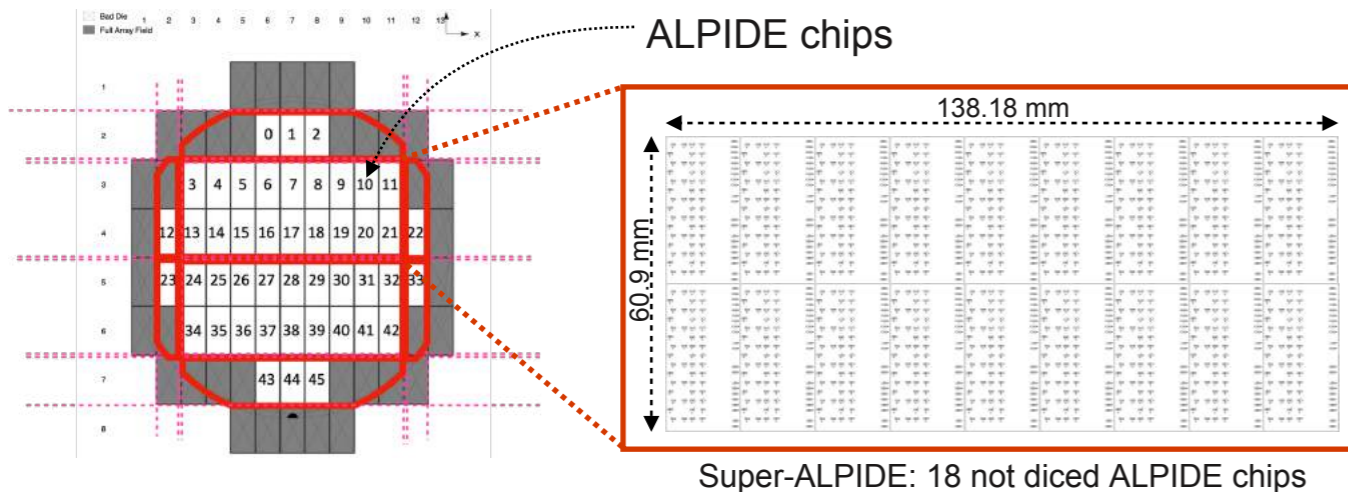
Foam ring radiators for peripheral cooling

From Work Package 5\_C.Gargiulo

# ITS3 R&D lines - Super ALPIDE-Layer interconnection



## TOWARD FIRST WORKING LARGE DIMENSION SENSOR



First prototype of bent flex

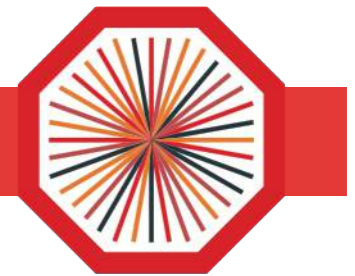
### » Super-ALPIDE

- 18 not diced ALPIDE chips
- dimensions close to the ones for L0 sensor

### » Goals

- verify bending tools for large-size working chips
- verify mechanical support alignment tools
- develop wire-bonding over bent surface tools
- develop first bent flex prototype (for powering and data streaming)
- assemble first working large dimension bent sensor





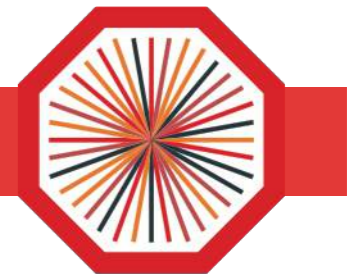
## Wire-bonding is the baseline technique for ITS3 large-area sensors

- Power/data connections on A-side (power on C-side under discussion)
- Expected sensor pad density: down to  $60\mu\text{m}$ -pitch

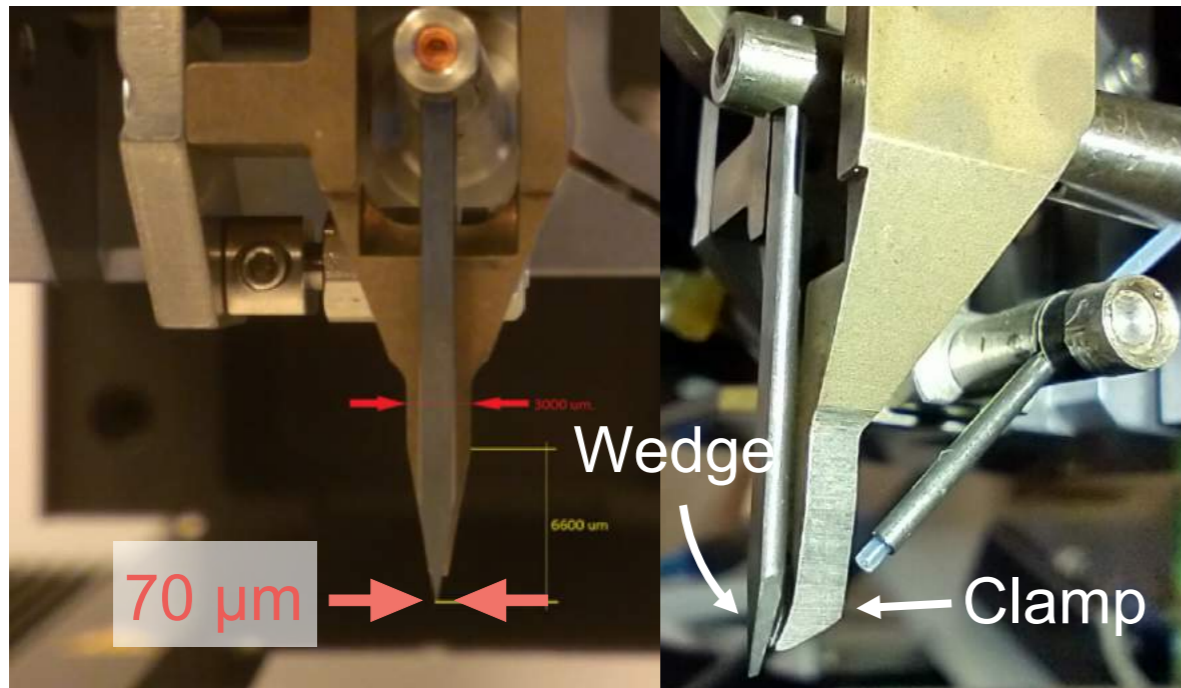
## Wire-bonding systematic studies:

- Pull test force Vs (loop height, loop length)
- Different pad size and arrangements
- Different bonding heads/systems
- On flat and bent surface

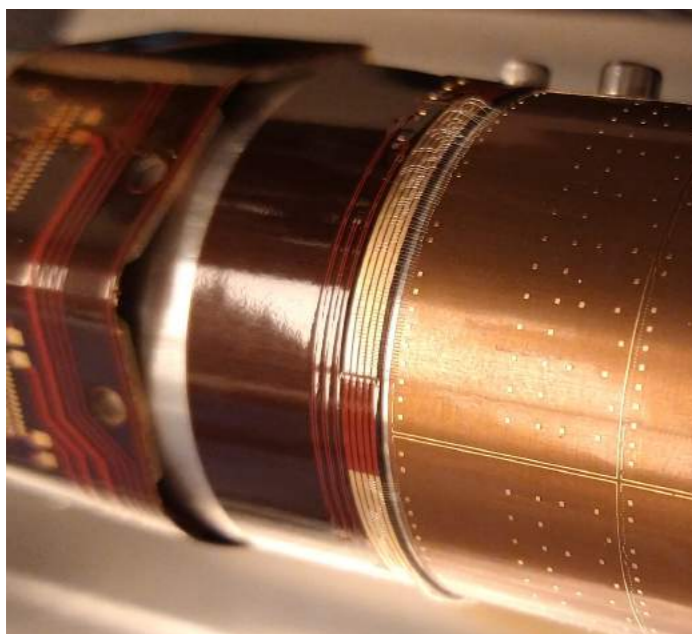




## Tools in Bari and already performed exercise

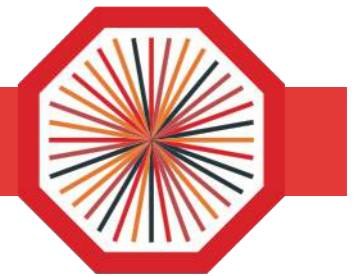


- The limit to the bonding density (distance between two adjacent bonding feet) is given by the width of the wedge and clamp.
- Usual wedge thickness 100  $\mu\text{m}$  → In Bari 70  $\mu\text{m}$  (modified for the ITS2 assembly)
- Wire thickness: 25  $\mu\text{m}$



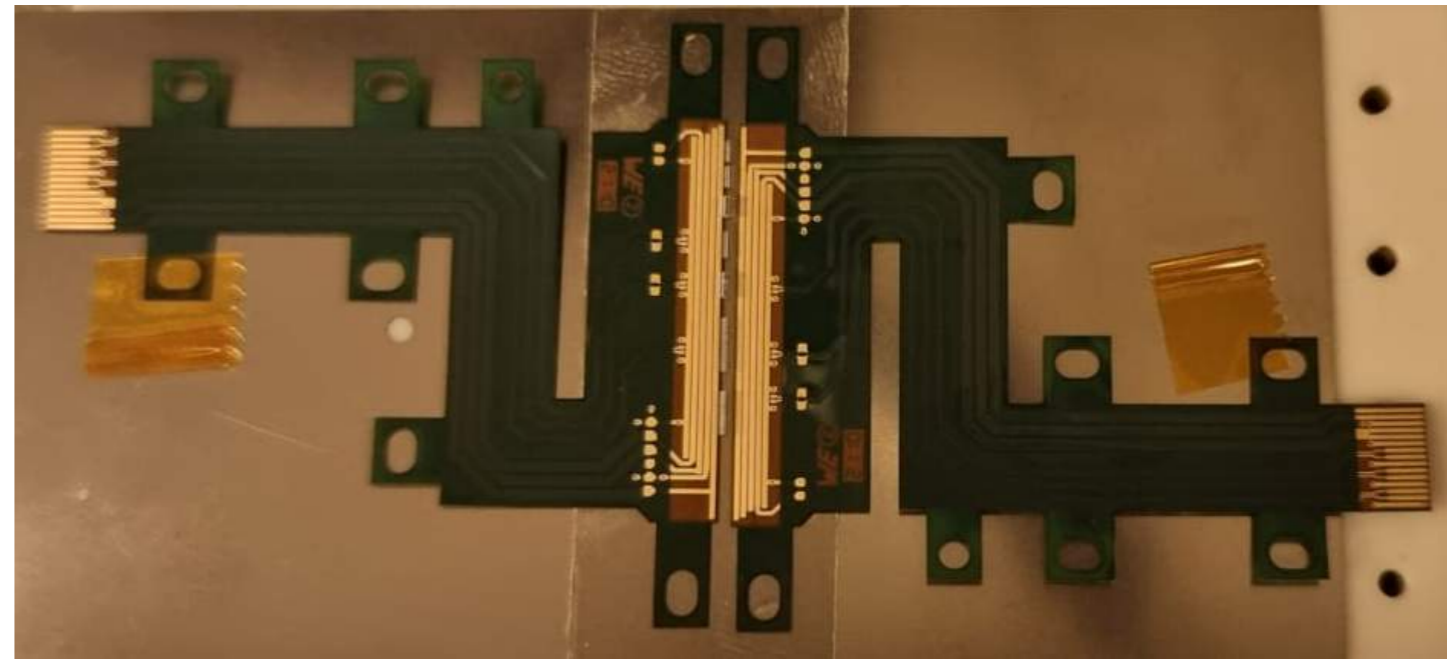
### SUPER-ALPIDE edge-FPC

- Wires length spans between ~1 mm and ~5 mm
  - Distance between ALPIDE mini-pads and FPC border: ~1 mm
  - Distances between FPC border and long pads: ~30  $\mu\text{m}$  - 4 mm
- ALPIDE mini-pads dimensions: ~90  $\mu\text{m}$  x 90  $\mu\text{m}$
- Inter-pad distance, in the denser regions: ~220  $\mu\text{m}$

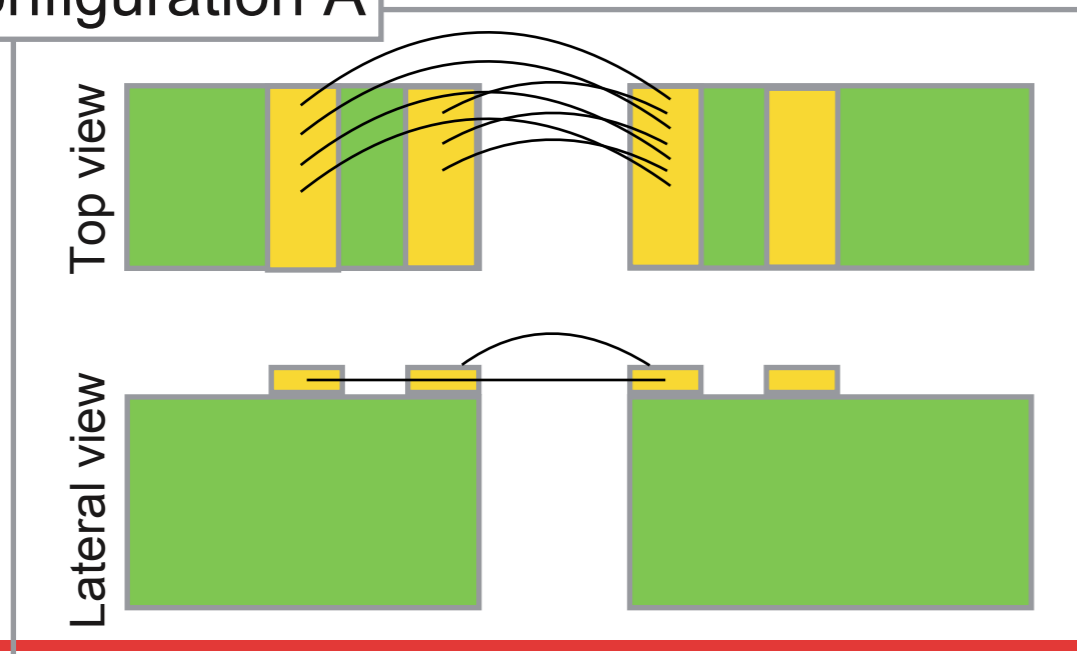


## wire-bonding - 100 $\mu\text{m}$

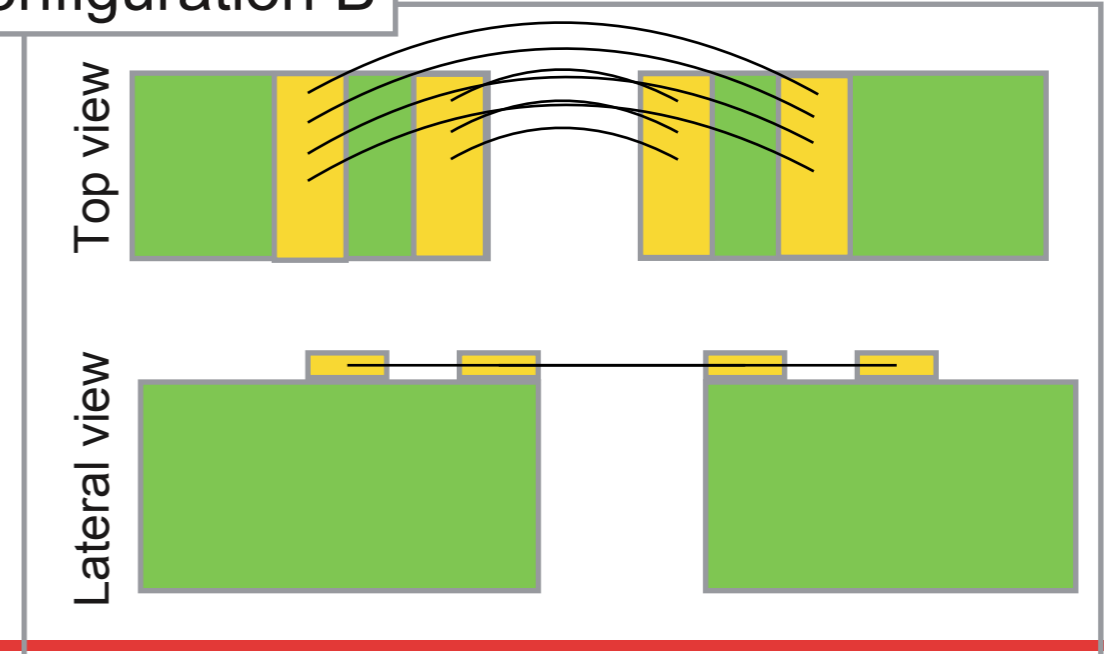
- Setup: two single-ALPIDE FPC one facing the other, with a gap similar of  $\sim 500 \mu\text{m}$
- Two bonding configurations:
  - A) “Deferred” : for two adjacent wires, one foot is on the same long-pad while the other is on a different long-pad
  - B) “Alternating” : for two adjacent wires, both feet are on two different long-pads
- Two inter-pad distances explored:
  - $\sim 100 \mu\text{m}$
  - $\sim 80 \mu\text{m}$



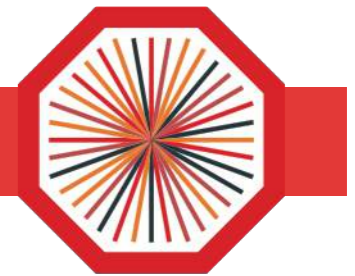
Configuration A



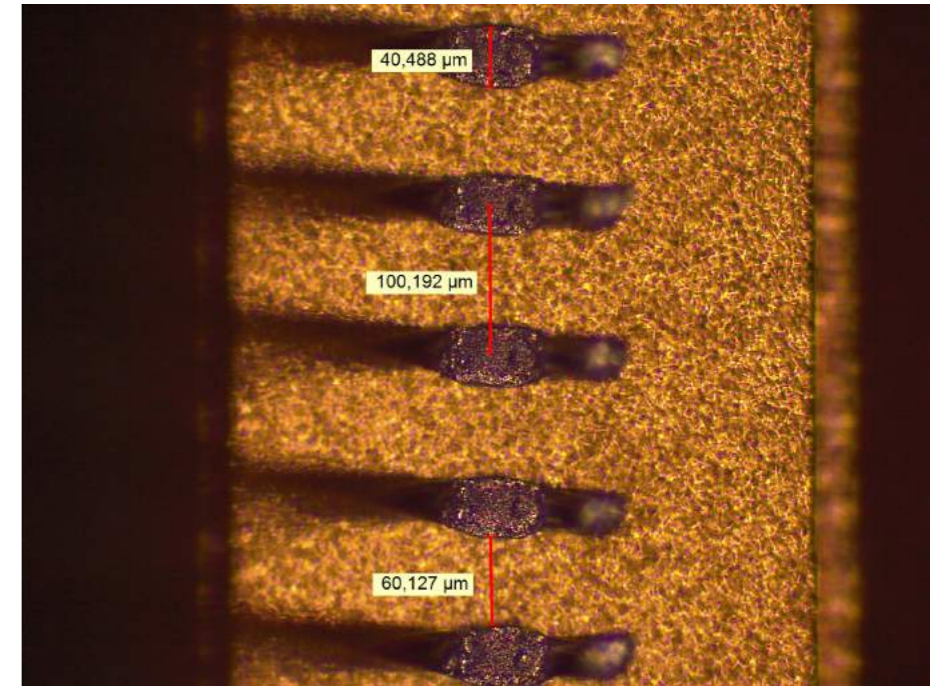
Configuration B



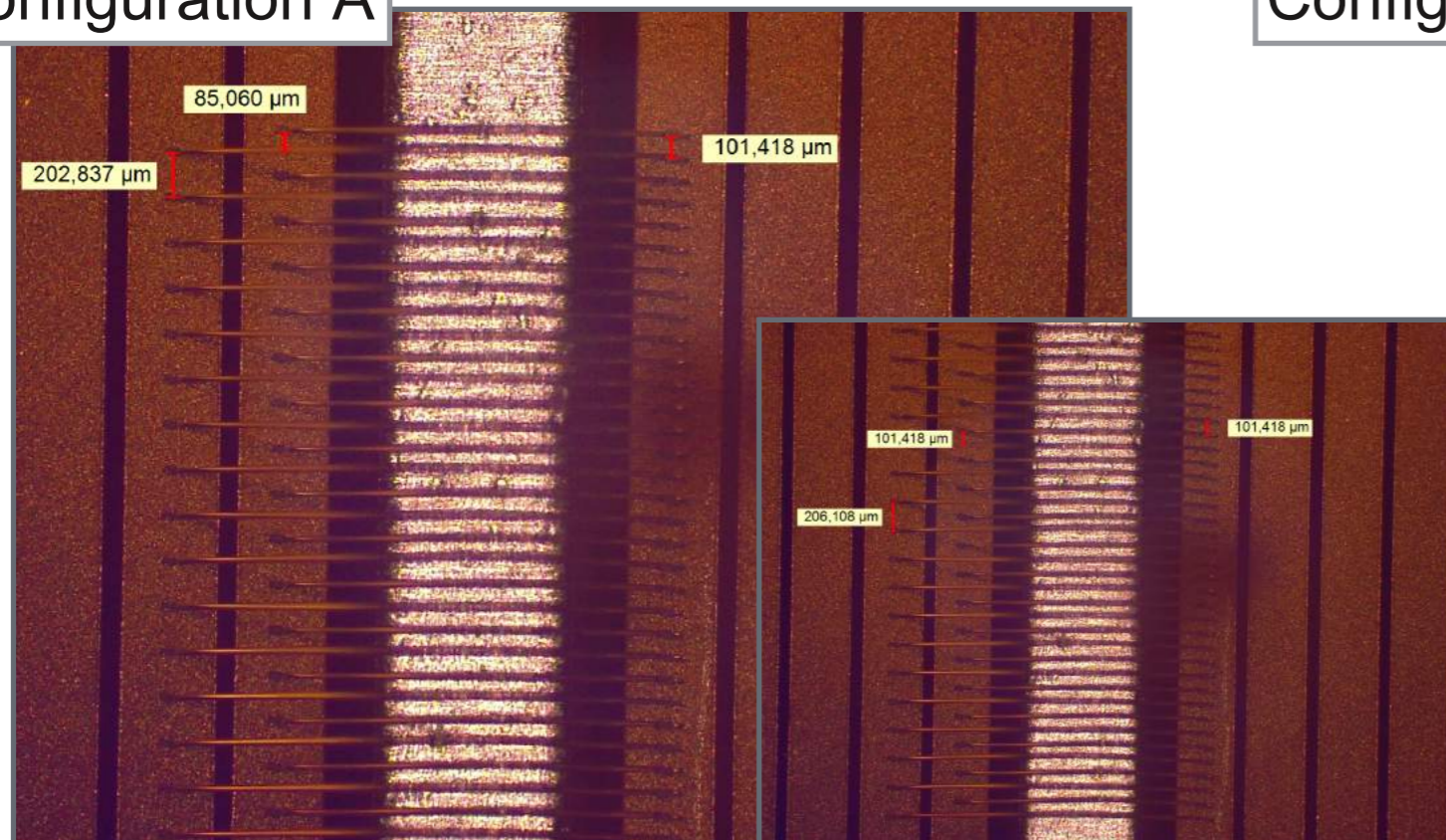
# ITS3 R&D lines - Super ALPIDE-Layer interconnection



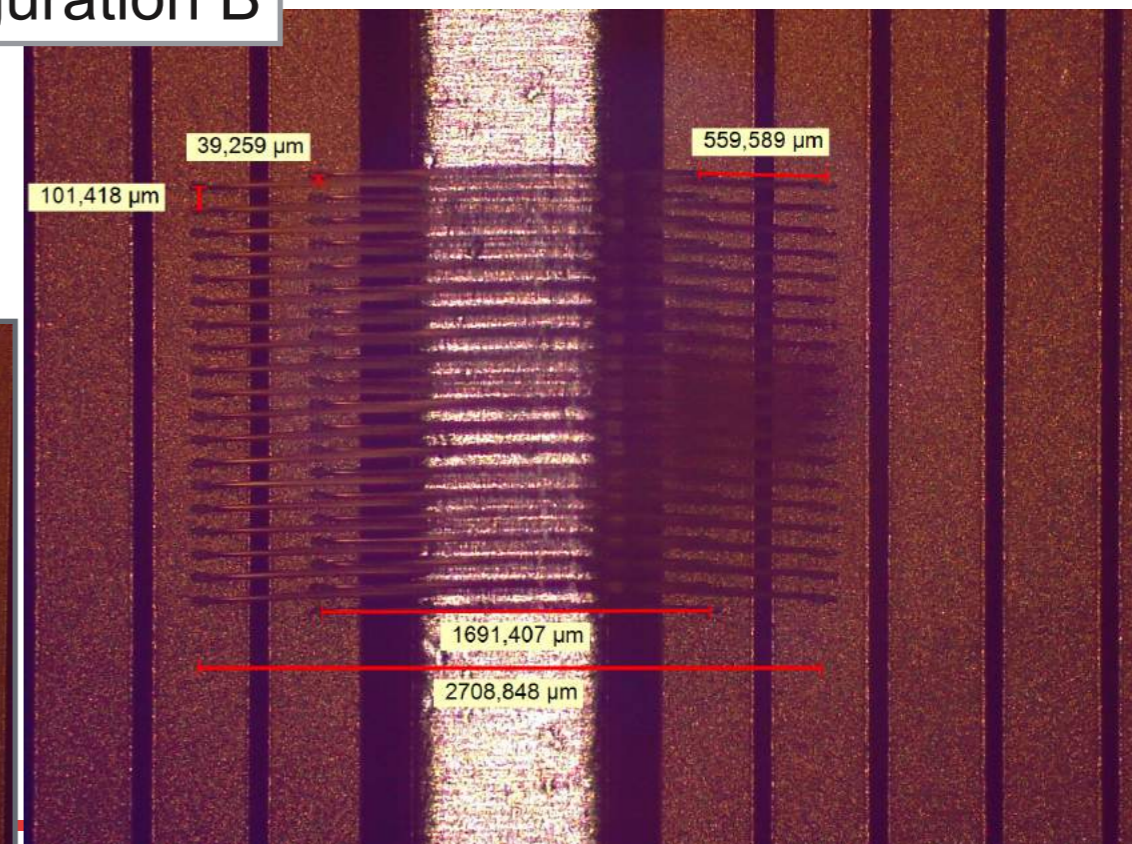
- With a wire thickness of  $25\ \mu\text{m}$ , a foot width of  $\sim 40\ \mu\text{m}$  is expected  $\rightarrow$  minimal pad dimension  $\sim 80\ \mu\text{m}$
- $\sim 100\ \mu\text{m}$  inter-pad distance can be easily achieved
- Configuration B allow to double the bonding density, but requires staggered pads



Configuration A



Configuration B



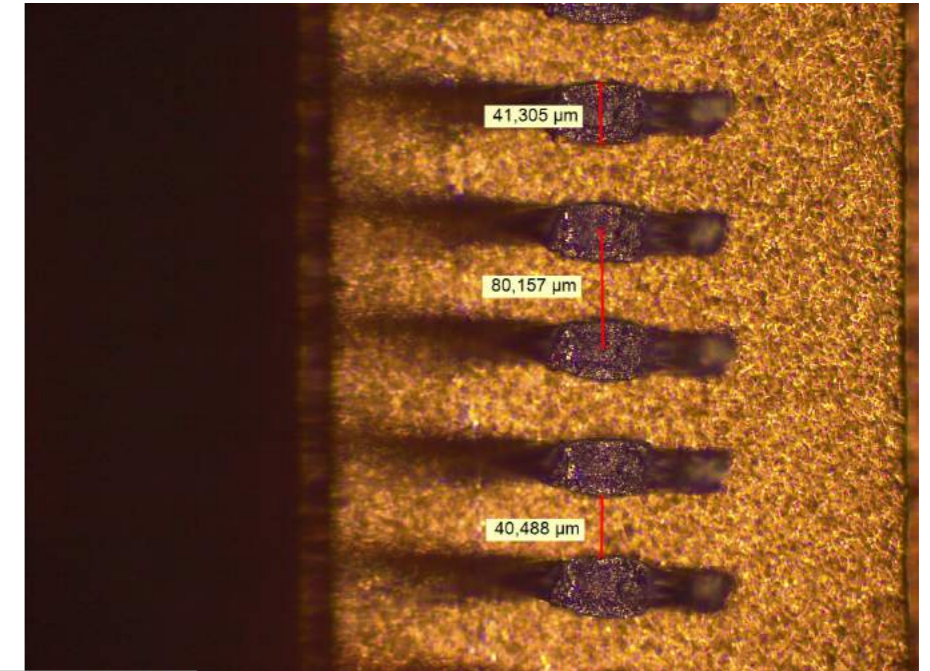
# ITS3 R&D lines - Super ALPIDE-Layer interconnection



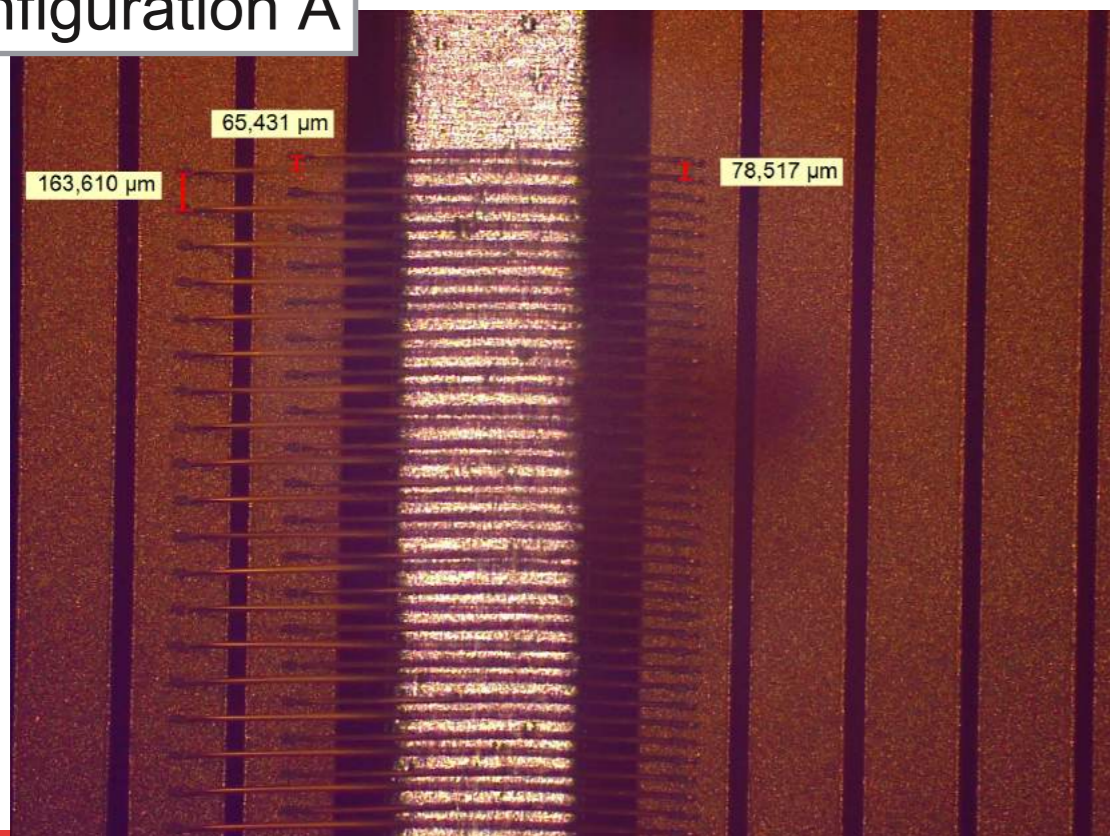
## TOWARD FIRST WORKING LARGE DIMENSION SENSOR

### wire-bonding - 80 $\mu\text{m}$

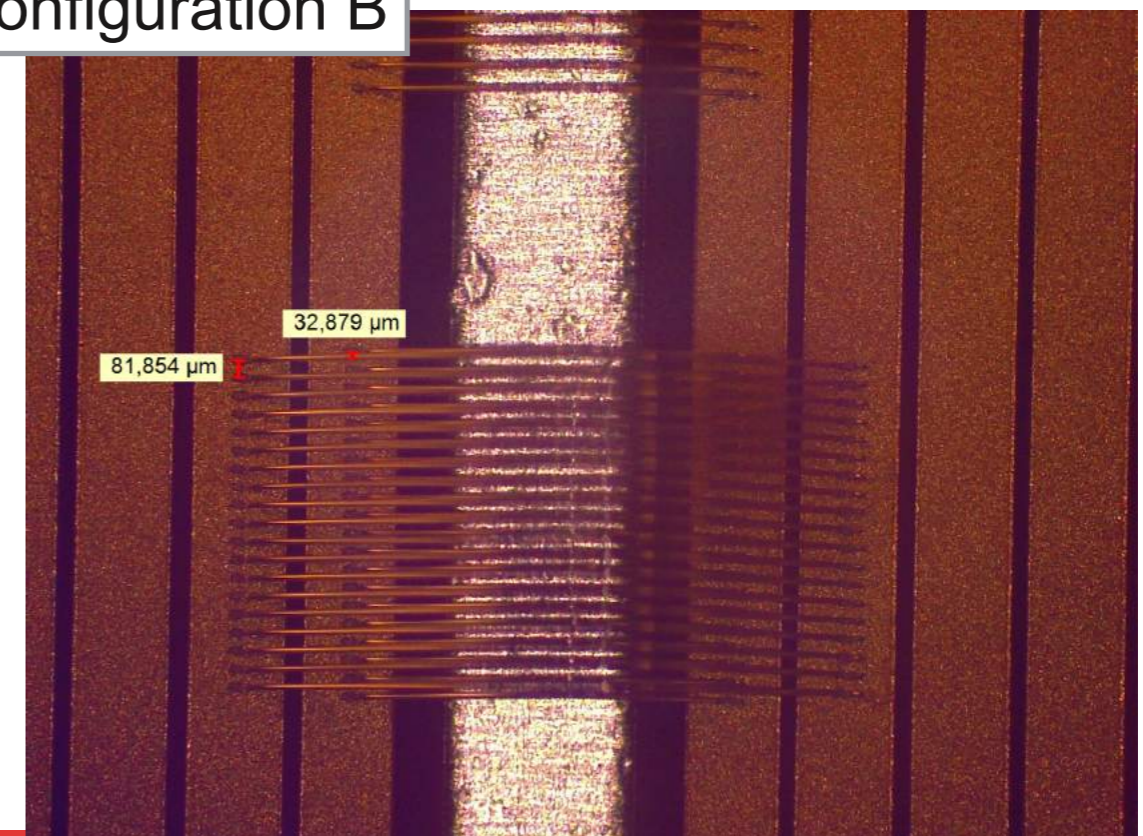
- With a wire thickness of 25  $\mu\text{m}$ , a foot width of  $\sim 40 \mu\text{m}$  is expected  $\rightarrow$  minimal pad dimension  $\sim 80 \mu\text{m}$
- $\sim 80 \mu\text{m}$  inter-pad distance achievable
- Configuration B bring to wire distance of  $\sim 40 \mu\text{m}$



Configuration A



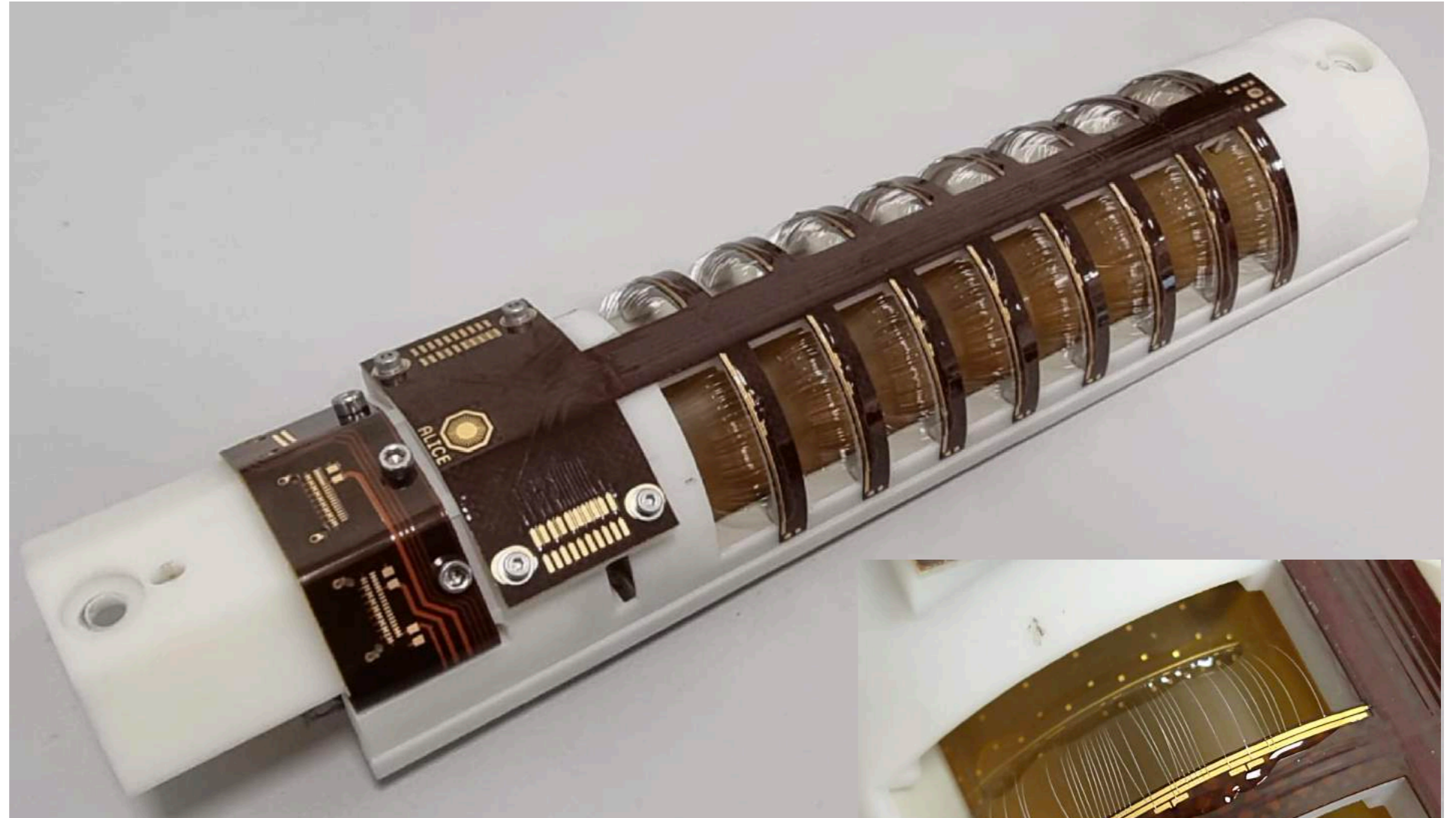
Configuration B





# ITS3 R&D lines - Super ALPIDE

## TOWARD FIRST WORKING LARGE DIMENSION SENSOR



**Wire-bonding through exoskeleton**



- » ALICE proposes to build the next-generation inner tracking detector, based on **300 mm wafer-scale, 20-40  $\mu\text{m}$  thin, bent MAPS**
- » **R&D** is making rapid progress on all fronts
  - successful **in-beam verification of bent MAPS**
  - study systematically wire-bonding interconnections
  - **full-size mechanical mockups**
  - **first large-size working sensor detector under assembly**