# **R.** Luz<sup>1</sup> on behalf of ATLAS TDAQ Collaboration

<sup>1</sup>Argonne National Laboratory, 9700 S Cass Ave, Lemont, IL 60439, USA

## Motivation

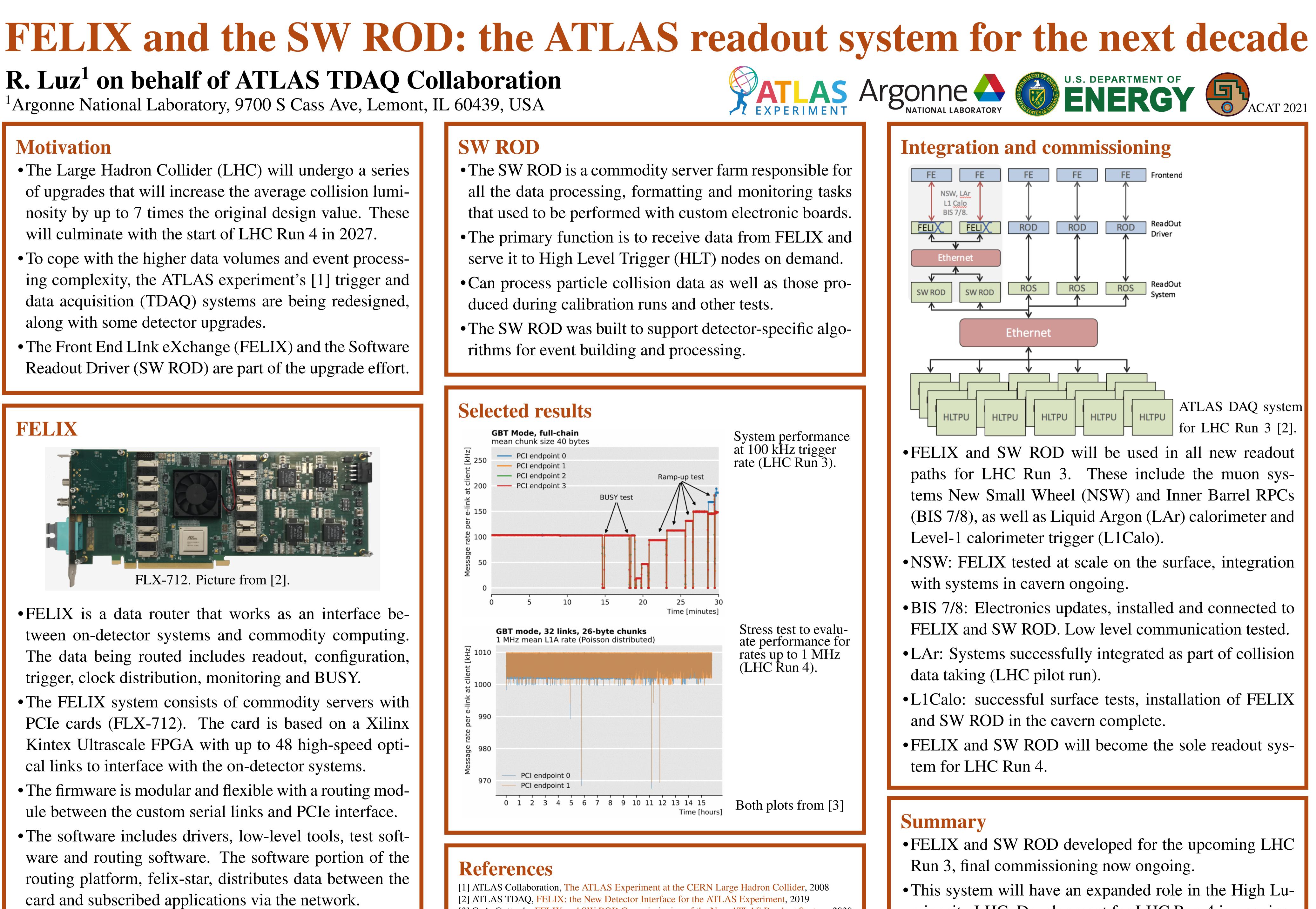
- The Large Hadron Collider (LHC) will undergo a series of upgrades that will increase the average collision luminosity by up to 7 times the original design value. These will culminate with the start of LHC Run 4 in 2027.
- To cope with the higher data volumes and event processing complexity, the ATLAS experiment's [1] trigger and data acquisition (TDAQ) systems are being redesigned, along with some detector upgrades.
- The Front End LInk eXchange (FELIX) and the Software Readout Driver (SW ROD) are part of the upgrade effort.

### FELIX



FLX-712. Picture from [2].

- •FELIX is a data router that works as an interface between on-detector systems and commodity computing. The data being routed includes readout, configuration, trigger, clock distribution, monitoring and BUSY.
- The FELIX system consists of commodity servers with PCIe cards (FLX-712). The card is based on a Xilinx Kintex Ultrascale FPGA with up to 48 high-speed optical links to interface with the on-detector systems.
- The firmware is modular and flexible with a routing module between the custom serial links and PCIe interface.
- The software includes drivers, low-level tools, test software and routing software. The software portion of the routing platform, felix-star, distributes data between the card and subscribed applications via the network.



[3] C. A. Gottardo, FELIX and SW ROD Commissioning of the New ATLAS Readout System, 2020

minosity LHC. Development for LHC Run 4 is ongoing.