Contribution ID: **65** Type: **Poster**

Developments on the Tile Computer on Module (TileCoM) mezzanine board for the ATLAS Tile Calorimeter off-detector electronics

Thursday, 28 May 2020 10:00 (30 minutes)

The TileCoM mezzanine board is one of the auxiliary boards of the TilePreProcessor (TilePPr) for Phase-2 Upgrade of the readout electronics of ATLAS TileCal. The TileCoM si responsible for remotely configuring the on-detector electronics and TilePPr-FPGAs and to interface ATLAS DCS-data to TilePPr. The TilePPr is composed of several FPGA-based boards to operate and read-out the on-detector electronics. The deployment of an embedded Linux for the ZYNQ System-on-Chip targeting an Avnet Ultra96-V2 Zynq UltraScale+MPSoC evaluation board is presented. This test-bench will serve as a basis for main functionalities development of the TileCoM mezzanine board and remote programming of TileCal FPGA-devices.

Presenter: GOLOLO, Mpho Gift Doctor (University of the Witwatersrand (ZA))

Session Classification: POSTER