TIPP 2020

Monday, 25 May 2020

Readout: Front-end electronics: Block 1 (14:00 - 15:30)

time	[id] title	presenter
14:00	[41] Electronics and Triggering Challenges for the CMS High-Granularity Calorimeter	
14:18	[59] Development of the ATLAS Liquid Argon Calorimeter Readout Electronics for the HL-LHC	
14:36	[91] The Phase-I Trigger Readout Electronics Upgrade of the ATLAS Liquid Argon Calorimeters	
14:54	[178] HGCROC-V2: the front-end readout ASIC for the CMS High Granularity Calorimete	Mr THIENPONT, Damien
15:12	[158] Development of Low Temperature Analog Readout (LTARS 2018) for LAr-TPC	SUMOMOZAWA, Shota

Tuesday, 26 May 2020

Readout: Front-end electronics: Block 2 (09:00 - 10:30)

time	[id] title	presenter
09:00	[66] pSLIDER32: a 32 channels mixed-signal processor for the GAPS Si(Li) Tracker	MANGHISONI, Massimo
09:18	[80] A Readout System for ALPIDE sensors of the ALICE Inner Tracking System Upgrade	SIDDHANTA, Sabyasachi
09:36	[94] Hybrid Circuits to Read Out the Forward Strip Inner Tracker at the ATLAS Detector for the High Luminosity LHC Upgrade	
09:54	[99] Pixel Chip Developments and Radiation Qualification for the High Luminosity LHC	
10:12	[207] High Voltage Monolithic Active Pixel Sensors for High Energy Electron Beam Compton Polarimetry	Dr PANDEY, Preeti

Readout: Front-end electronics: Block 3 (14:00 - 15:30)

time	[id] title	presenter
14:00	[83] Upgrade of the Muon Drift Tube (MDT) electronics for the ATLAS Phase-II upgrade	ATLAS, Muon Coll.
14:18	[88] Electronics Performance of the ATLAS New Small Wheel Micromegas wedges at CERN	ATLAS, MUON COLL.
14:36	[176] Sub-nanosecond Cherenkov photon detection for LHCb RICH particle identification in high-occupancy conditions	
14:54	[199] A 4D fast tracking detector for the high-luminosity LHC	
15:12	[227] Analog front-end in 65 nm CMOS with amplitude and timing measurement capability for large-area SiPM readout.	DABROWSKI, Mietek

Readout: Front-end electronics: Block 4 (16:00 - 17:30)

time	[id] title	presenter
16:00	[135] Performance of the "IRSX" multi-GSa/s, Switched Capacitor Array Waveform Sampling Frontend ASIC	HARTBRICH, Oskar
16:18	[157] bunch-by-bunch vertical beam size monitor for SuperKEKB	ANDREW, Matt
16:36	[140] Front End Electronics Module Design for the Schwarzschild-Couder Telescope (SCT) Camera	
16:54	[195] High resolution imaging and time resolution using capacitive division	
17:12	[237] Development of the front-end electronics for Hyper-Kamiokande	