Thursday 28 May 2020

Readout: Trigger and DAQ: Block 1 (09:00 - 10:30)

time	[id] title	presenter
	[8] Algorithm design and expected performance of the ATLAS L1 calorimeter trigger for Run 3	CARLSON, Ben
	[10] FELIX: commissioning the new detector interface for the ATLAS trigger and readout system	TANG, Shaochun
09:36	[35] ECAL trigger performance in Run 2 and improvements for Run 3	TISHELMAN CHARNY, Abraham
10:12	[141] The Global Level-1 Trigger for CMS at the High-Luminosity LHC	

Readout: Trigger and DAQ: Block 2 (11:00 - 12:30)

time	[id] title	presenter
	[118] CALICE SiW ECAL - Development and performance of a highly compact digital readout system	
11:18	[63] The Terabit Readout Architecture for the LHCb VELO Upgrade	HENNESSY, Karol BUYTAERT, Jan
11:36	[62] The CMS Data Acquisition System for the Phase-2 Upgrade	
11:54	[36] Serenity: An ATCA data-processing platform for CMS HL-LHC upgrades	WILLIAMS, Tom

Readout: Trigger and DAQ: Block 3 (14:00 - 15:30)

time	[id] title	presenter
14:00	[128] Implementation of A High Throughput IO System for Detector Control System of JUNO	
14:18	[139] Experience and performance of persistent memory for the DUNE data acquisition system	
14:36	[239] Commissioning of the Waveform-Sampling Scintillator Readout for the Belle II KLM Detector	KETTER, Christopher
14:54	[247] Development of level-1 trigger system in Belle II experiment	
15:12	[87] NSW sTGC Front-end electronics integration and commissioning	ATLAS, Muon Coll.