



T-CAD Simulations of 3D Microstrip detectors

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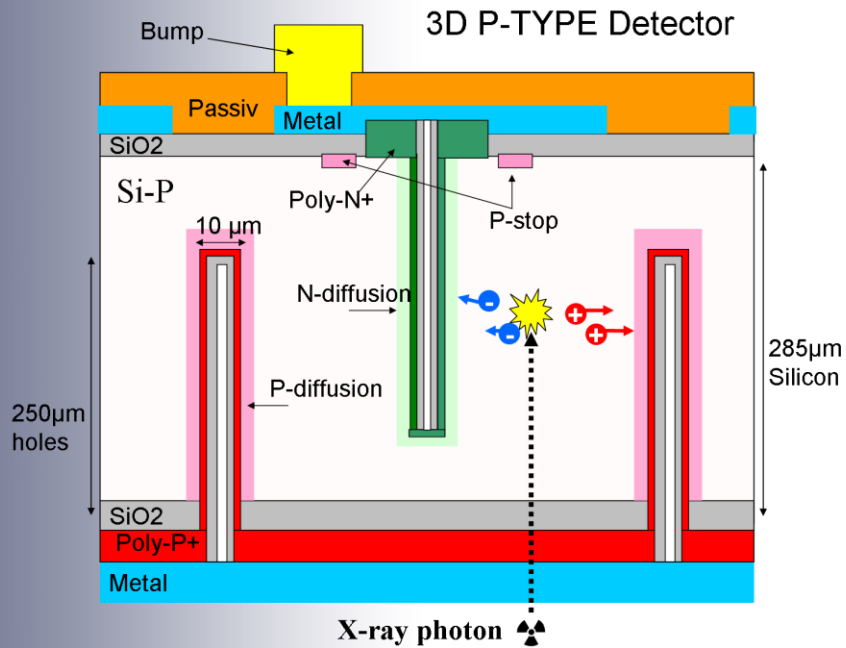
- Introduction
 - Motivation
 - Double-sided 3D detectors
- Different designs
 - CNM-IMB (C.S.I.C.)
 - FBK-irst
 - Full3D
- Experimental data available
- Simulations

Aims

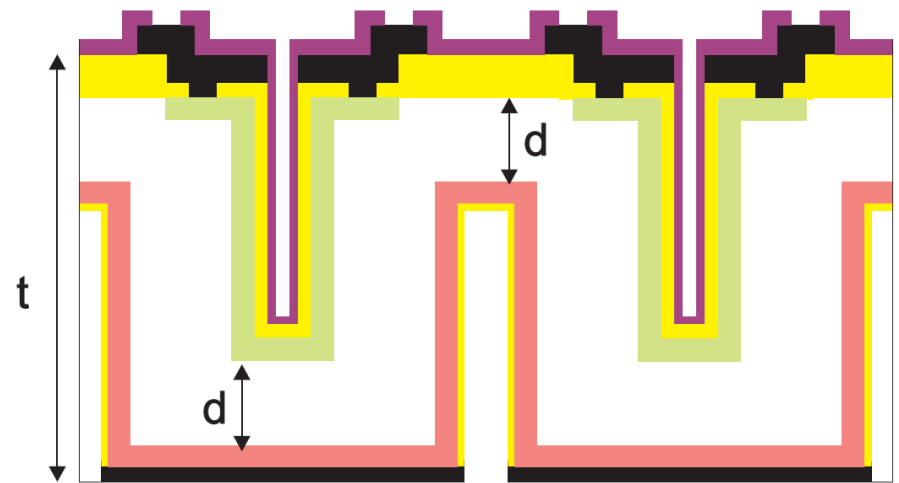
- Simulate negative pulses that have been experimentally observed from 3D detectors in Test Beam and Laser tests measured at Freiburg
- Examine charge multiplication after irradiation
 - Builds on simulations of David Pennicard

3D Detectors

3D-DDTC (CNM-IMB)

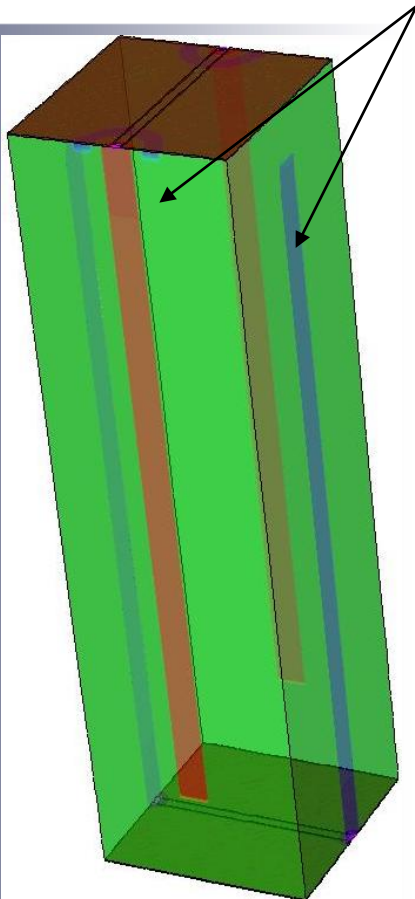


3D-DDTC (FBK-irst)



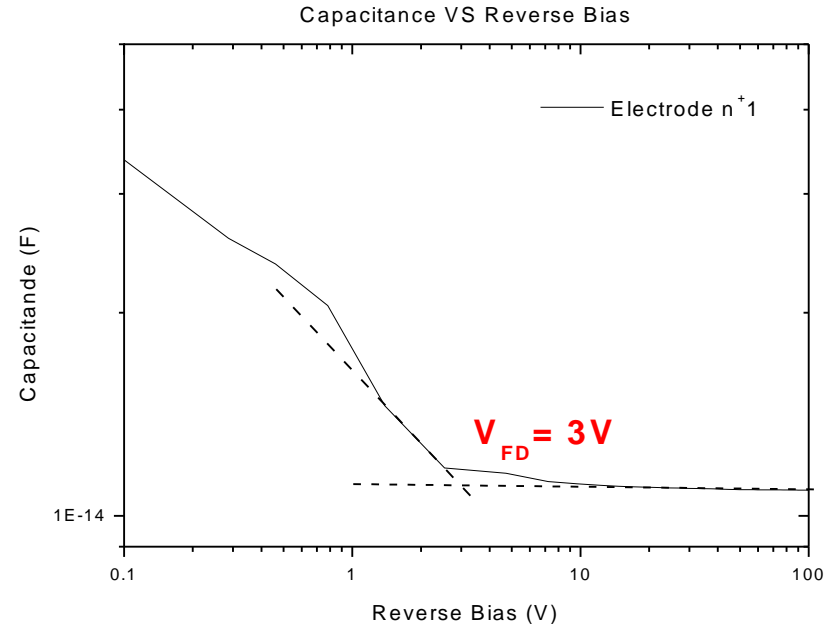
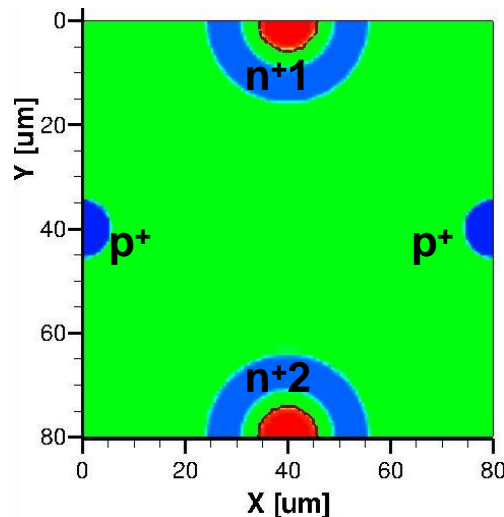
Designs simulated: CNM (p-type)

Columns filled with single crystal silicon



Geometrics

- pitch: 80 μm
- wafer thickness: 285 μm
- column depth: 250 μm
- column diameter: 10 μm



Doping levels

- p-type bulk: $7 \times 10^{11} \text{ cm}^{-3}$
- n+ columns: 10^{19} cm^{-3}
- p+ columns: 10^{19} cm^{-3}
- column doping has error profile
- p-stop: 10^{18} cm^{-3}
- Si/SiO₂ interface charge: 10^{11} cm^{-2}

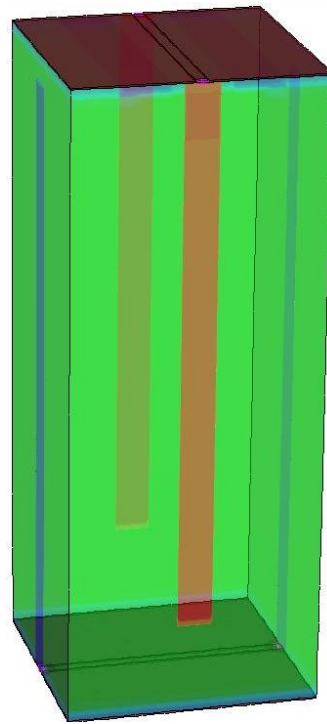
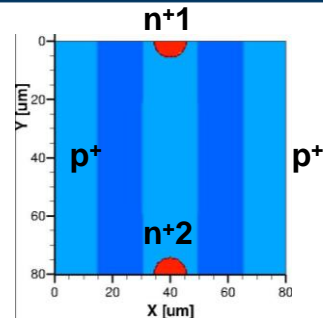
P-type

Geometrics

- pitch: 80 μm
- wafer thickness: 200 μm
- p⁺ column depth: 190 μm
- n⁺ column depth: 170 μm
- column diameter: 10 μm

Doping levels

- p-type bulk: $7 \times 10^{11} \text{ cm}^{-3}$
- n⁺ columns: 10^{19} cm^{-3}
- p⁺ columns: 10^{19} cm^{-3}
- p-stop: 10^{18} cm^{-3}
- p-spray: $5 \times 10^{16} \text{ cm}^{-3}$
- p-layer: 10^{19} cm^{-3}
- Si/SiO₂ interface charge: 10^{11} cm^{-2}



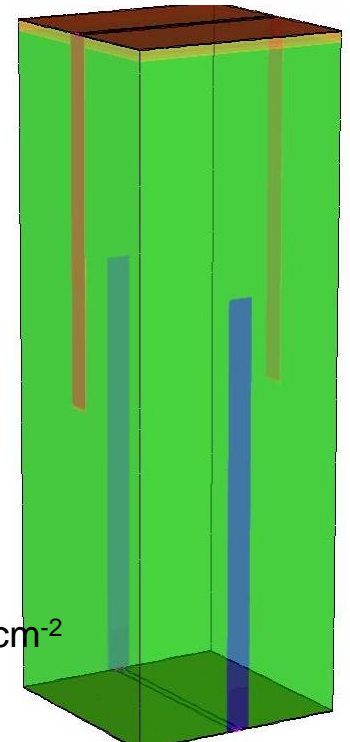
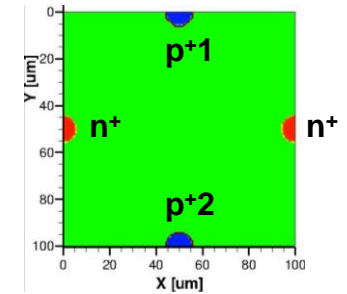
N-type

Geometrics

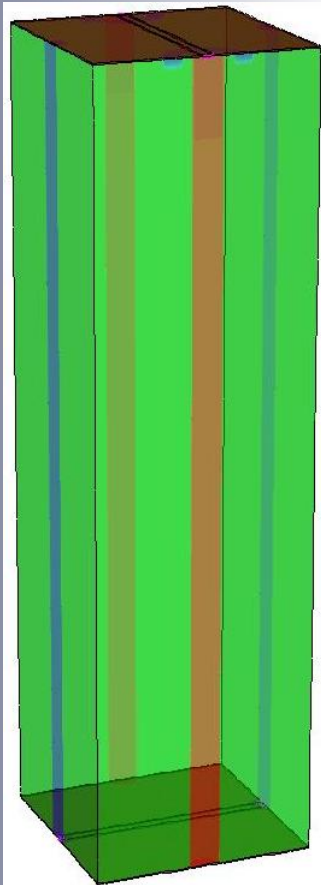
- pitch: 100 μm
- wafer thickness: 300 μm
- p⁺ column depth: 190 μm
- n⁺ column depth: 160 μm
- column diameter: 10 μm

Doping levels

- n-type bulk: 10^{12} cm^{-3}
- n⁺ columns: 10^{19} cm^{-3}
- p⁺ columns: 10^{19} cm^{-3}
- p-layer: 10^{19} cm^{-3}
- Si/SiO₂ interface charge: 10^{11} cm^{-2}

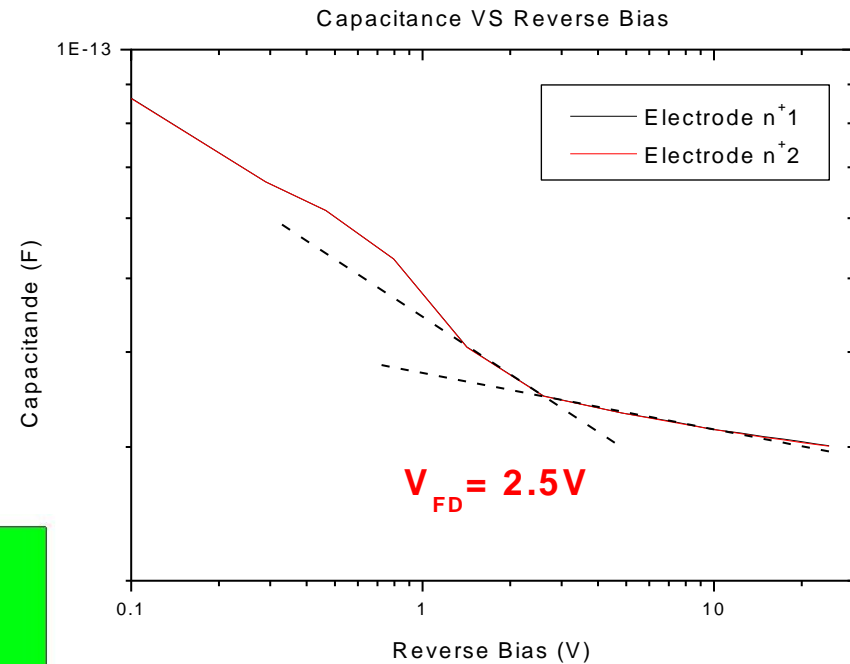
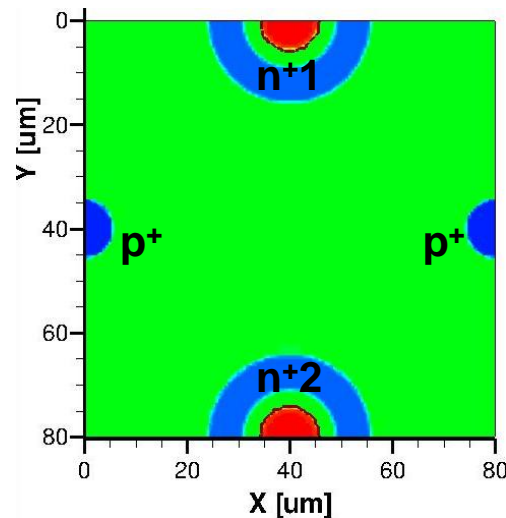


Columns completely filled with doping material



Geometrics

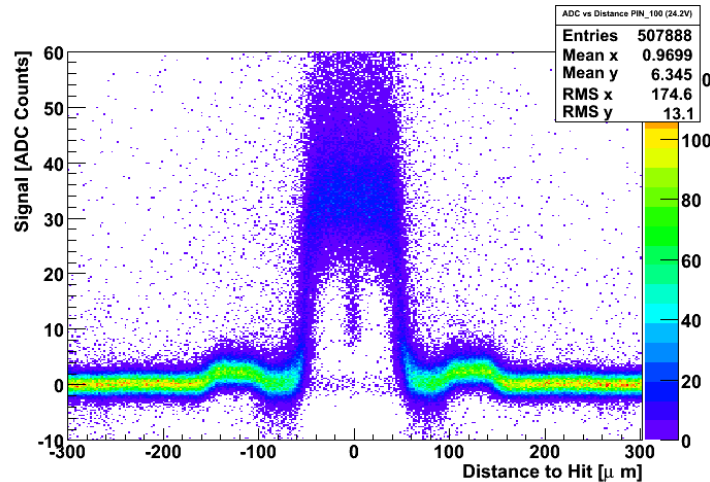
- pitch: 80 μm
- wafer depth: 285 μm
- column diameter: 10 μm



Doping levels

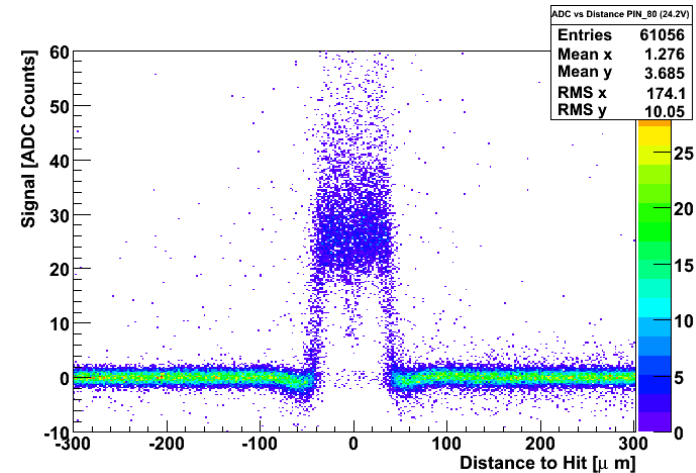
- p-type bulk: $7 \times 10^{11} \text{ cm}^{-3}$
- n+ columns: 10^{19} cm^{-3}
- p+ columns: 10^{19} cm^{-3}
- column doping has error profile
- p-stop: 10^{18} cm^{-3}
- Si/SiO₂ interface charge: 10^{11} cm^{-2}

FBK n-type. 24V bias



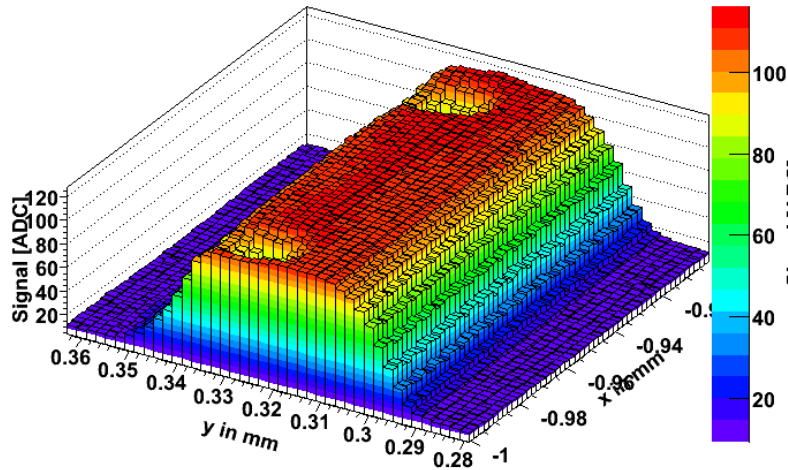
- Pitch is 100μm
- Readout strip centred at $x = 0$
- Negative pulses seen for tracks from 50 to 100μm
 - First half of neighbouring cell
- Positive pulses seen for tracks in 2nd half of neighbouring cell

CNM n-type. 24V bias



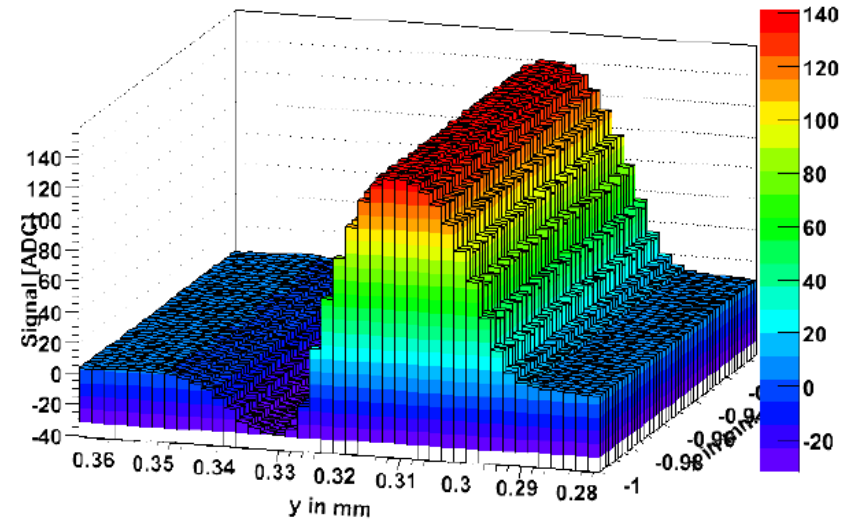
- Pitch is 80μm
- Readout strip centred at $x=0$
- Negative pulses seen from 40 to 80μm on either side of readout strip
- No positive pulses seen in 2nd half of neighbouring cell

Summiertes Signal von Kanal 191 und 192 bei 80V



Signal from sum of two neighbouring strips

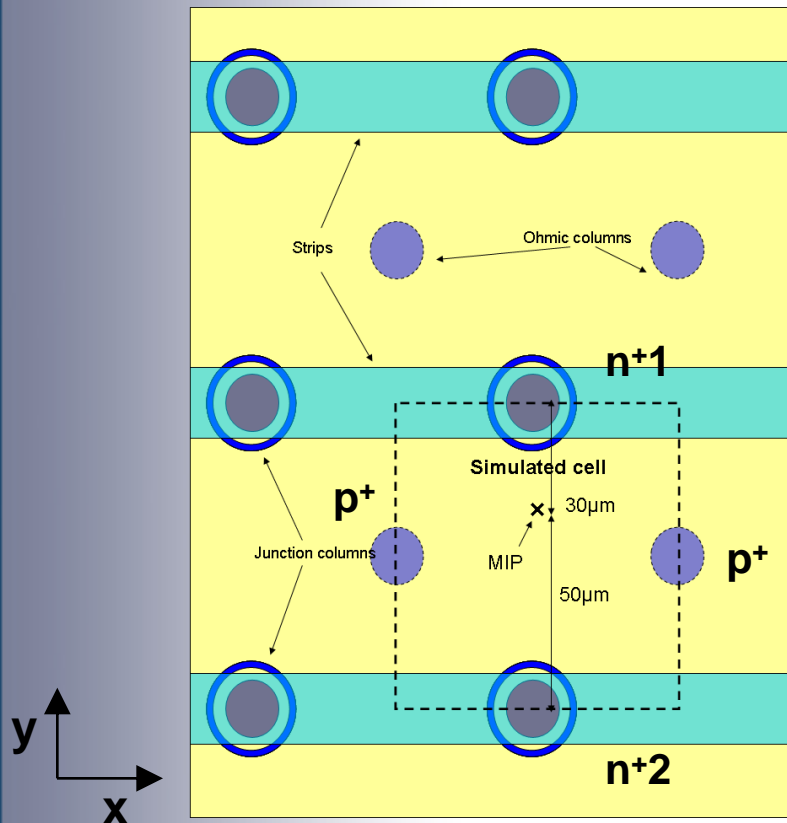
Kanal 191 bei 80V



Signal from only one strip

- CNM double sided device
- P-type bulk, n-type readout strips, 80 μ m pitch
- Laser illumination, step size 2 μ m
- Negative pulse seen when laser illuminates neighbouring strip
- Maximum negative signal seen 50 μ m from centre of readout strip

Simulated cell for both CNM and Full3D designs



Deposited uniform charge cloud
with $80e/h$ pairs / μm of thickness

⇒ **22800 Electrons in total (CNM)**

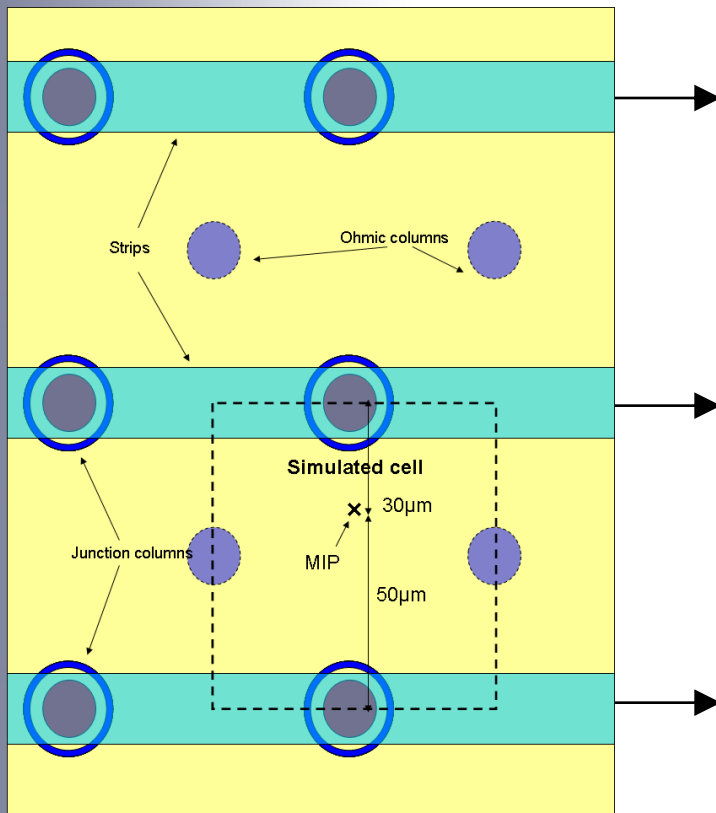
The track had a Gaussian lateral
distribution, with a standard deviation of
1 micrometer

Track position:

**Midway between p^+ back
side contacts (x position)**

**$10 \mu\text{m}$ off centre between n^+
readout columns of different strips
(y position)**

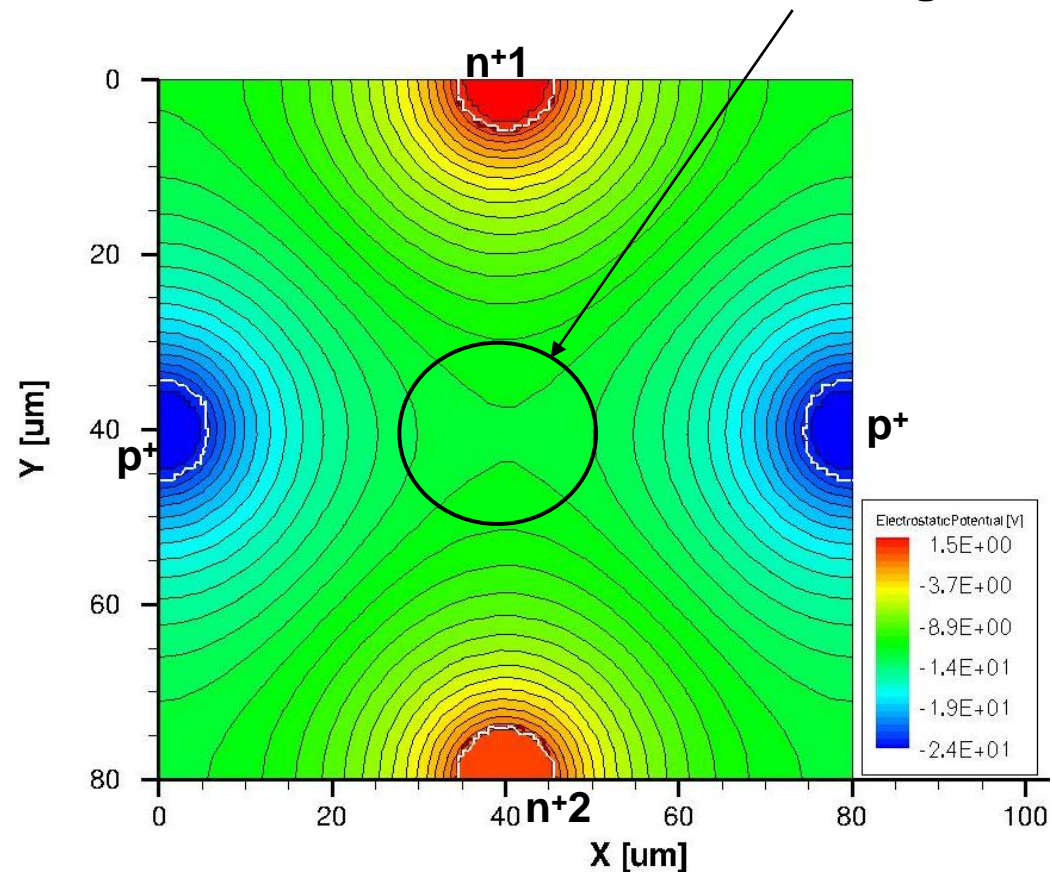
Simulated cell for both CNM and Full3D designs



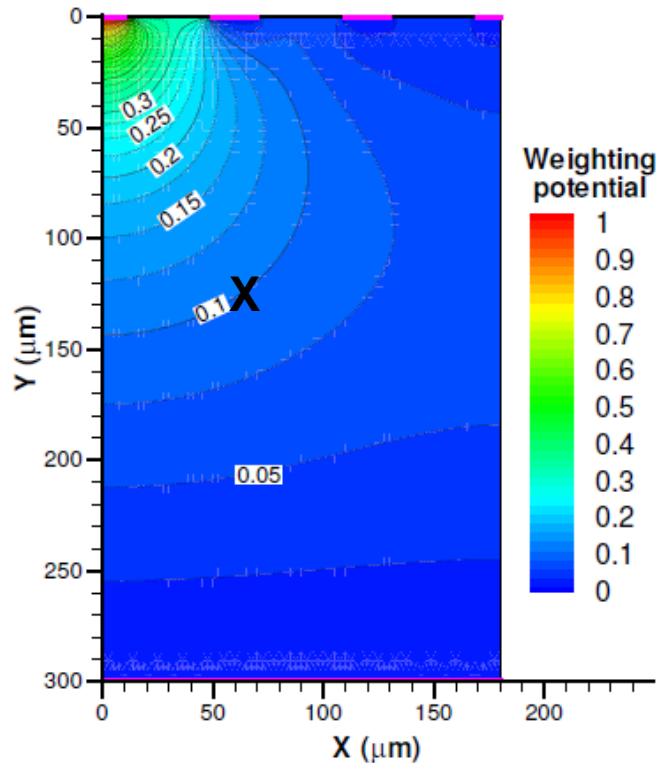
Simulated Potential. CNM P-type.

Reverse bias: -24V

Low field region



Weighting potentials



- Charge signal for carrier moving from point A to point B

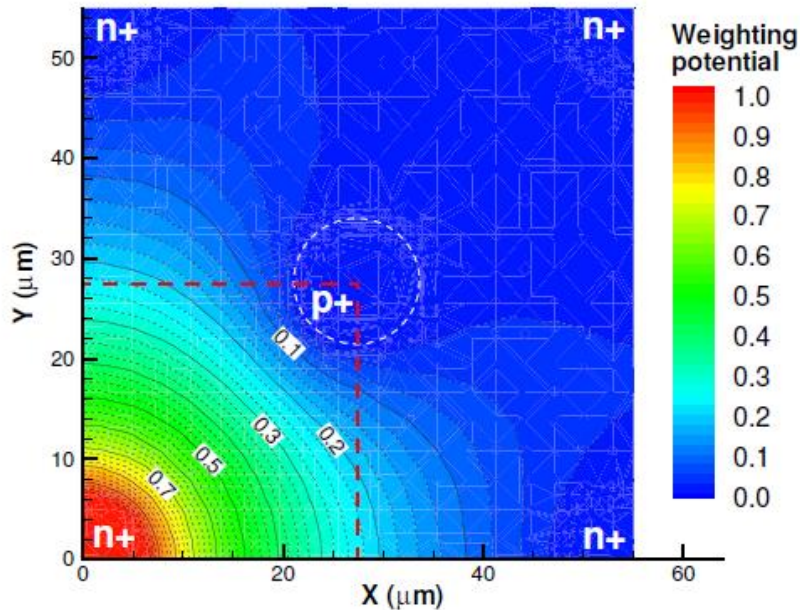
$$= q (\Psi_w(x_B) - \Psi_w(x_A))$$
- So signal for e/h generates at X is

$$-q(0 - 0.1) + q(0 - 0.1) = 0$$

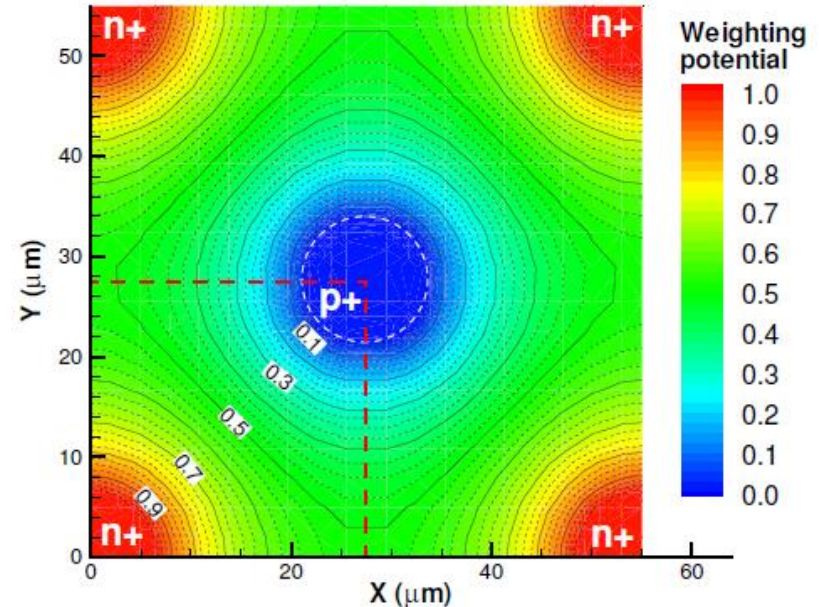
Which is nice
- Signal also bipolar due to charge carrier crossing higher weighting potential lines in both directions
 - 0.1 to 0.15 to 0.1 to 0

Weighting potentials

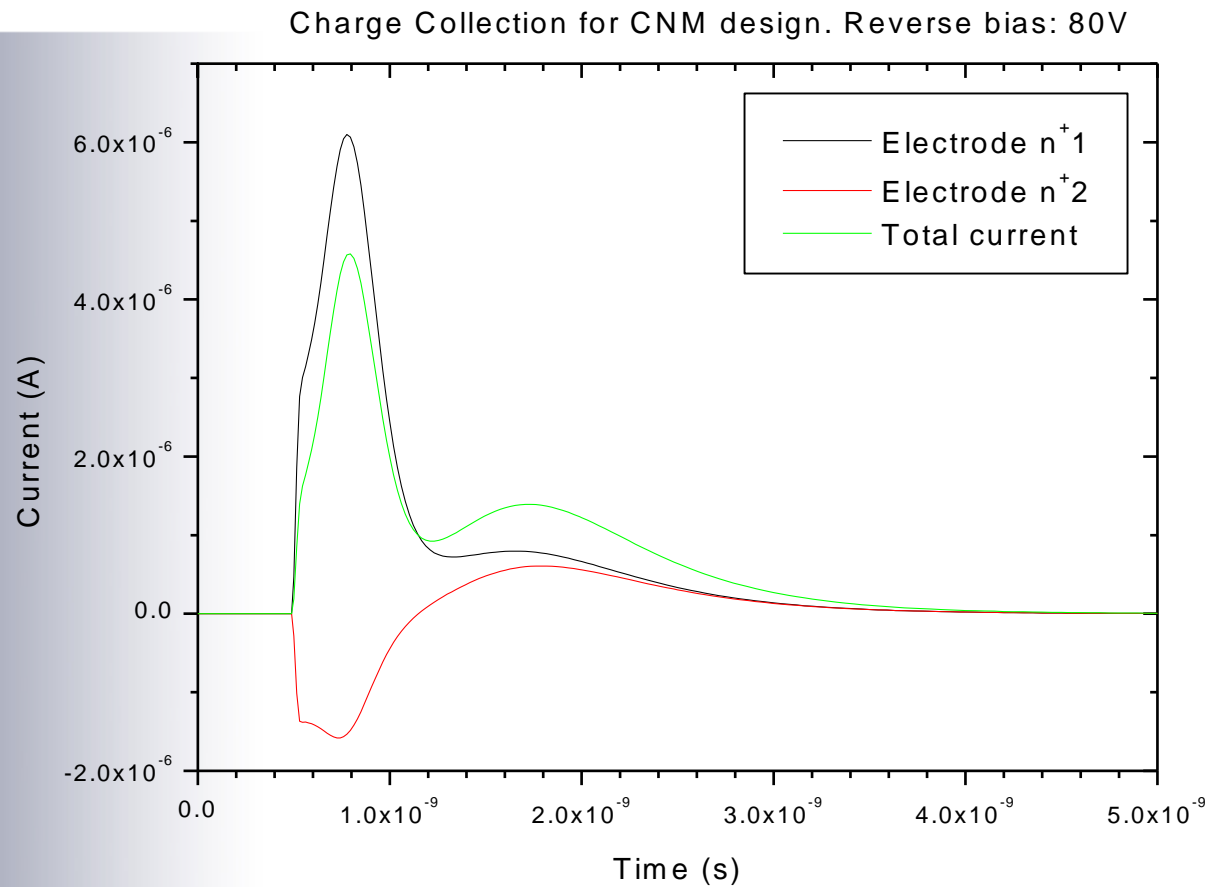
Pixel



Diode

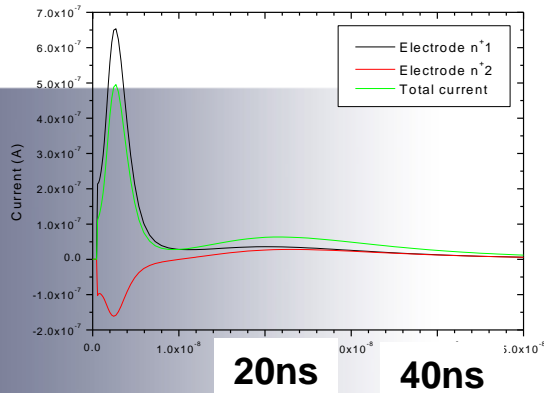


- Our weighting potential some where between these two cases
- Reasonable potential in neighbouring cell
 - Big current for movement in neighbouring cell
 - Should go to zero is all charge is collected



Results: Charge Collection (CNM p-type)

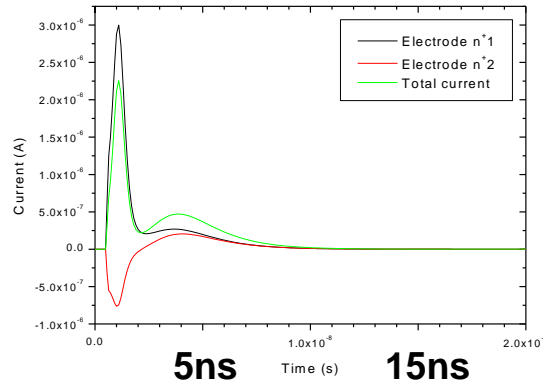
I at 3V bias



20ns 40ns

	25ns	50ns
n+1	17097	19753
n+2	-1868	585
total	15223	20338

I at 24V bias

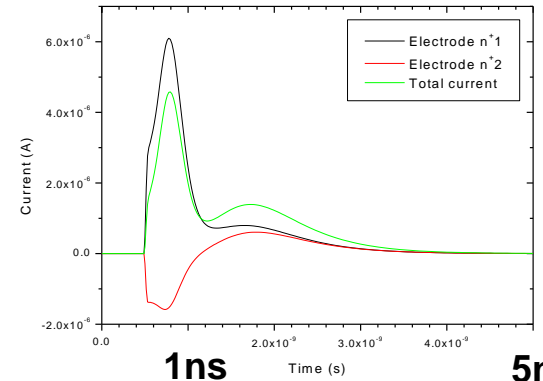


5ns 15ns

	20ns
n+1	20983
n+2	812
total	21795

4.5% CEI

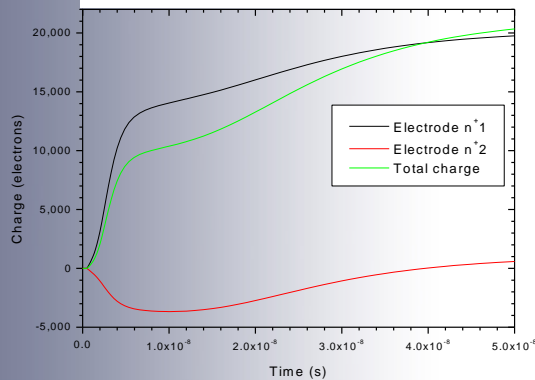
I at 80V bias



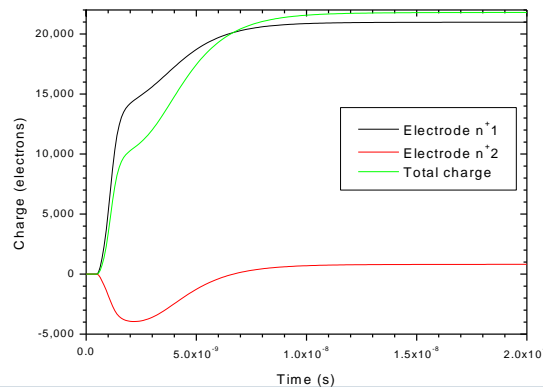
1ns 5ns

	5ns
n+1	21827
n+2	757
total	22584

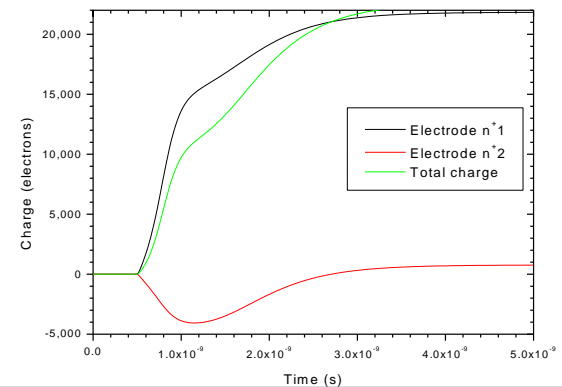
Q at 3V bias



Q at 24V bias



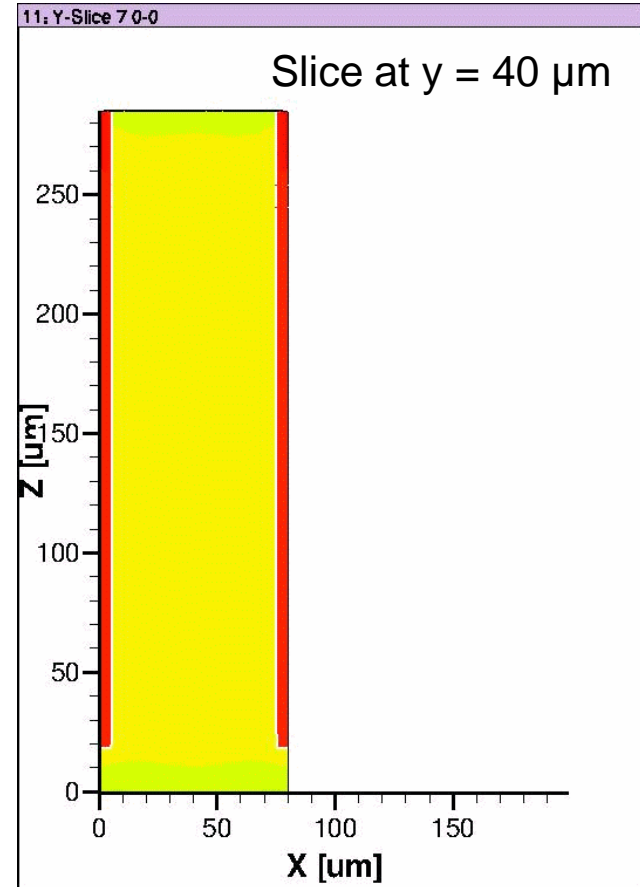
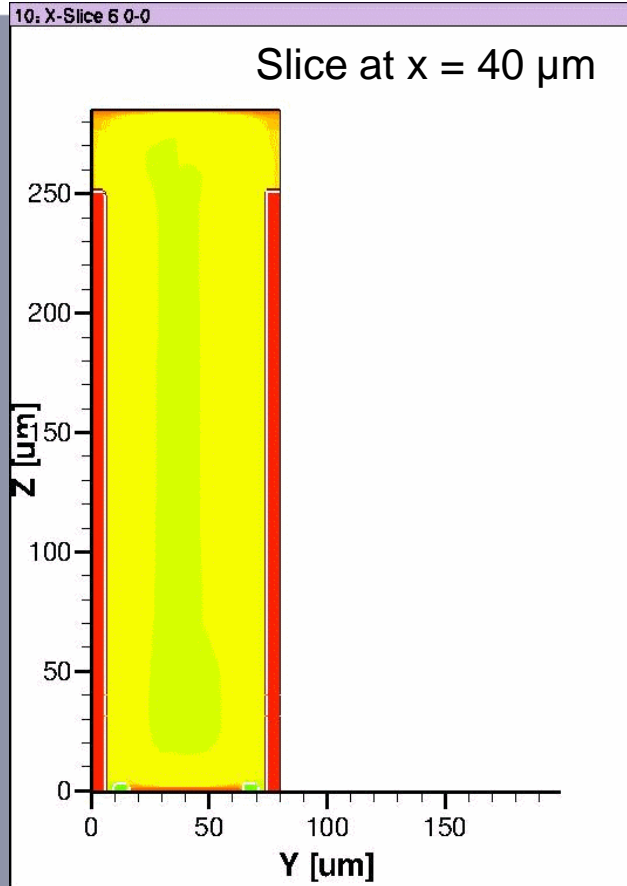
Q at 80V bias



Electrons collection

80V

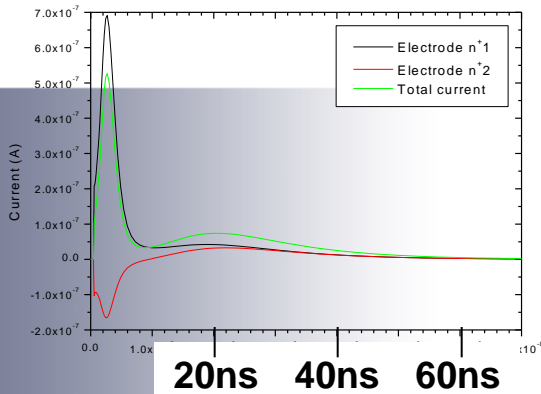
Holes collection



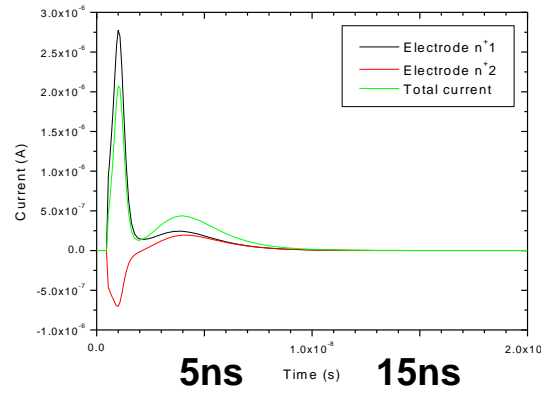
Time: 0 – 5 ns (step 0.5ns)

Results: Charge Collection (Full3D p-type)

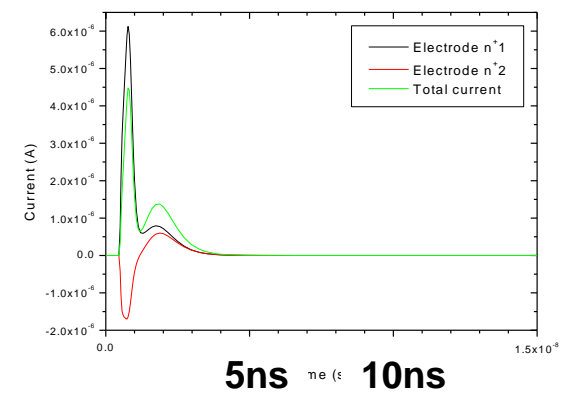
I at 3V bias



I at 24V bias



I at 80V bias



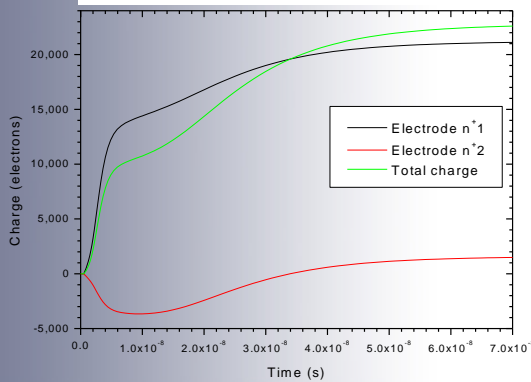
	25ns	70ns
n+1	18023	21113
n+2	-1422	1488
total	16600	22601

	20ns
n+1	17774
n+2	955
total	18729

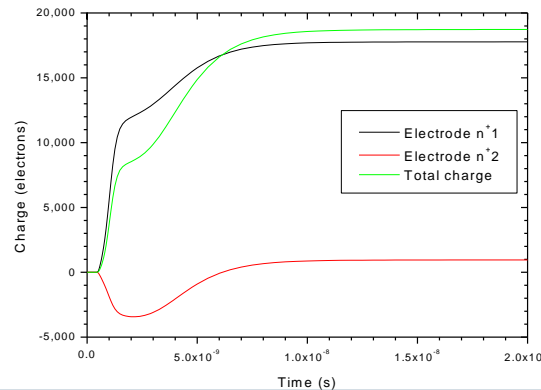
Something wrong here!

	15ns
n+1	21247
n+2	244
total	21591

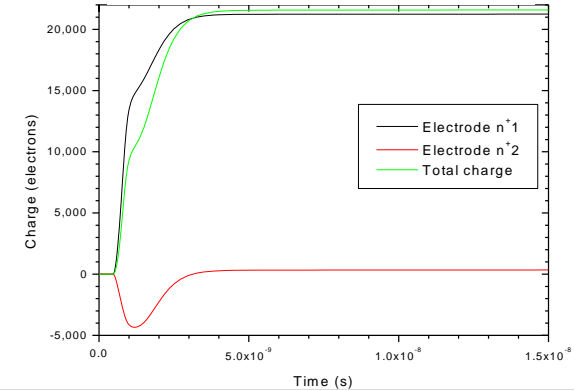
Q at 3V bias



Q at 24V bias



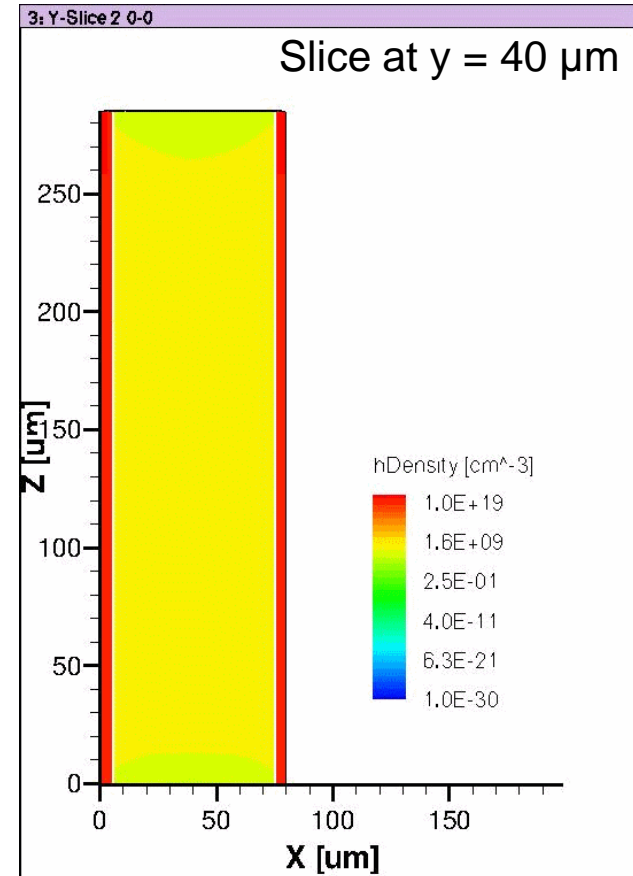
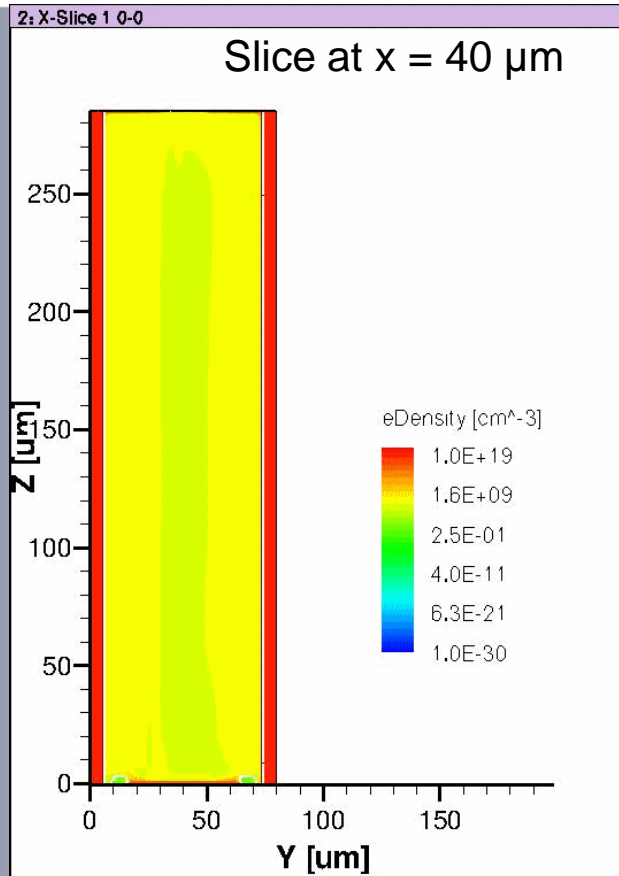
Q at 80V bias



Electrons collection

80V

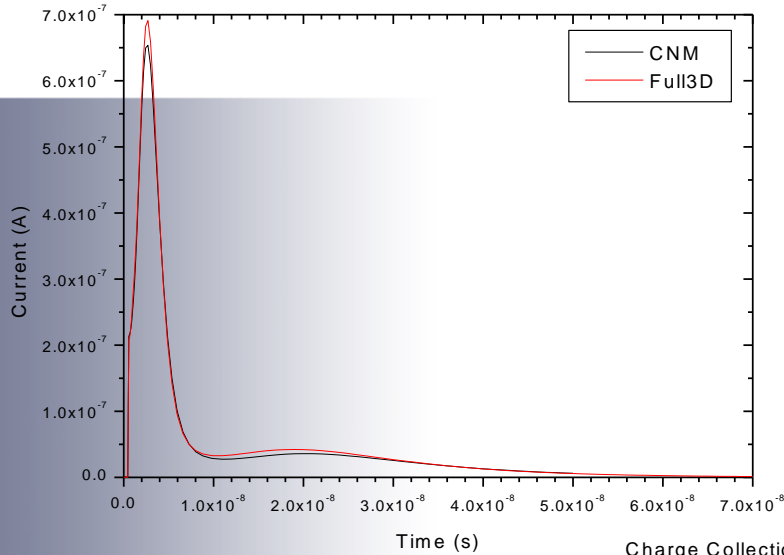
Holes collection



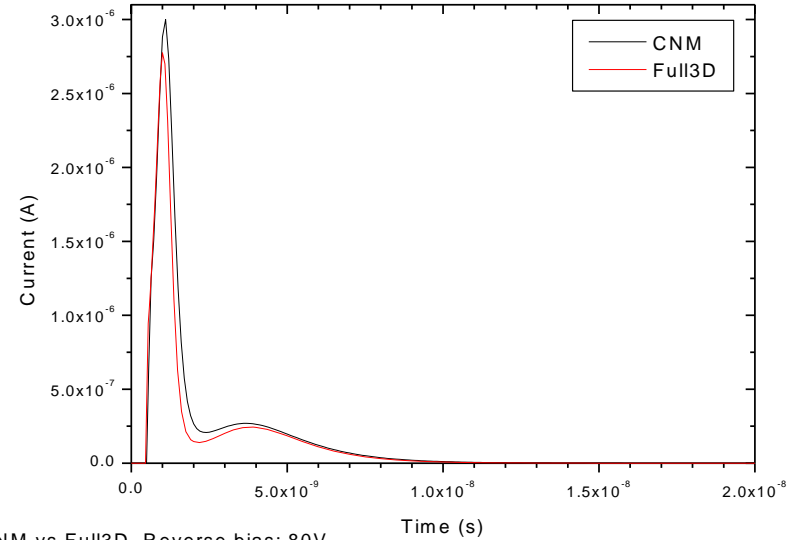
Time: 0 – 5 ns (step 0.5ns)

Results: CNM vs Full3D (p-type)

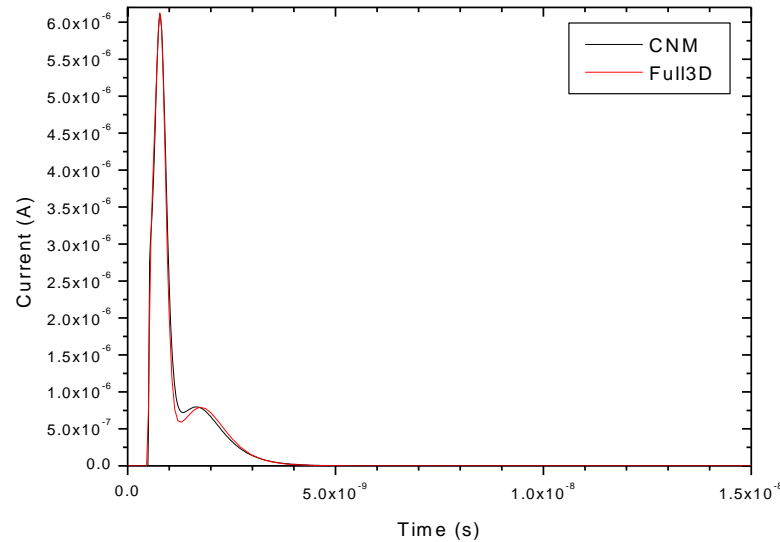
Charge Collection Comparison of CNM vs Full3D. Reverse bias: 3V



Charge Collection Comparison of CNM vs Full3D. Reverse bias: 24V



Charge Collection Comparison of CNM vs Full3D. Reverse bias: 80V



- Early days
- 3D simulation of 3D detectors up and running again.
- We have not yet managed yet to simulate the negative signals that are observed in Testbeam and Laser tests.
- Need to improve mesh, time steps and model to be more realistic
 - More realistic electrodes - added some resistivity & recombination to the doped columns (poly rather than signal crystal)
 - Maybe a contact resistance?
 - Recombination at the oxide
- We can see what would be a negative pulse when operated at low bias and fast sampling time.
- But at high bias there is no negative charge signal.

- Simulate 3D negative pulses as observed in Test Beams and Laser tests.
 - Currently working on n-type devices
- Examine charge multiplication after irradiation