

## **Title: *Quantum information processing and beam diagnostic with single electron devices***

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Implementation scheme for electrostatic quantum computer is given with reference to recent cryogenic single-electron(s) CMOS technologies [1-4]. Particular attention is paid to sensing of moving charge what has its importance in accelerator beam diagnostics. Analytical and numerical solutions describing quantum gates and weak measurement are provided with a use of tight-binding approximation model and Schroedinger formalism [5-8]. The implementation scheme for quantum networks is given in the framework of CMOS technology. Decoherence effects occurring in electrostatic qubits and quantum gates are described analytically for two interacting electrons confined by local potentials with a use of tight-binding simplistic model and in Schrödinger formalism with an omission of spin degree-of-freedom [8-9]. The concept of CMOS programmable quantum matter with built-in quantum sensors is discussed. At very end the concept of quantum AI is introduced with particular attention focused on programmable quantum neural network. The perspective of usage of quantum chips for quantum sensing in CERN experiments is given with first analytical and numerical results with reference to recent technological frontiers [10-13].

### **References**

- [1]. T.Fujisawa, "**Quantum Information Technology based on Single Electron Dynamics**", NTT Technical Review, Vol,1, 2003.
- [2]. I.Bashir, M.Asker, C.Cetintepe, D.Leipold, A.Esmailian, H.Wang, T.Siriburanon, P.Giounanlis, E.Blokhina, K.Pomorski, R.B.Staszewski, "**Mixed-Signal Control Core for a Fully Integrated Semiconductor Quantum Computer System-on-Chip**", Sep 2019 ESSCIRC 2019 - IEEE 45th European Solid State Circuits Conference (ESSCIRC), (<https://ieeexplore.ieee.org/abstract/document/8902885/authors#authors>)
- [3]. K.Pomorski, P. Giounanlis, E.Blokhina, D.Leipold, R.B.Staszewski, "**From two types of electrostatic position-dependent semiconductor qubits to quantum universal gates and hybrid semiconductor-superconducting quantum computer**", Proceedings Volume 11054, Superconductivity and Particle Accelerators 2018; 110540M, 2019 (<https://www.spiedigitallibrary.org/conference-proceedings-of-spie/11054/110540M/From-two-types-of-electrostatic-position-dependent-semiconductor-qubits-to/10.1117/12.2525217.full>)
- [4]. P.Giounanlis, E.Blokhina, K.Pomorski, D.Leipold, R.B.Staszewski, "**Modeling of Semiconductor Electrostatic Qubits Realized Through Coupled Quantum Dots**", IEEE Access, vol. 7, pp. 49262-49278, 2019, doi: 10.1109/ACCESS.2019.2909489
- [5]. K.Pomorski, P.Giounanlis, E.Blokhina, D.Leipold, R.B.Staszewski, "**Analytic view on coupled single electron lines**", IOP Journal of Semiconductor and Technology Semiconductor Science and Technology, Vol.34, Nr.12, 2019 (<https://iopscience.iop.org/article/10.1088/1361-6641/ab4f40>)
- [6]. K.Pomorski, R.B.Staszewski, "**Analytical Solutions for N-Electron Interacting System Confined in Graph of Coupled Electrostatic Semiconductor and Superconducting Quantum Dots in Tight-Binding Model with Focus on Quantum Information Processing**", 2019, Arxiv: 1907.03180 (<https://arxiv.org/abs/1907.03180>)
- [7]. K.Pomorski, R.B.Staszewski, "**Towards quantum internet and non-local communication in position based qubits**", 2019, Arxiv: 1911.02094 (<https://arxiv.org/abs/1911.02094>)
- [8]. K.Pomorski, "**Analytic view on N body interaction in electrostatic quantum gates and decoherence effects in tight-binding model**", 2019, Arxiv: 1912.01205 (<https://arxiv.org/abs/1912.01205>)
- [9]. K.Pomorski, "**Analytical view on tunable electrostatic quantum swap gate in tight-binding model**", 2020, Arxiv: 2001.02513 (<https://arxiv.org/abs/2001.02513>)
- [10]. E.Charbon, "**The role of Cryo-CMOS in Quantum Computers**" (indico.physics.lbl.gov/indico/event/837/attachments/1780/2309/Edoardo\_Charbon\_Berkeley19\_released-compressed.pdf)
- [11]. A.Beckers, "**Cryogenic Characterization of 28 nm Bulk CMOS Technology for Quantum Computing**" (<https://arxiv.org/pdf/1806.01142.pdf>).
- [12]. D.Kim et al., "**A CMOS-integrated quantum sensor based on nitrogen-vacancy centers**", Nature Electronics, 2019
- [13]. A. R. Mills, D. M. Zajac, M. J. Gullans, F. J. Schupp, T. M. Hazard & J. R. Petta, "**Shuttling a single charge across a one-dimensional array of silicon quantum dots**", Vol.10, Nature Communications, 2019