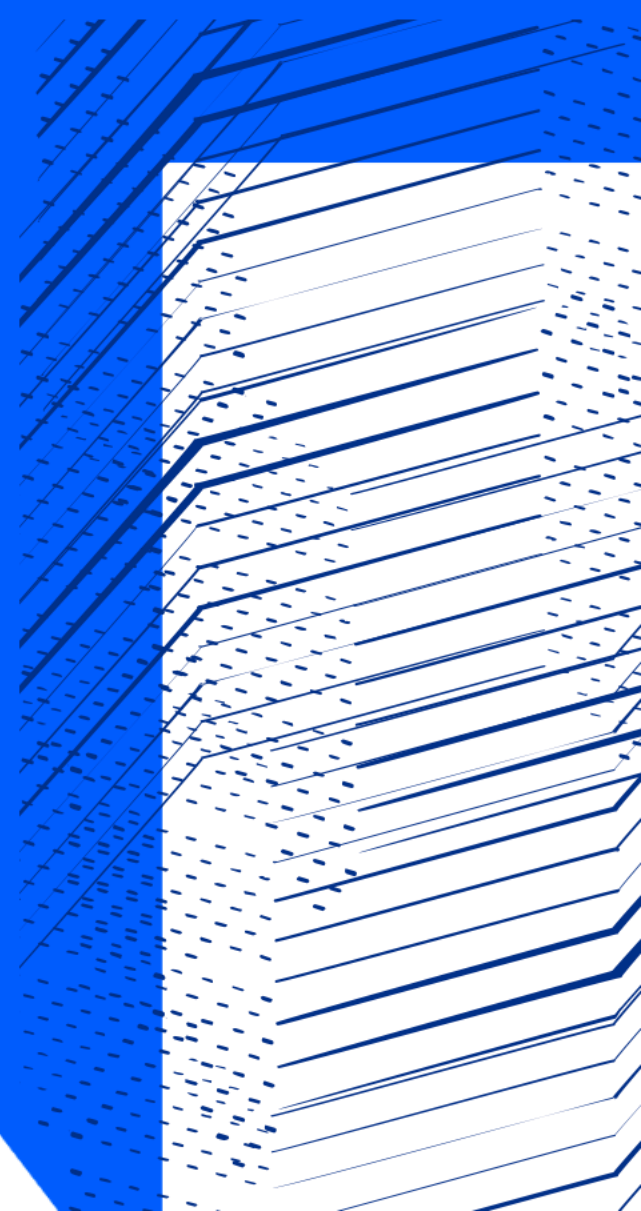




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Introduction

Alastair Dewhurst



Motivation

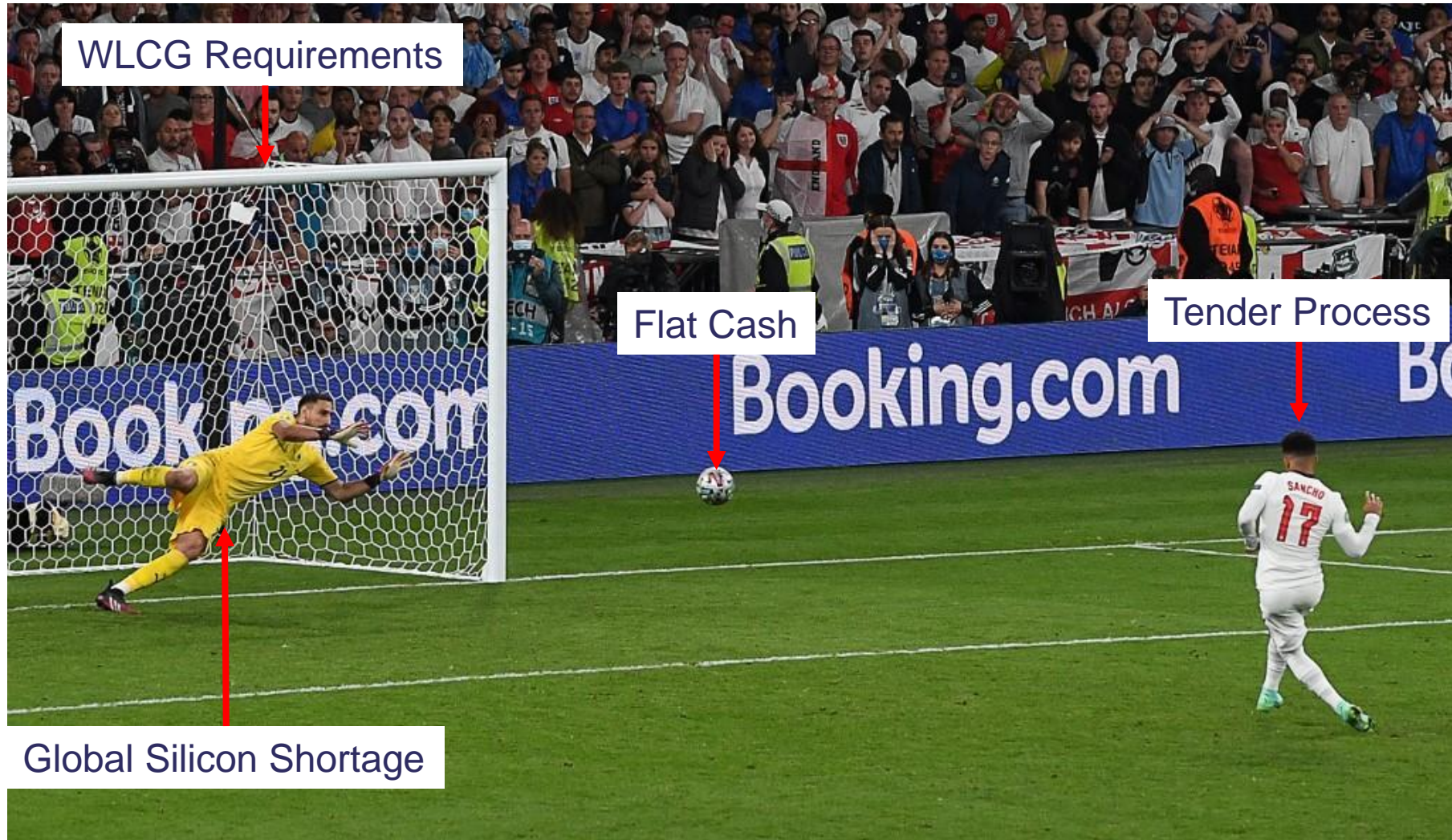
- When trying to debug CPU efficiency issues at RAL, I found there was a significant variety in how sites deploy their CPU.
 - Wanted to have a discussion between sites about setup.
- Significant progress is being made with new HEPSCORE benchmarking, which may also influence setup.
- Global Silicon shortage is causing problems for procurement which may also influence setup
 - Multiple Vendors have told us of long lead times (up to 30 weeks).
 - Prices are higher than last year.
- Sensible to ask VOs about their requirements and if they might change.

Agenda

- Requirements
 - Ensure we have the same understanding.
- Benchmarking
 - Progress with new benchmark, what can it tell us?
- Procurement
 - Talk from AMD
- Site Reports

13:30	→ 13:45	Introduction	🕒 15m	📄
		Speaker: Alastair Dewhurst (Science and Technology Facilities Council STFC (GB))		
13:45	→ 14:30	VO Requirements	🕒 45m	📄
		Speakers: Danilo Piparo (CERN) , Ivan Glushkov (University of Texas at Arlington (US)) , Steven Timm (Fermi National Accelerator Lab. (US))		
		ATLAS	🕒 10m	📄
		CMS ¶	🕒 10m	📄
		Dune	🕒 10m	📄
		Other	🕒 10m	📄
14:30	→ 15:00	Benchmarking	🕒 30m	📄
		HEPScore	🕒 20m	📄
		Speaker: Michele Michelotto (Universita e INFN, Padova (IT))		
		RAL Benchmarking results	🕒 10m	📄
		Speaker: Alastair Dewhurst (Science and Technology Facilities Council STFC (GB))		
15:00	→ 15:30	Discussion	🕒 30m	📄
15:30	→ 16:00	Break	🕒 30m	
16:00	→ 16:30	AMD Leadership High Performance Computing	🕒 30m	📄
		Speaker: Alexey Nechuyatov (AMD)		
16:30	→ 16:50	KIT Report	🕒 20m	📄
		Speaker: Manfred Alef (Karlsruhe Institute of Technology (KIT))		
16:50	→ 17:10	CERN Report	🕒 20m	📄
		Speaker: Luis Fernandez Alvarez (CERN)		
17:10	→ 17:30	BNL Report	🕒 20m	📄
		Speaker: Christopher Henry Hollowell (Brookhaven National Laboratory (US))		
17:30	→ 18:00	Discussion	🕒 30m	📄

Procurement Nightmare?





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Questions?