

Pixel2010 - International Workshop on Semiconductor Pixel Detectors for Particles and Imaging

Tuesday, September 7, 2010

Pixel Electronics (2:50 PM - 5:30 PM)

-Conveners: R. Horisberger

time	[id] title	presenter
2:50 PM	[48] Qpix v.1: A high speed 400-pixels readout LSI with 10-bit 10MSps pixel ADCs	LI, F.
3:10 PM	[49] Radiation test on FD-SOI Readout ASIC of Pair-monitor for ILC	SATO, Y.
3:30 PM	Coffee	
4:00 PM	[50] FE-14 Chip Development for the Upgraded ATLAS Pixel Detector at LHC	BARBERO, M.
4:20 PM	[51] A 130 nm ASIC prototype for the NA62 Gigatracker readout	DELLACASA, G.
4:40 PM	[52] Read-out Electronics for Low Dark Count Pixel Detectors based on Geiger Mode Avalanche Photodiodes Fabricated in Conventional CMOS Technologies for Future Linear Colliders	ARBAT, A,
5:00 PM	[53] sCMOS chips and associated electronics	HOLST, G.