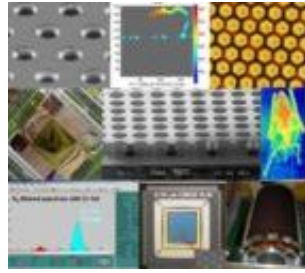


## 5th RD51 Collaboration Meeting



Contribution ID: 63

Type: **not specified**

# A new FPGA-based readout for Timepix Chips

*Tuesday 25 May 2010 16:50 (20 minutes)*

**Presenter:** KHARA, Christian (University of Mainz)

**Session Classification:** WG5: MPGD Related Electronics