

NSW Trigger Processor PRR (Part 2)

Introduction

- NSW TP was previously reviewed on October 14 2019, based on carrier v2 and mezzanine v1*
- The NSW community had hoped that the October review would be the PRR, to release the series production for the hardware
 - This had assumed sufficient firmware to be in place to have tested and validated all the hardware interfaces --- it was agreed that algorithms themselves could come later,
 - And it also assumed that details on production testing plans and production schedule would come later

In any case, the review panel did not share the communities' view, and requested the build of another carrier prototype, v3, to address the v2 bugs, and another review before then releasing full production

- Green light was however given for FPGA procurement (and later, in a discussion with Upgrade management, PCB order since here a split of pre-series (new prototype) and series was not practicable, and financial risk limited

So, today's review is a PRR in the classical sense, with all standard questions to be addressed. Combined test results, for carrier + mezz, are either based on measurements done by Samway with v3, or by the N SDW team with carrier v2 – this is a consequence of the COVID shutdown and CERN's closure