



Universidad de Oviedo

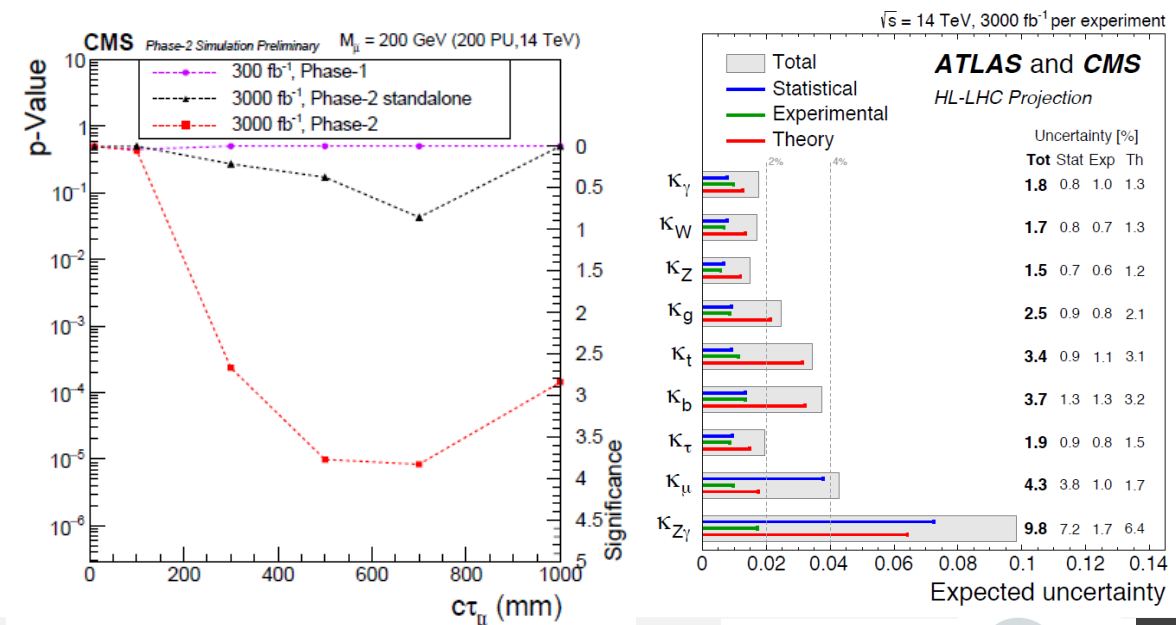
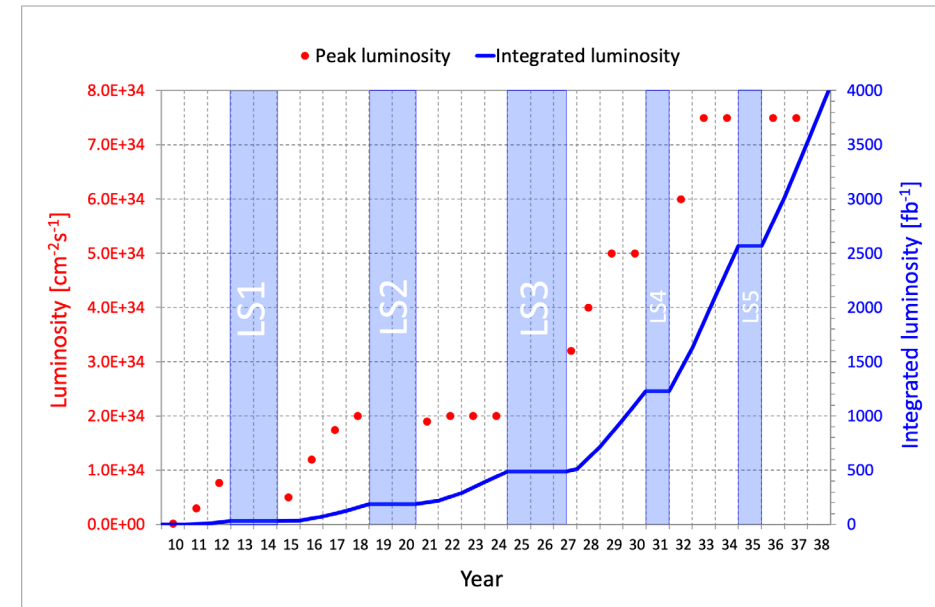


CMS upgrades

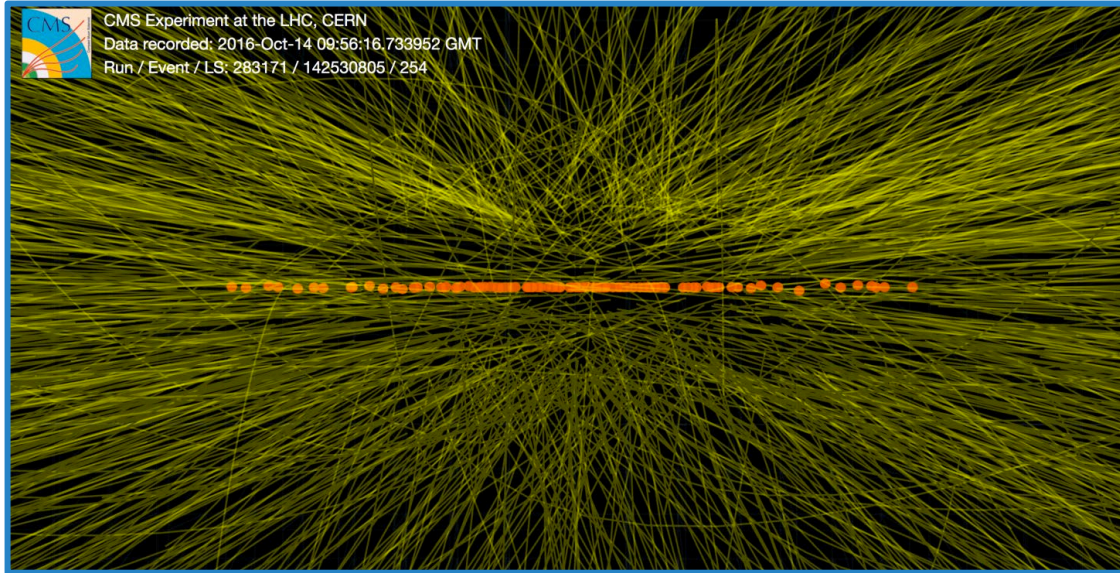
Santiago Folgueras, on behalf of the CMS Collaboration

Why HL-LHC?

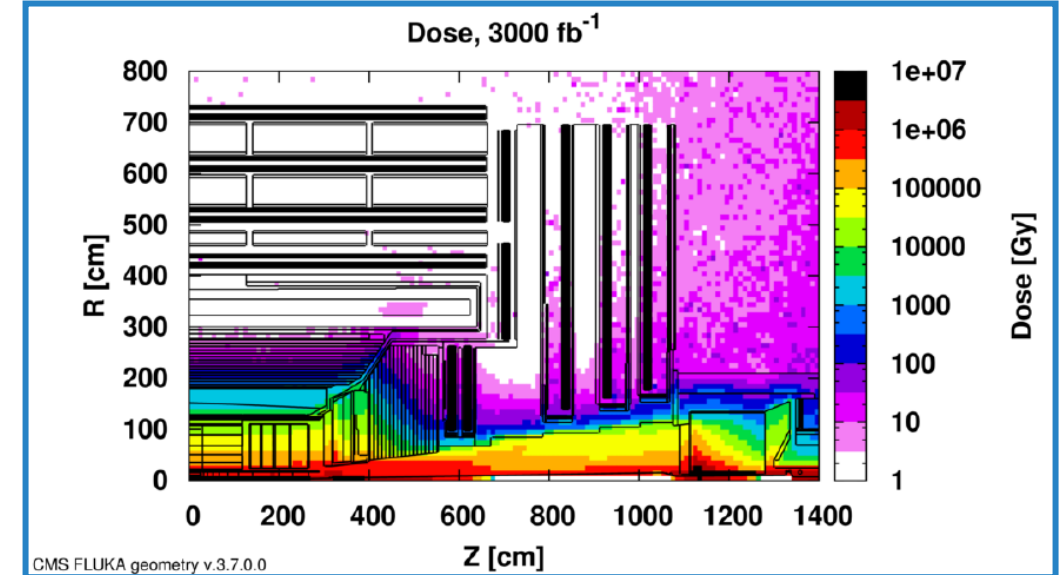
- HL-LHC upgrade offers an **unprecedented opportunity** to explore uncharted lands and achieve scientific progress.
- 10 times more data to what we will have by the end of Run 3 will facilitate a rich physics program.
- **Extend reach of new physics searches:** unexplored signatures (LLPs, HSCPs...) or regions of the phase-space will be within reach.
- **Improve current understanding of the SM and Higgs** sector by improving existing precision measurements and accessing rare decays ($H \rightarrow \mu\mu$) or production modes (HH) previously unseen at the LHC.
- However, this physics program will have to overcome **significant challenges** to succeed.



HL-LHC: challenges



- **Expected pileup (PU):** ~ 140 (nominal HL-LHC lumi)
- Motivates/requires:
 - Improved granularity wherever possible
 - Novel approaches to in-time Pile Up mitigation: Precision Timing detectors (30ps)
 - A complete renovation of the Trigger and DAQ systems for better selectiveness, despite the high PU.



- **Radiation damage / accumulated dose** in detectors and on-board electronics may result in a progressive degradation of the performance.
- Maintain detector performance in harsh conditions:
 - The complete replacement of the Tracker and Endcap Calorimeter systems.
 - Major electronics overhaul and consolidation of the Barrel Calorimeters and Muon systems

CMS Phase-2 upgrades

Replacements of existing system/detector
Electronics upgrade/replacement
New detector

L1-Trigger/HLT/DAQ [CMS-TDR-021 / 022]

- Tracks in L1-Trigger at 40 MHz
- PFlow-like selection 750 kHz output
- HLT output 7.5 kHz

Calorimeter Endcap [CMS-TDR-019]

- 3D showers imaging for pattern recognition
- Precision timing for PU mitigation
- Si, Scint+SiPM in Pb/W-SS

Tracker [CMS-TD-014]

- P_T module design for tracking in L1-Trigger
- Extended coverage to $\eta \approx 3.8$
- Much reduced material budget
- Si-Strip and Pixels increased granularity

Barrel Calorimeters [CMS-TDR-015]

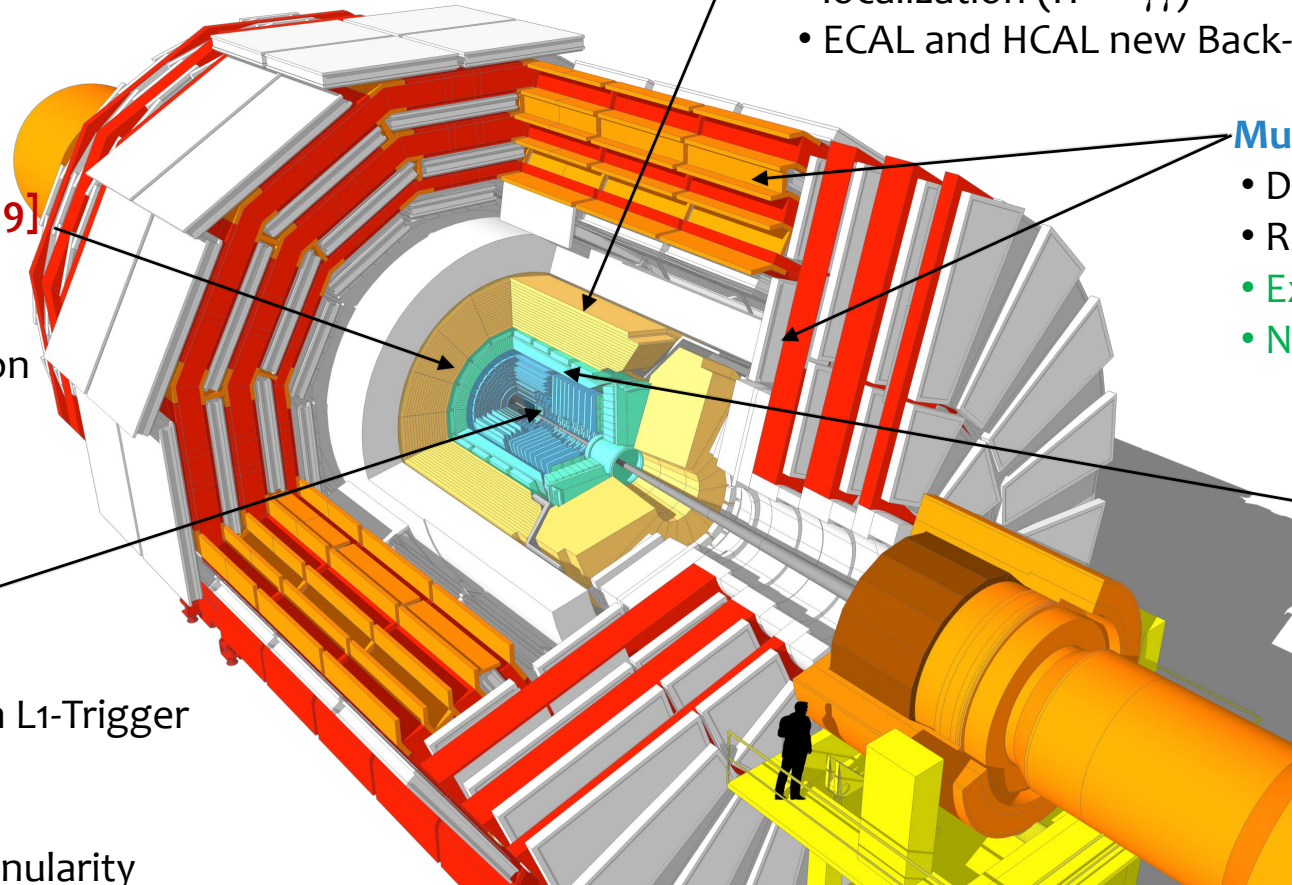
- ECAL crystal granularity readout at 40 MHz
- Precision timing for e/γ at 30 GeV, for vertex localization ($H \rightarrow \gamma\gamma$)
- ECAL and HCAL new Back-End boards

Muon systems [CMS-TDR-016]

- DT & CSC new FE/BE readout
- RPC back-end electronics
- Extended GEM coverage to $\eta \approx 3$
- New GEM/RPC $1.6 < \eta < 2.4$

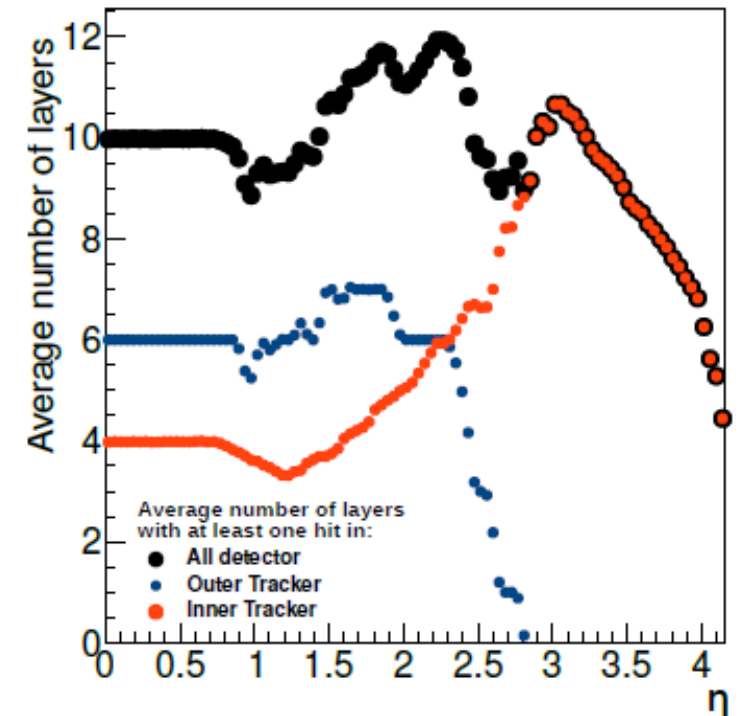
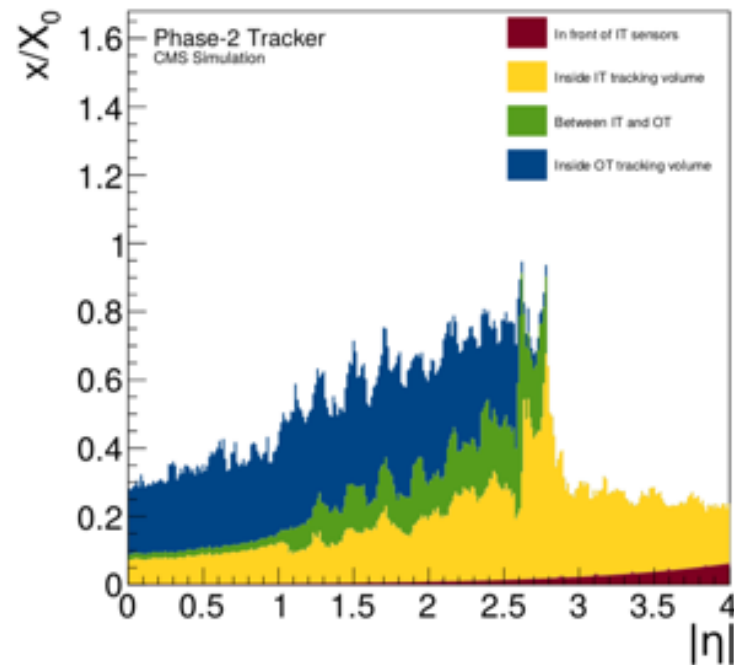
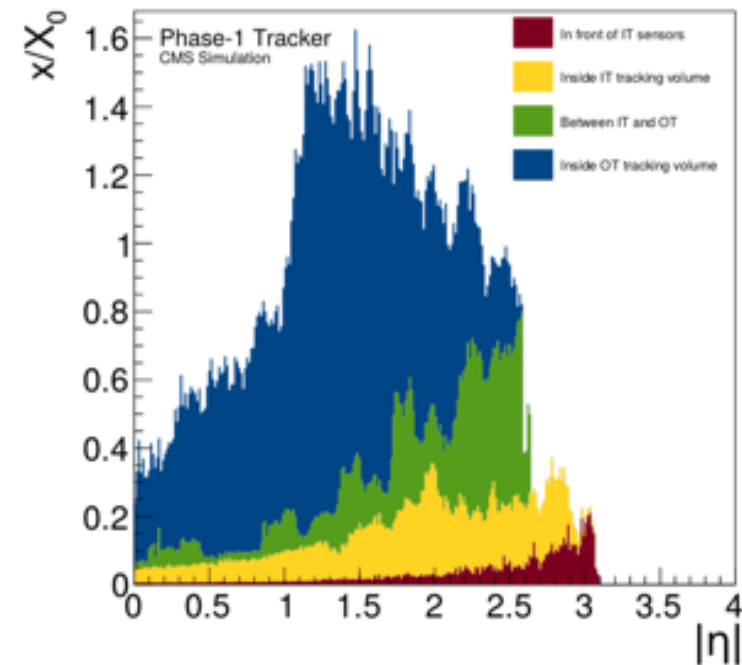
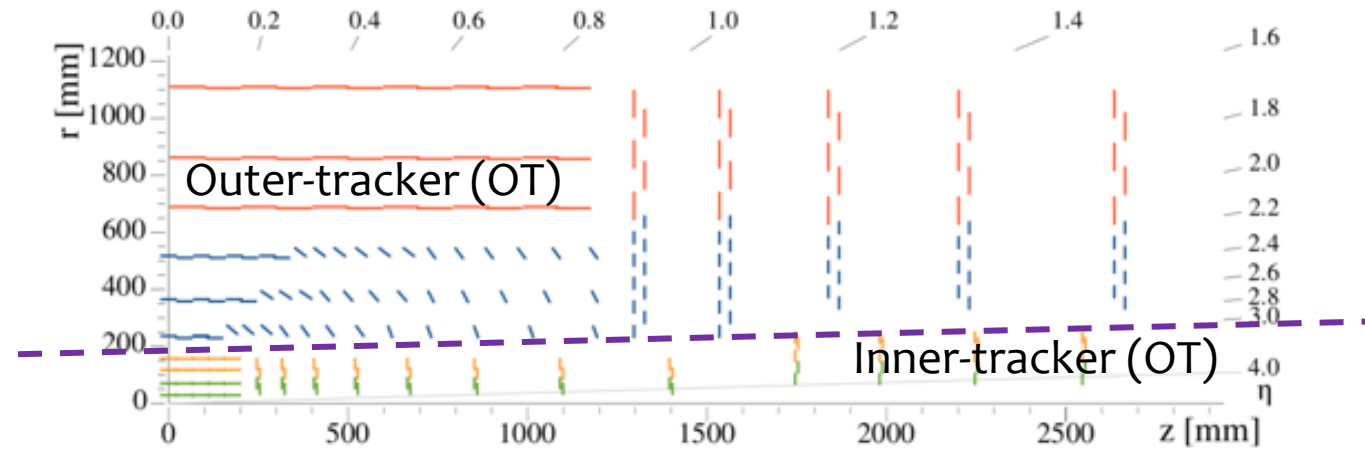
MIP Timing Detector [CMS-TDR-020]

- Precision timing for PU mitigation
- Barrel layer: Crystals + SiPMs
- Endcap layer: Low Gain Avalanche Diodes



Phase-2 Tracker Upgrade

- **Key features:**
 - More granularity
 - Lower material budget
 - Extended coverage
 - Tracking included in L1 Trigger for the first time



Outer Tracker: p_T module

Introducing tracking at L1 trigger

Local rejection of low- p_T tracks

Exploit bending of charged particle tracks in CMS'4T B-field

Correlate hits from 2 closely spaced sensors to form stubs compatible with a track $p_T > 2\text{GeV}$

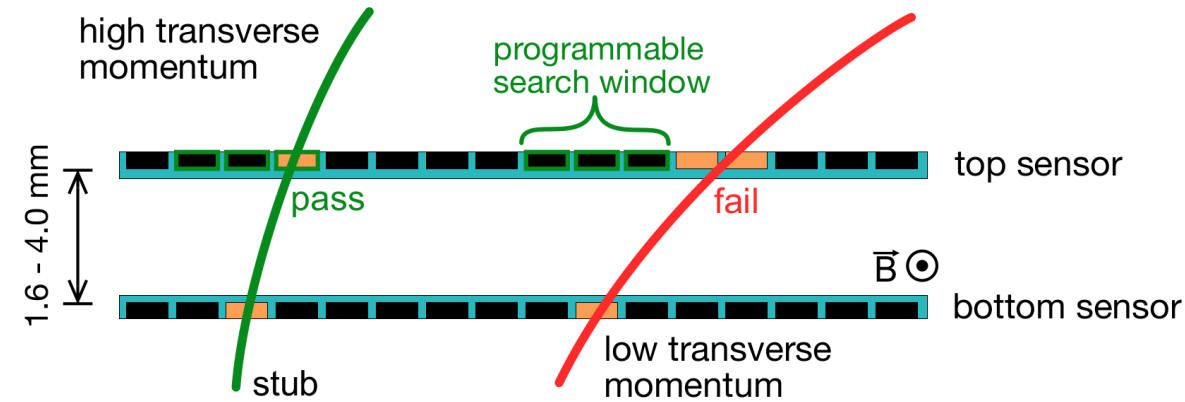
Tuneable offset and window for homogeneous p_T threshold throughout the Outer Tracker

Tracker input to the L1 trigger

Stub information is sent out at BX frequency of 40 MHz

Two data streams: trigger information and hit data

Full data read-out at ~ 750 kHz



2S module

2 strip sensors with 2×10^{16} channels each
 $10 \times 10 \text{ cm}^2$ sensors with 5 cm long strips
90 μm pitch, sensors spaced at 1.8 and 4 mm
Radii > 60 cm

The 3D rendering shows a yellow rectangular module with orange borders. A red line indicates the 'Coarse z coordinate'.

PS module

Sandwich of 1 strip and 1 macro-pixel sensor
 $\sim 10 \times 5 \text{ cm}^2$ sensitive area, 2×960 strips with 100 μm pitch
 $1467 \times 100 \mu\text{m}^2$ pixels, sensor spacings 1.6, 2.6 and 4 mm

The 3D rendering shows a yellow rectangular module with orange borders. A blue line indicates the 'Accurate z coordinate'.

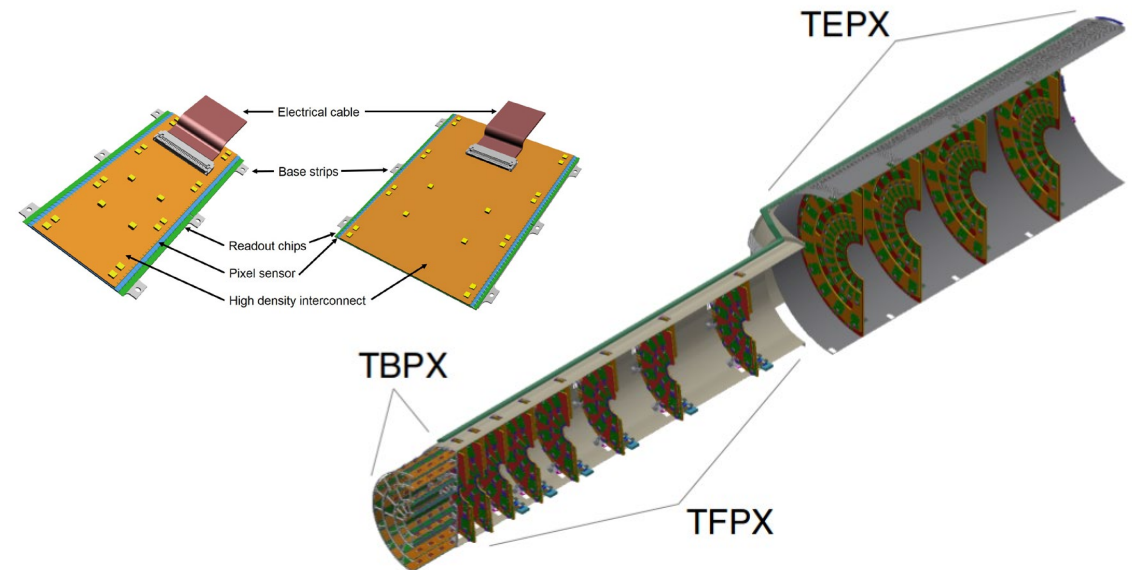
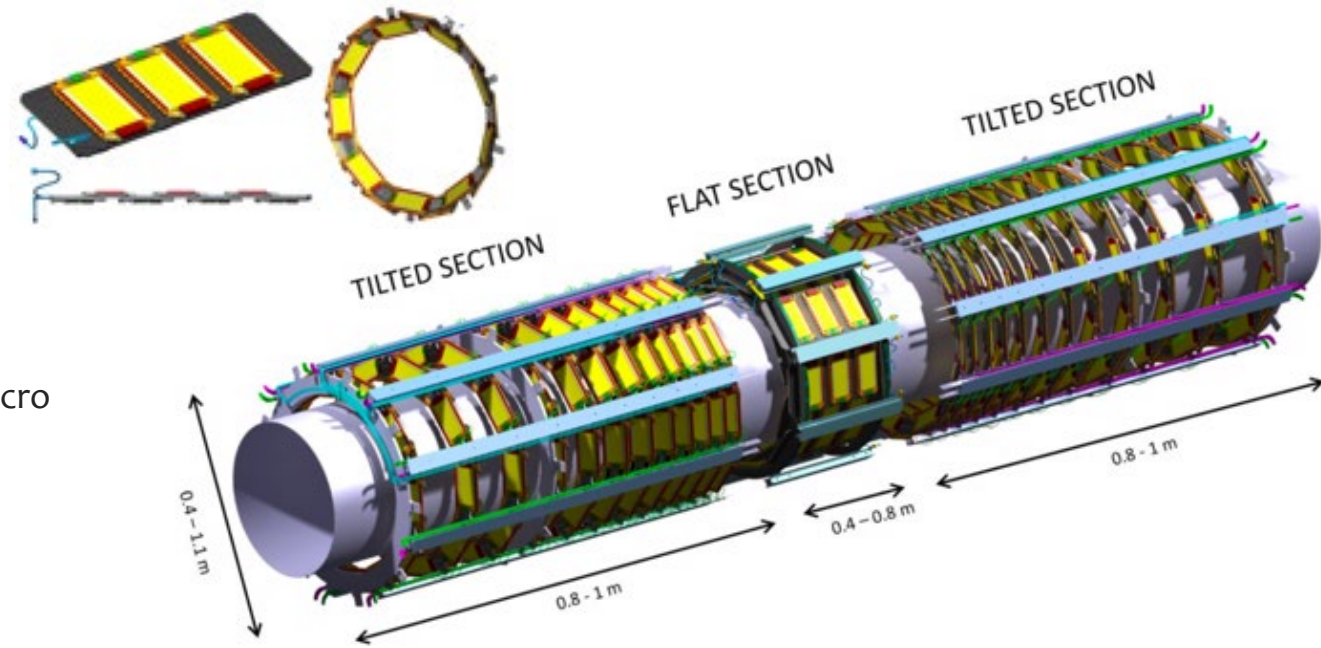
Future Layout

- **Outer tracker:**

- Layout: 6 barrel layers, 5 discs per endcap
- 9.5 million channels:
 - $\sim 200 \text{ m}^2$ of active silicon sensors \rightarrow 44M strips and 174M macro pixels ($r < 60 \text{ cm}$)
- Vastly **reducing** material:
 - light-weight mechanics and modules & improved routing of services
 - tilted barrel section

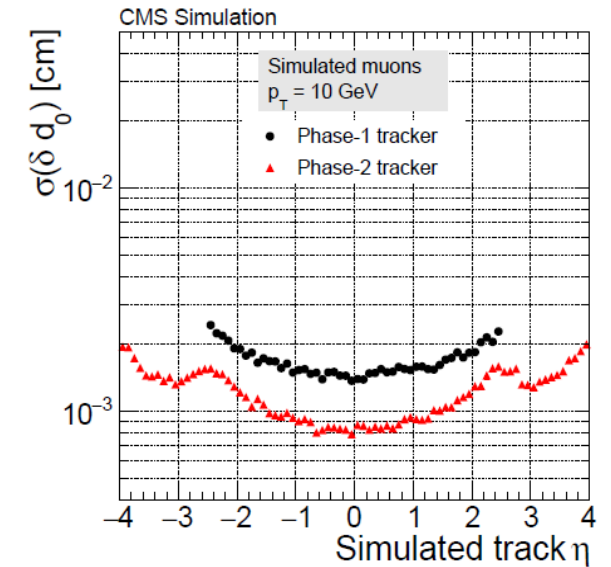
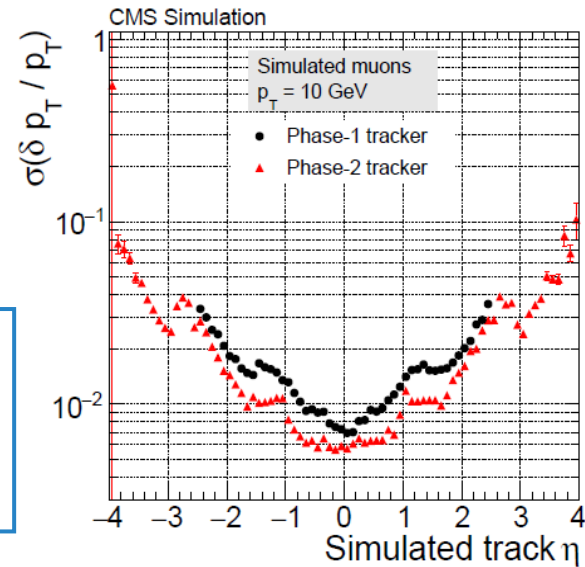
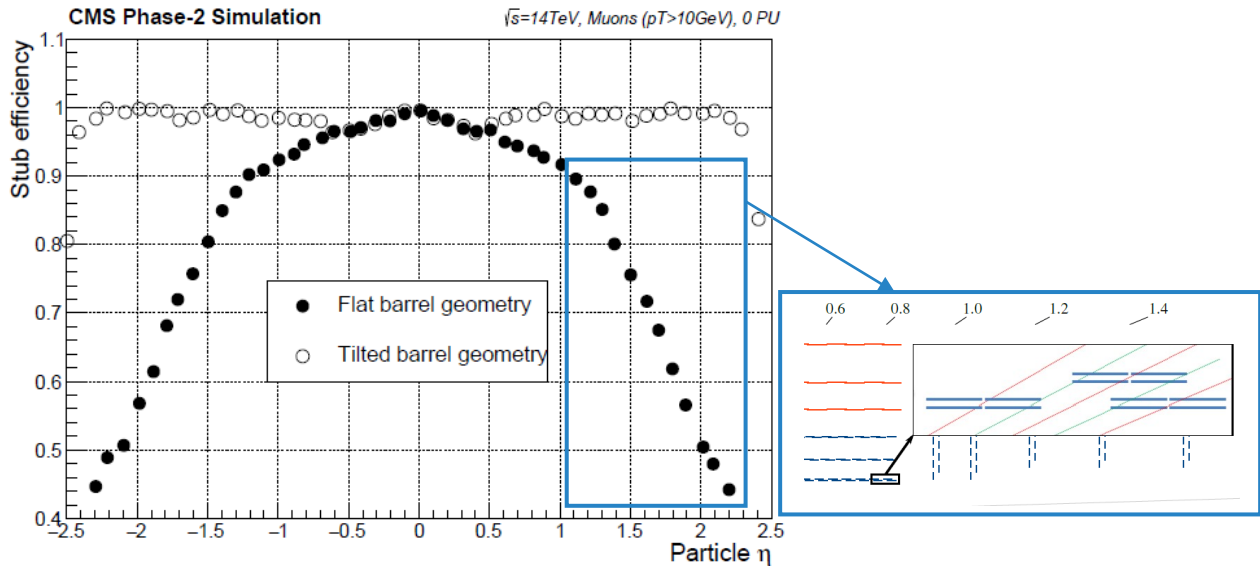
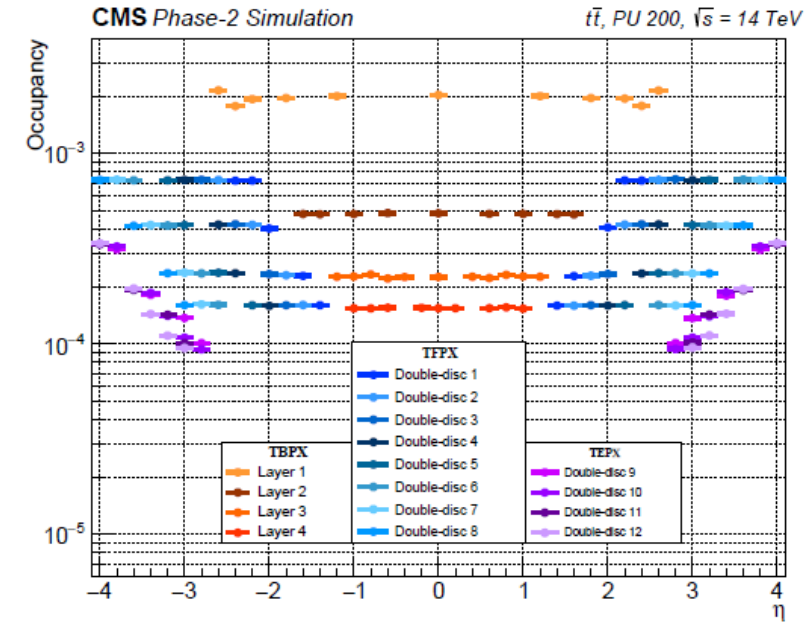
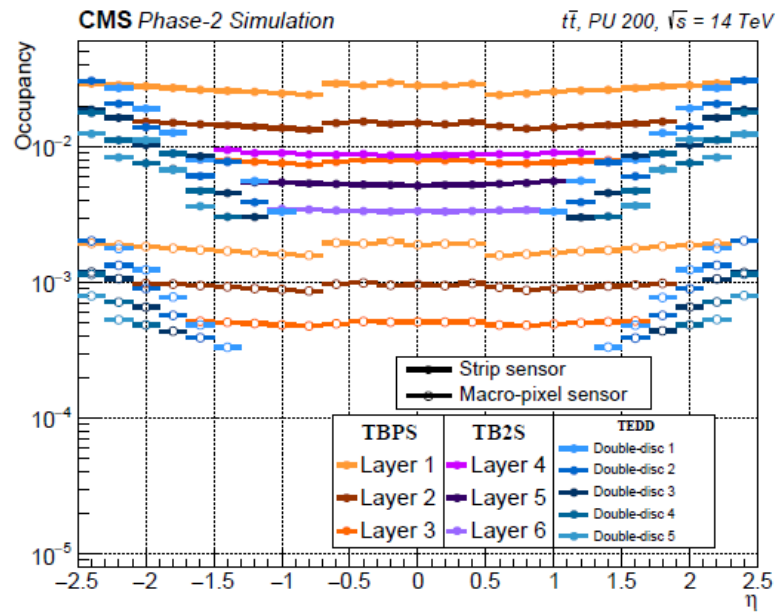
- **Inner tracker:**

- Increased granularity with occupancy at per mille level: pixel size $\sim 25 \times 100 \mu\text{m}^2$ or $50 \times 50 \mu\text{m}^2$
- Coverage up to $\eta \sim 4$, with $\sim 4.9 \text{ m}^2$ active area
- Layout: 4 barrel layers, 8 small disks, 4 large discs per side
- Mechanics and support: simple structure for **easy installation and removal** \rightarrow potential replacement of inefficient parts possible!



Expected performance

- Occupancy will not exceed 3%
- Resolution:
 - Momentum resolution deteriorates at higher η due to shorter lever arm
 - Transverse impact parameter, below $10\mu\text{m}$ in centre, $20\mu\text{m}$ at edge



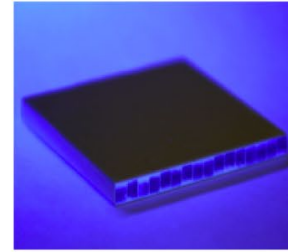
MIP Timing Detector

An overview

- Thin layer between tracker and calorimeters
 - Hermetic coverage for $|\eta| < 3.0$
- MTD will **feature**:
 - Time resolution of 30-50 ps for MIPs
 - 4D vertex reconstruction
- **Technology**:
 - Well-established technologies will be used
 - LYSO crystals with dual end SiPM readout in barrel
 - Low Gain Avalanche Detectors (LGAD) for the endcap

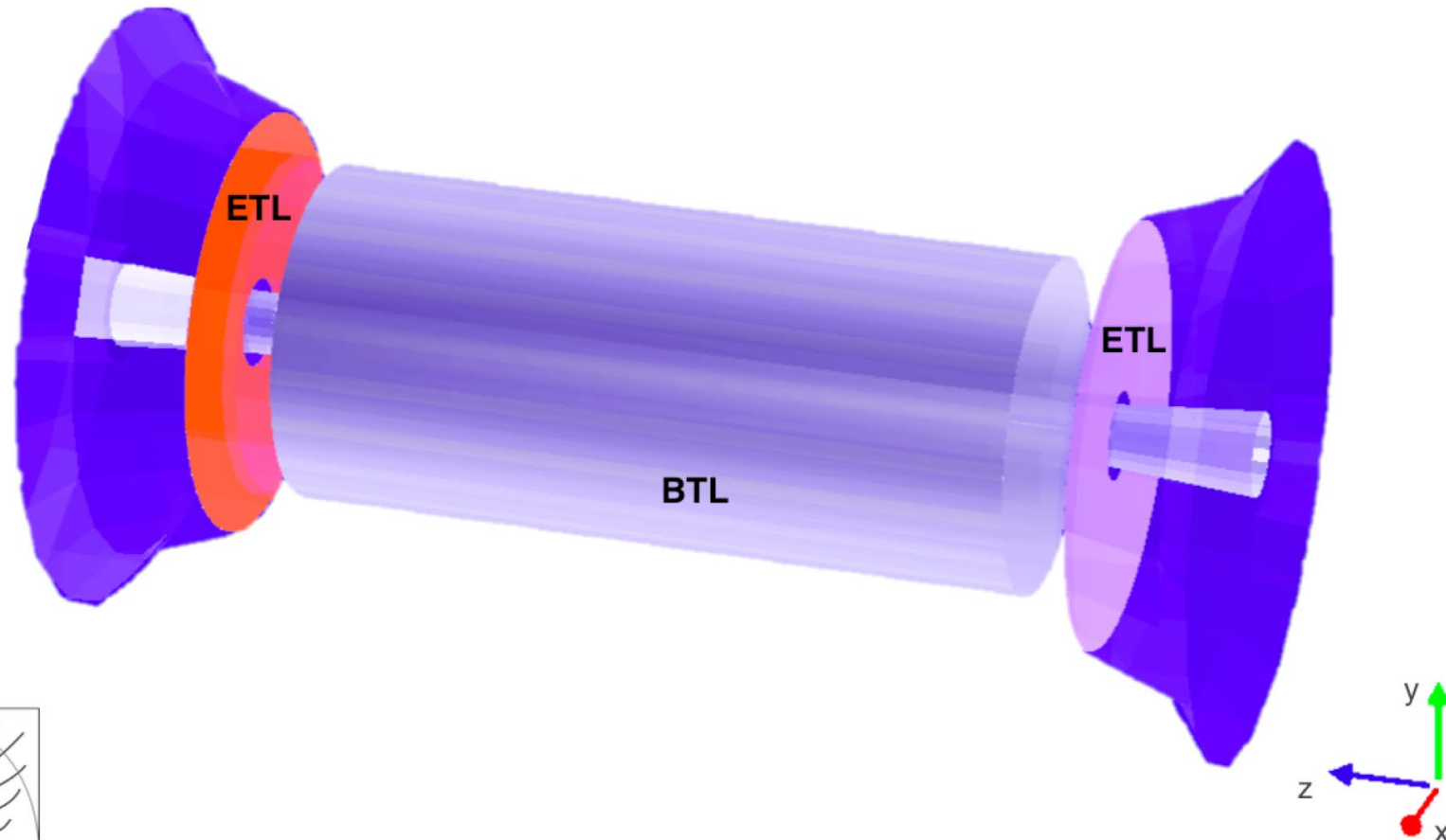
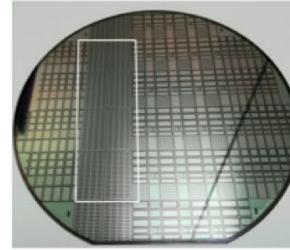
BTL: LYSO bars + SiPM readout:

- TK / ECAL interface: $|\eta| < 1.45$
- Inner radius: 1148 mm (40 mm thick)
- Length: ± 2.6 m along z
- Surface ~ 38 m²; 332k channels
- Fluence at 4 ab⁻¹: 2×10^{14} n_{eq}/cm²



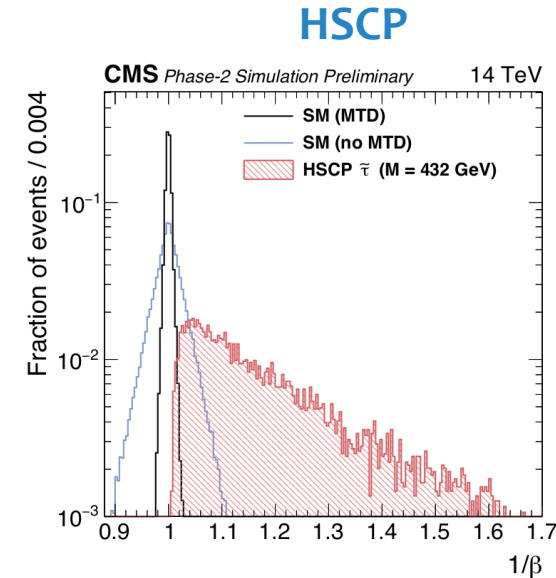
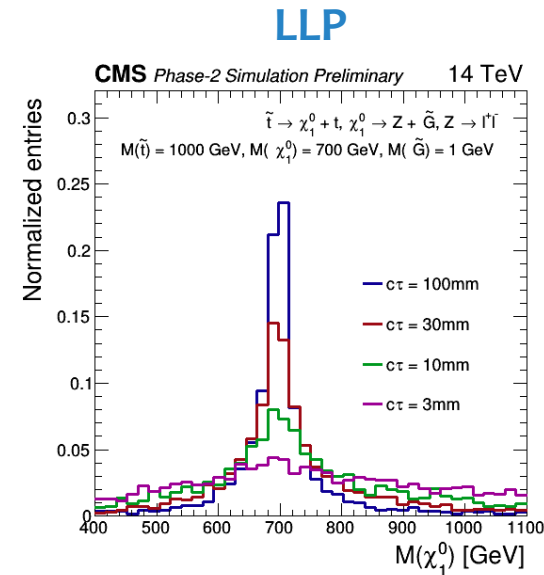
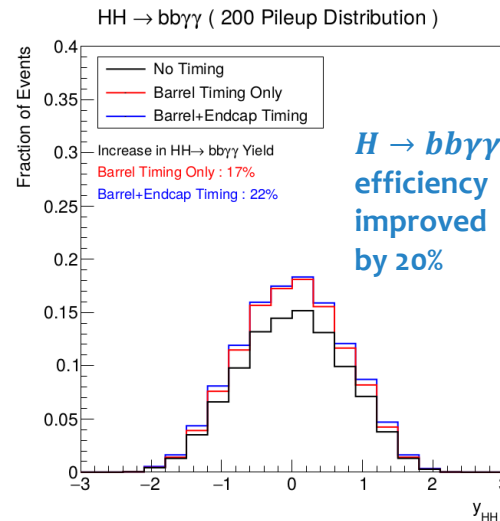
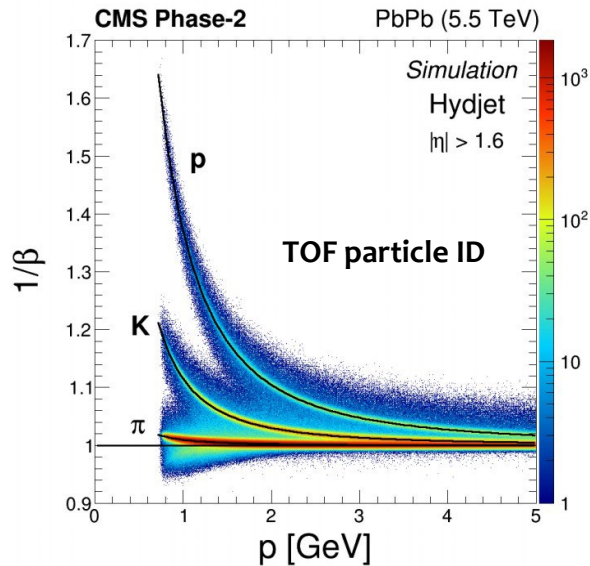
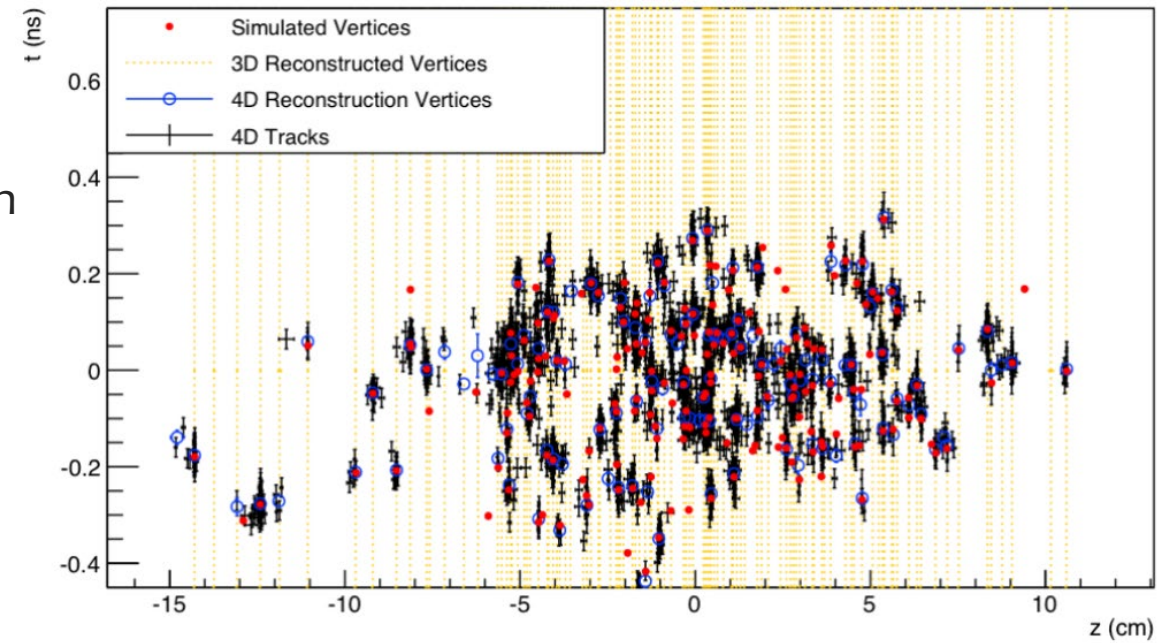
ETL: Si with internal gain (LGAD):

- On the CE nose: $1.6 < |\eta| < 3.0$
- Radius: $315 < R < 1200$ mm
- Position in z: ± 3.0 m (45 mm thick)
- Surface ~ 14 m²; ~ 8.5 M channels
- Fluence at 4 ab⁻¹: up to 2×10^{15} n_{eq}/cm²



MIP Timing Detector

- **Time information** improves the quality of the reconstruction of physics objects.
 - Track time association allows to remove **spurious pile-up tracks** from reconstruction,
 - Impact on fake jet reconstruction, lepton isolation and ID, b-tagging, p_T^{miss} resolution.
 - Also adding the possibility to perform time-of-flight particle identification
- Impact on the reach of physics analysis: both SM and BSM



High-Granularity Calorimeter (HGCAL): design parameters

- **Key Parameters:**

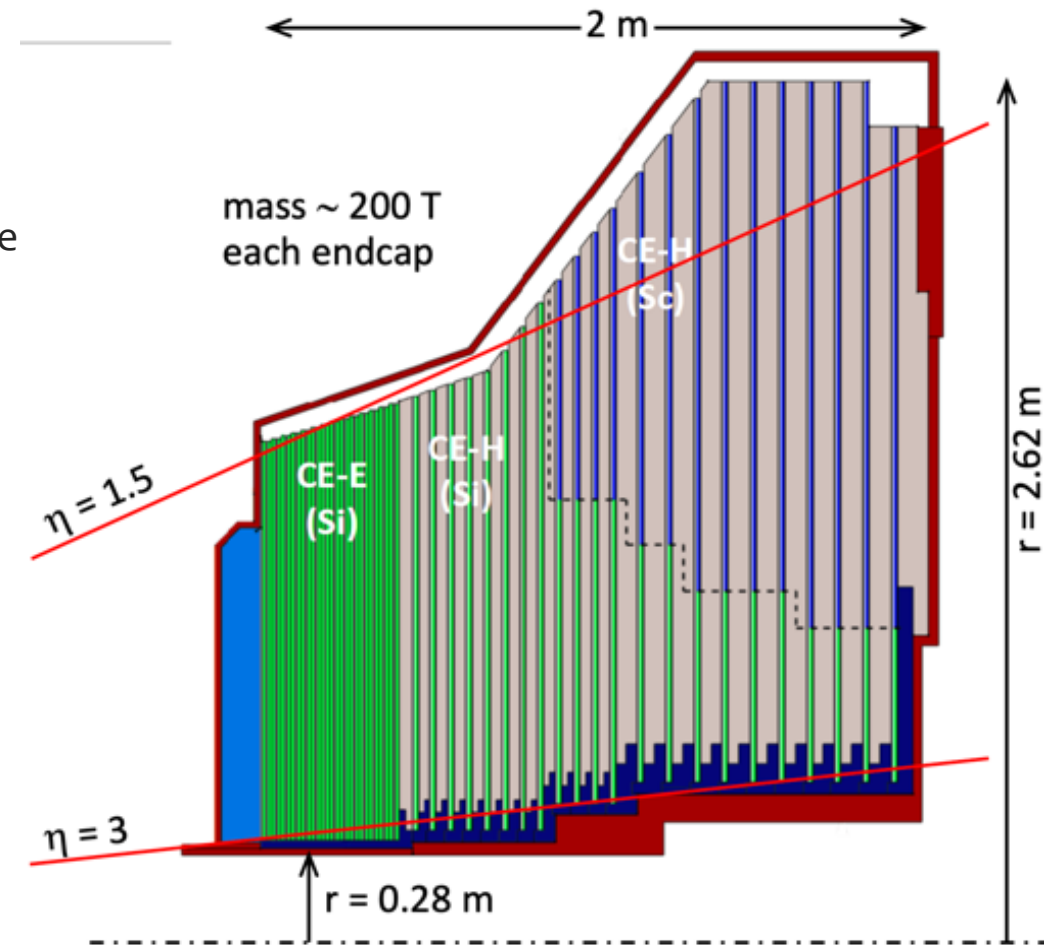
- The HGCAL covers $1.5 < |\eta| < 3.0$
- 215 ton/endcap, full system at -30C
- 620 m² of Si sensors in 30k modules: 6M Si channels, 0.5 or 1 cm² cell size
- 400 m² of scintillator in 4k boards: 240k scintillator chan., 4-30 cm² cell size

- **Active Elements:**

- Hexagonal modules based on Si sensors in CE-E and high-radiation regions of CE-H
- Scintillating tiles with SiPM readout in low-radiation regions of CE-H
- "Cassettes": multiple modules mounted on cooling plates with electronics and absorbers

- **Detector Configuration:**

- Electromagnetic calorimeter (CE-E) : Si, Cu/CuW/Pb absorbers; 28 layers, 25.5λ and 1.7λ
- Hadronic calorimeter (CE-H) : Si & scintillator, steel absorbers; 22 layers and $\sim 9.5 \lambda$ (including CE-E)



CMS HGCAL: Pattern Recognition and Reconstruction

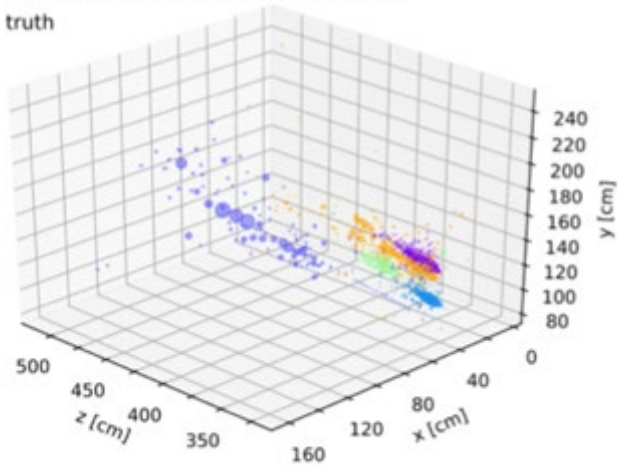
ML exploration: Dynamic Graph Networks

- Using graph neural networks for reconstruction
arxiv:1801.07829v2 
- Developed dedicated dynamic space transformation networks:
GravNet arxiv:1902.07987 

Edge Classifier Results:

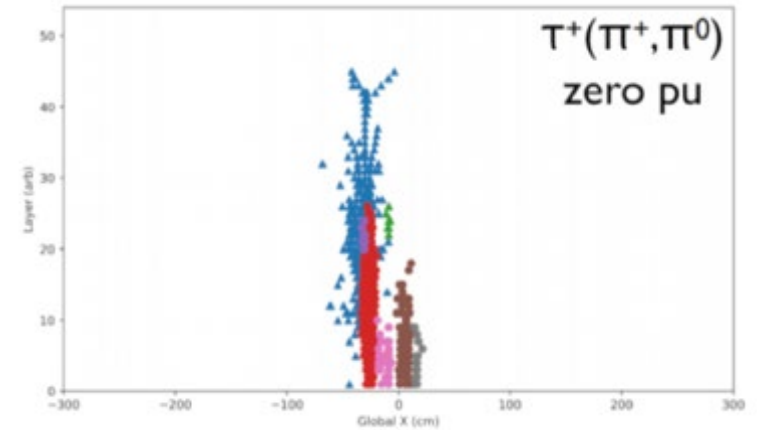
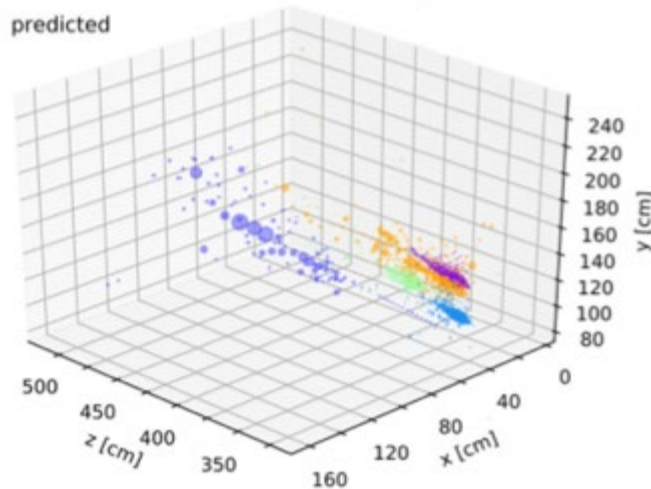
- Edges are used to group together points of the same particle type:
 - Individual decay products of taus clearly separable
- Accurate separation of EM vs HAD energy across multiple clusters

CMS Phase-2 Simulation Preliminary
truth

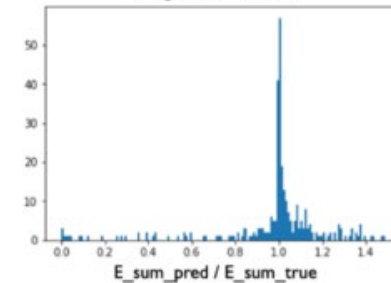


GravNet Results

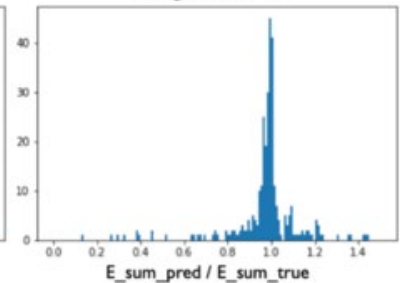
CMS Phase-2 Simulation Preliminary
predicted



Categorized as Hadron

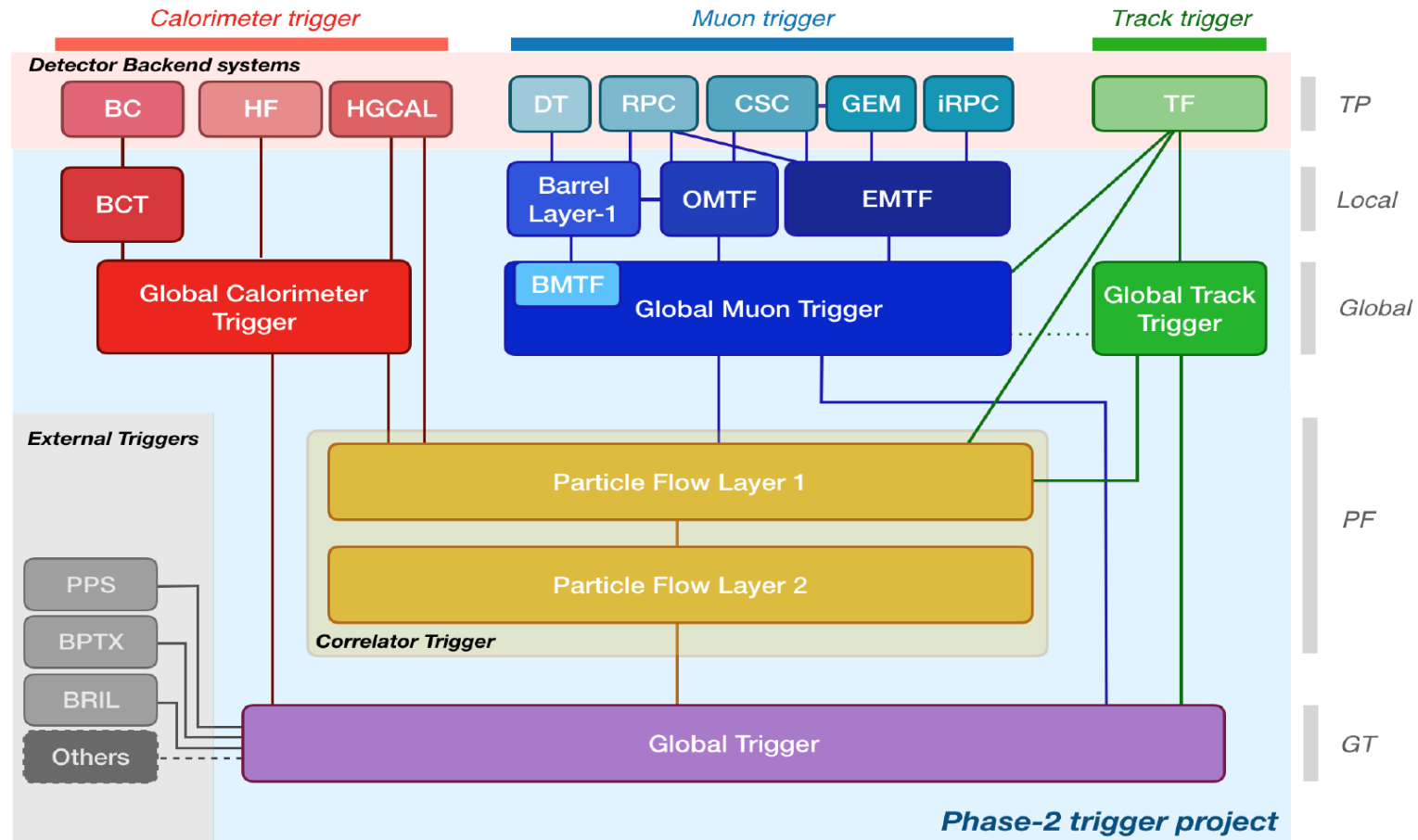


Categorized as EM



L1 trigger upgrade

- CMS will keep a 2-level triggering approach: L1 & HLT
- Key features of Phase2 Upgrade of Level-1 Trigger:
 - Increase bandwidth 100 kHz \rightarrow 750kHz
 - Increase latency 3.8 μ s \rightarrow 12.5 μ s
- Including info from tracker and HGAL
- **Sophisticated FPGA-based algorithms:** using particle-flow (PF) reconstruction techniques or Machine-Learning based approaches.
- Increased trigger acceptance, and physics sensitivity, while maintaining Run-2 thresholds.
- Scouting into HL-LHC data @ 40 MHz: storing only high-level information.



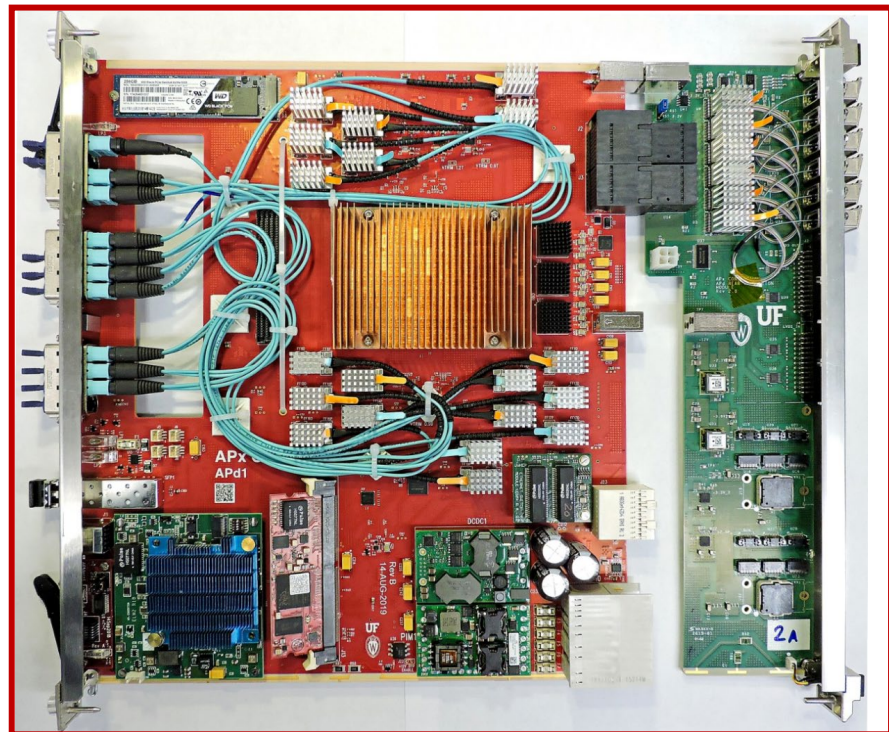
Four distinct and independent trigger processing paths: a calorimeter trigger, a muon trigger, a track trigger and a particle-flow (correlator) trigger

L1T: Hardware prototypes

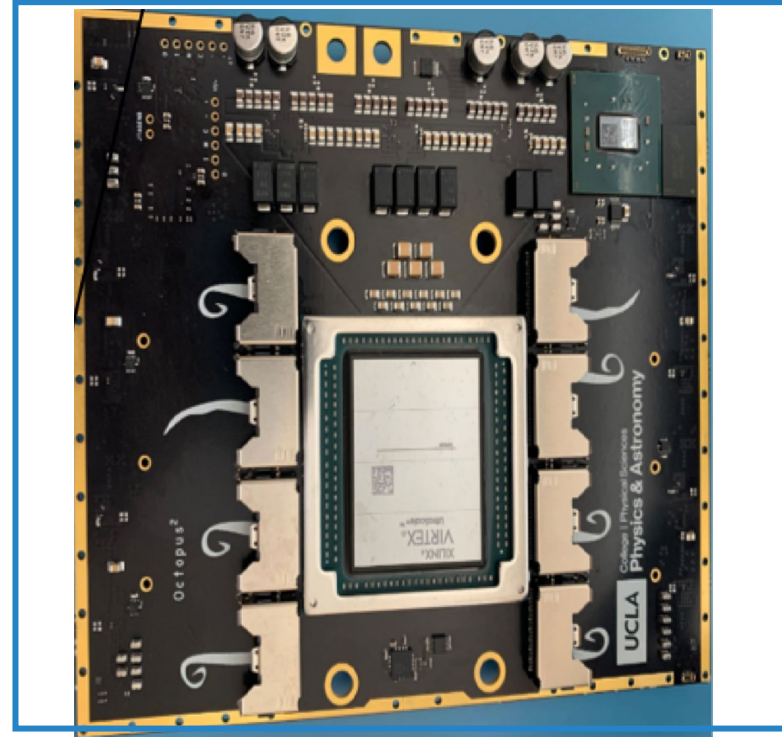
Design philosophy: Generic Processing Engines → I/O, FPGA

- FPGA : 1 or 2 Xilinx Virtex Ultrascale / Ultrascale+
- Optics : +100 high-speed. Samtec Firefly x4 /x12 flyover
- Processors on board running commercial linux for flexible configuration and monitoring

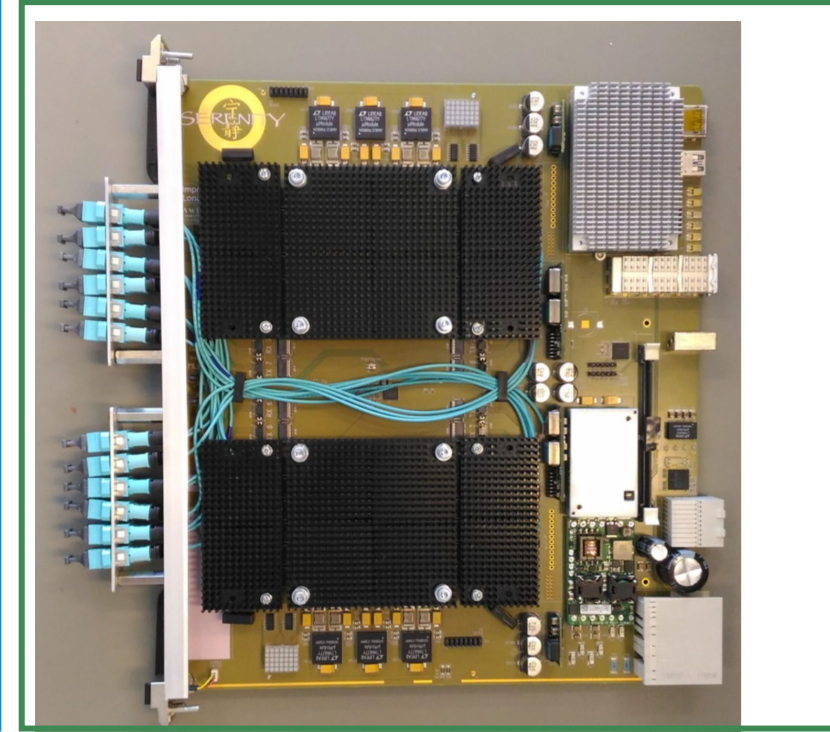
APx



X2O/Octopus (OCEAN prototype)

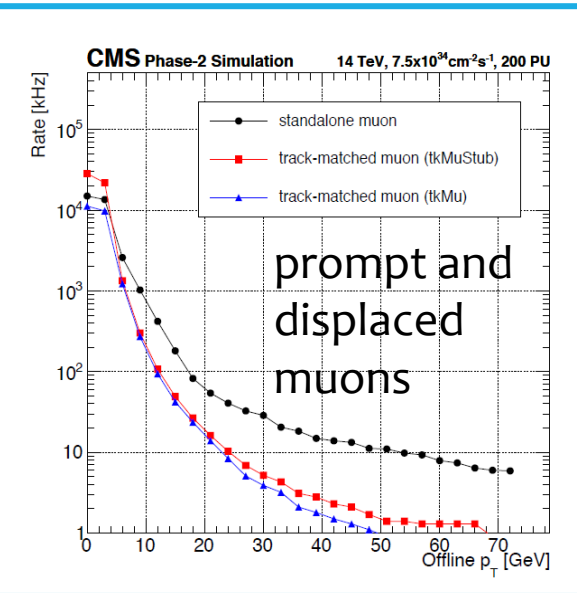
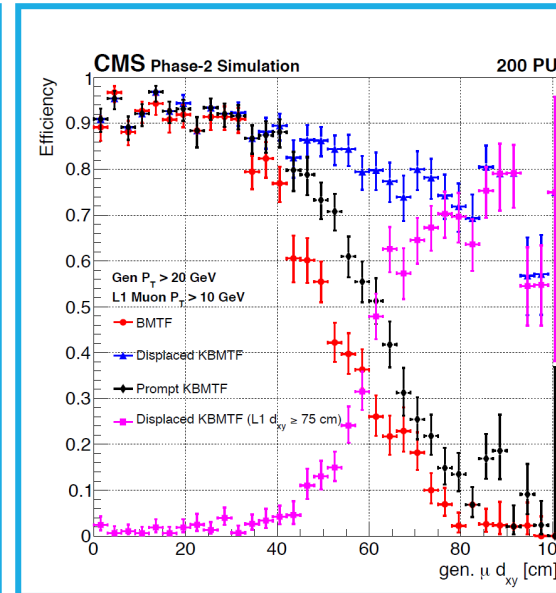
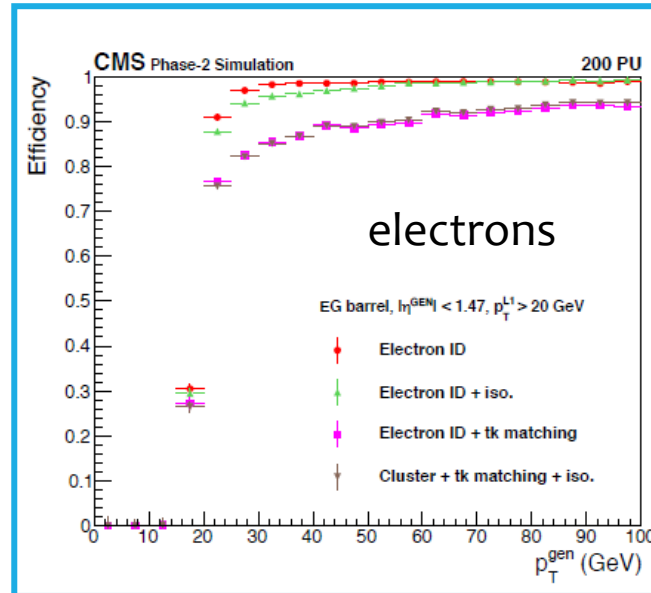
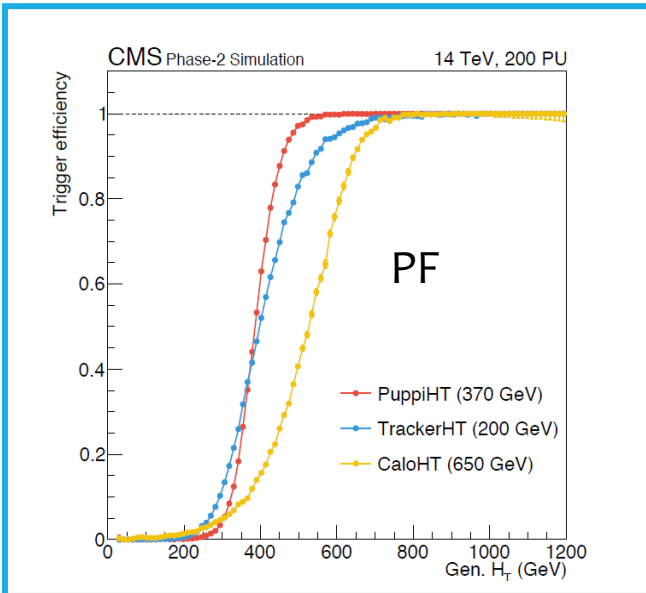
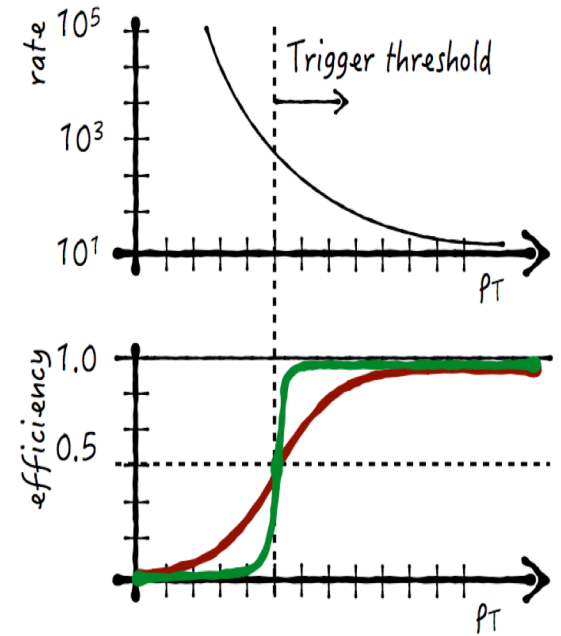


Serenity:



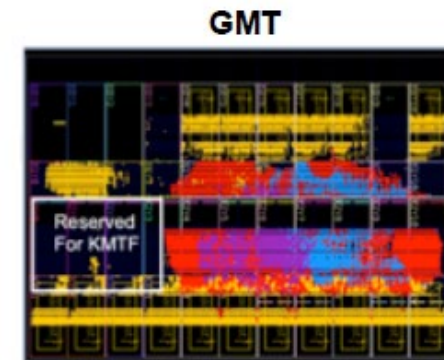
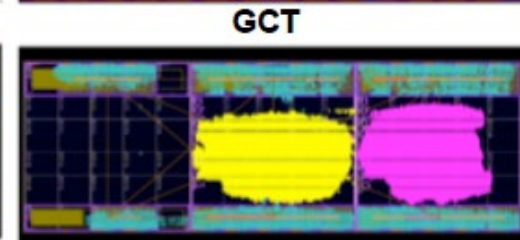
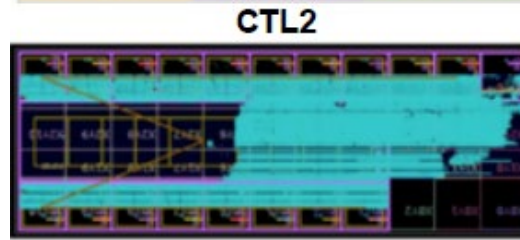
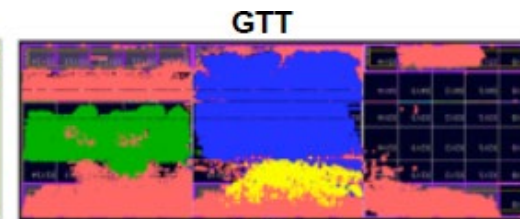
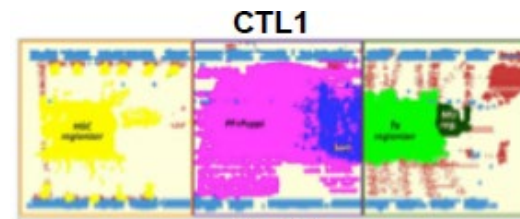
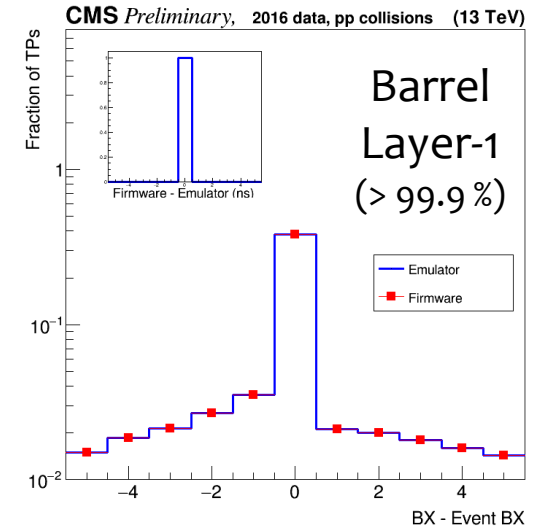
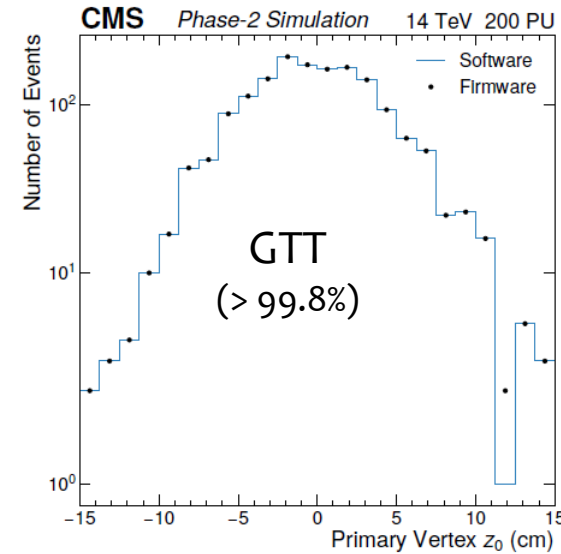
L1T: algorithms

- Extensive use of tracking to reach near offline performance (sharper efficiency turn-on curves) + reconstruction of Primary Vertex.
- Exploit complementarity of different object flavor:
 - Standalone objects: robust triggers based on independent sub-detectors
 - Track-matched objects: tracking used to confirm standalone objects, significant improvement with simple design
 - Particle-flow (PF) objects: ultimate performance improvement, combine all information to match offline algorithms, require most processing time and resources for calculation



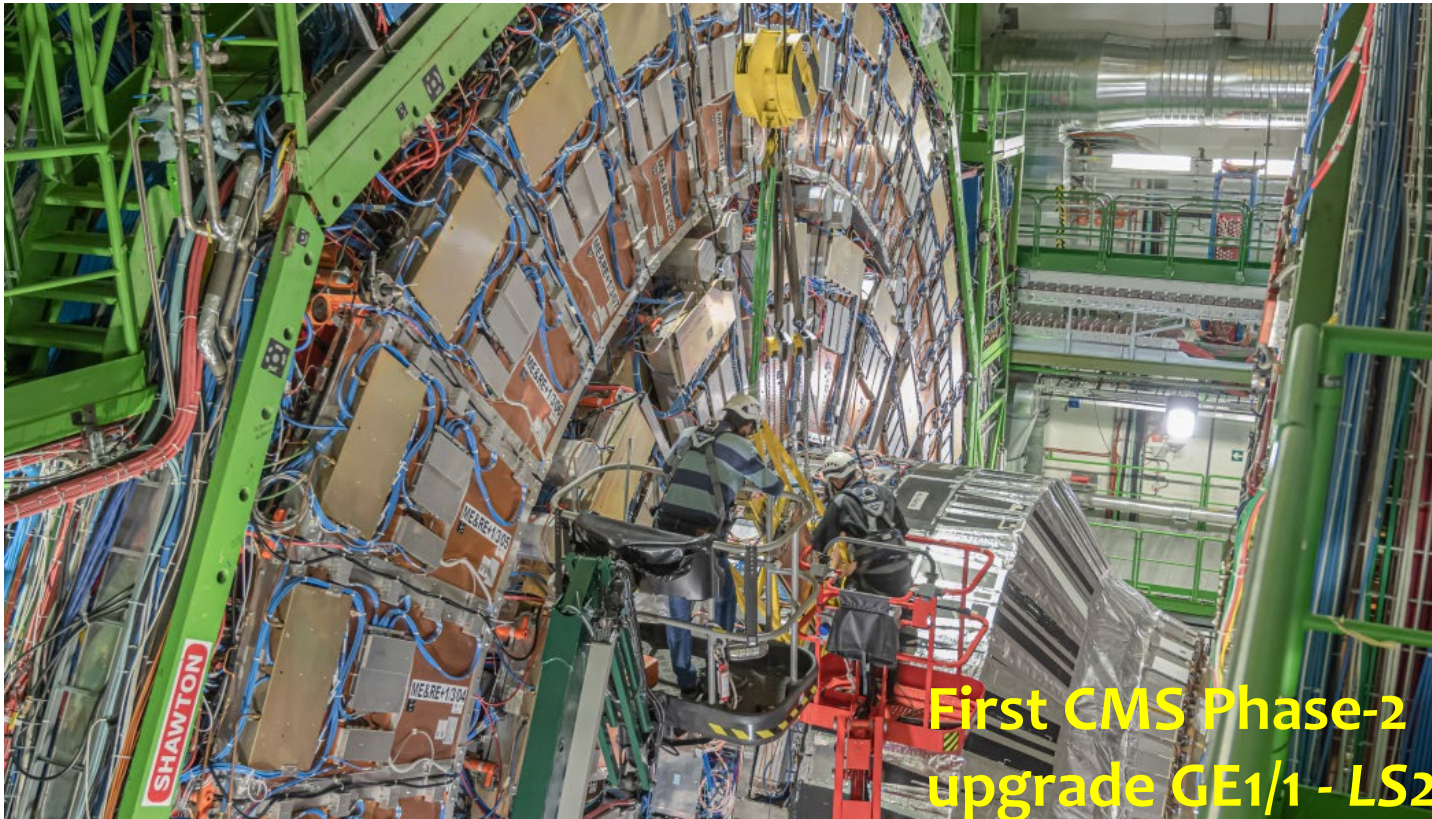
L1 trigger upgrade: testing and demonstrators

- **From SW to FW:**
 - Algorithm designed in C++ (with fixed-point precision arithmetic) and converted to HLS.
 - Timing, resource consumption and latency verified.
 - Close to 100% agreement between SW and FW implementation
- **System demonstrators:**
 - Single and multiple-board tests for every subsystem
 - Multiple sites, including a planned larger scale demonstrator @CERN.



Summary

- In response to the unprecedented challenges of the HL-LHC environment and physics program, the CMS Upgrade introduces new paradigms
 - such as p_T modules for L1 Trigger Tracking, High Granularity Calorimetry and Precision Timing for PU mitigation
- Breaks new ground in detector technology.



First CMS Phase-2
upgrade GE1/1 - LS2

- The CMS HL-LHC upgrade program is by necessity ambitious, and many challenges still lie ahead.
- This, together with the commitment and support of CERN and of all the Funding Agencies and Institutes, provide a solid footing for successful completion of the HL-LHC Upgrade program.

Thanks!



backup



LHC / HL-LHC Plan

CSC, GEM1/1

BCAL, Tracker, Muon barrel, MTD, HGAL, ME0

LHC

HL-LHC

Run 1

Run 2

Run 3

Run 4 - 5...

LS1

13 TeV

EYETS

LS2

13 - 14 TeV

EYETS

LS3

14 TeV

energy

7 TeV

8 TeV

splice consolidation
button collimators
R2E project

cryolimit
interaction
regions

Diodes Consolidation
LIU Installation
Civil Eng. P1-P5

inner triplet
radiation limit

HL-LHC
installation

2011

2012

2013

2014

2015

2016

2017

2018

2019

2020

2021

2022

2023

2024

2025

2026

2027

2040

experiment
beam pipes

nominal Lumi

2 x nominal Lumi

ATLAS - CMS
upgrade phase 1

ALICE - LHCb
upgrade

2 x nominal Lumi

ATLAS - CMS
HL upgrade

5 to 7.5 x nominal Lumi

75% nominal Lumi

30 fb⁻¹

190 fb⁻¹

350 fb⁻¹

integrated
luminosity
3000 fb⁻¹
4000 fb⁻¹

HL-LHC TECHNICAL EQUIPMENT:

DESIGN STUDY



PROTOTYPES

CONSTRUCTION

INSTALLATION & COMM.

PHYSICS

HL-LHC CIVIL ENGINEERING:

DEFINITION

EXCAVATION

BUILDINGS

GEM2/1, RPC



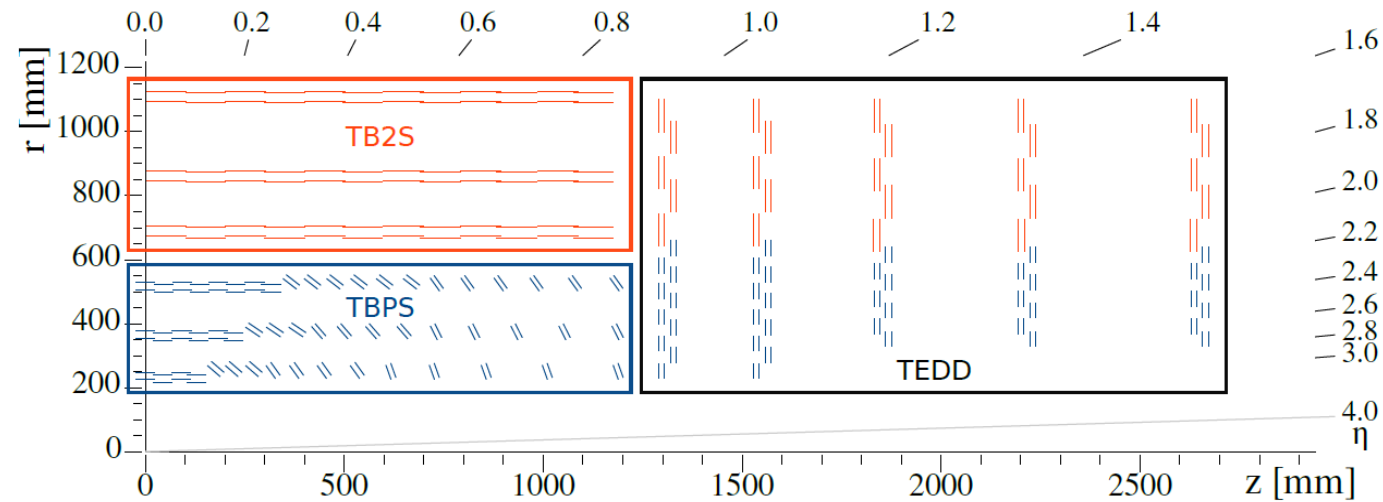
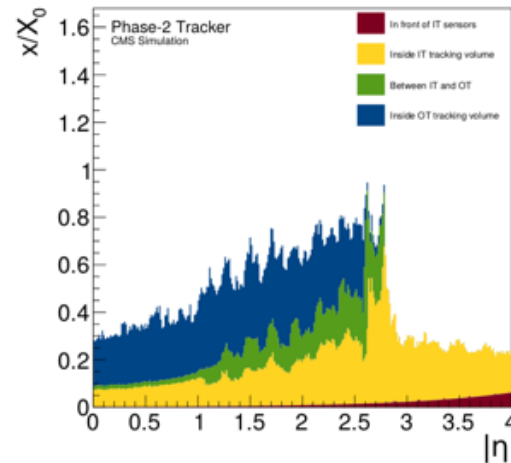
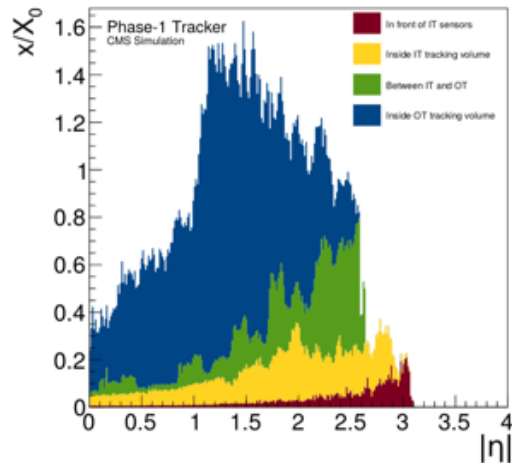
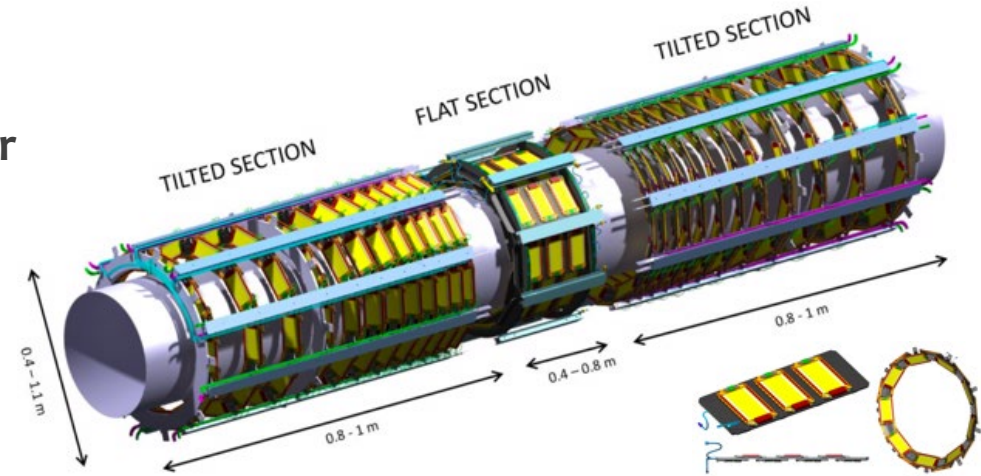
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Future Layout

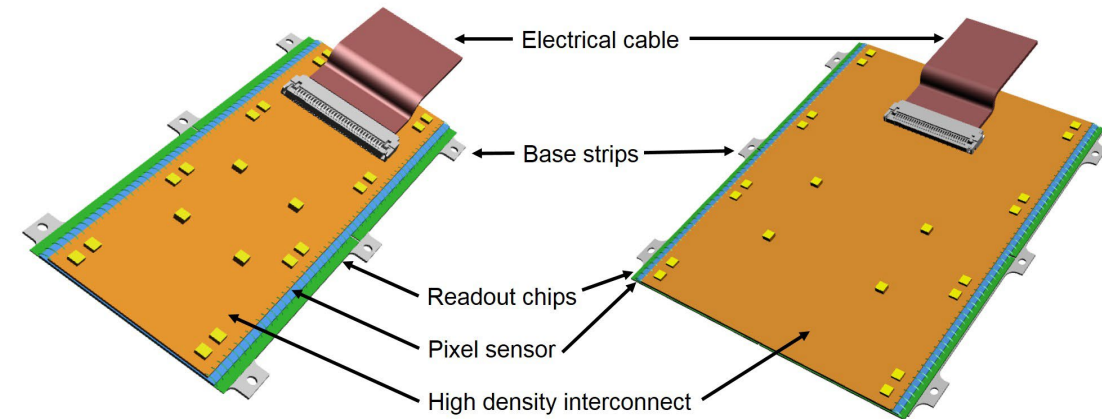
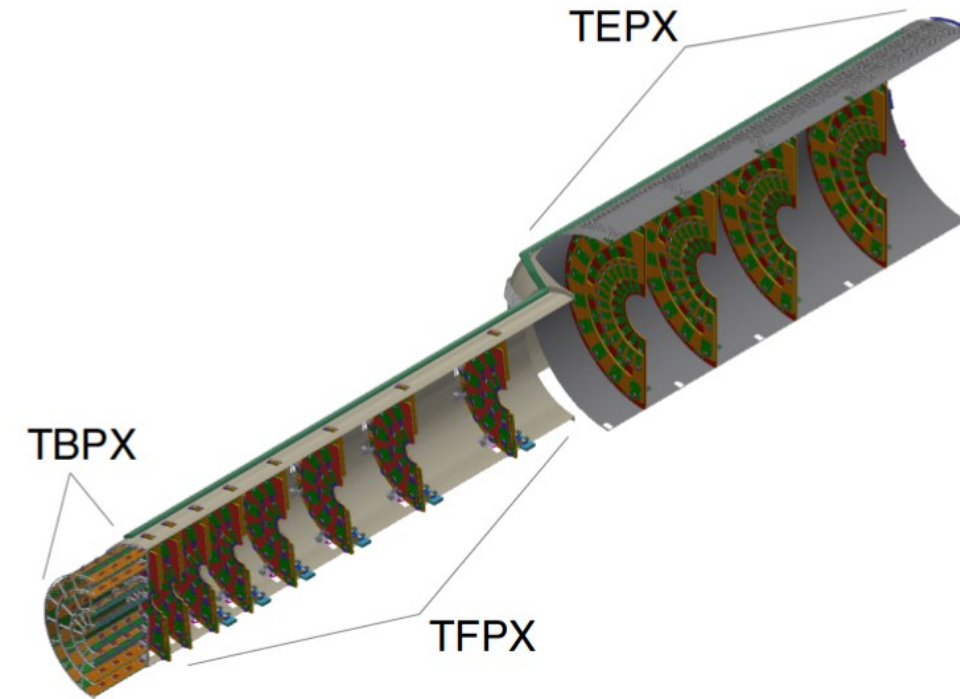
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 - 5 discs per endcap
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 - ~ 200 m² of active silicon sensors
 - 44M strips
 - 174M macro pixels (r < 60 cm)
- ... while vastly reducing mater
 - light-weight mechanics and modules
 - improved routing of services
 - tilted barrel section



Inner tracker

- **Radiation levels at HL-LHC:**
 - Ionizing radiation: 1.2 Grad
 - Hadron fluence: $2.3 \times 10^{16} \text{ n}_{\text{eq}} \text{ cm}^{-2}$
 - Up to 3 GHz/cm² hit rate
- **Concept and design:**
 - Increased granularity with occupancy at per mille level: pixel size $\sim 25 \times 100 \mu\text{m}^2$ or $50 \times 50 \mu\text{m}^2$
 - Coverage up to $\eta \sim 4$, with $\sim 4.9 \text{ m}^2$ active area
 - Layout: 4 barrel layers, 8 small disks, 4 large discs per side
 - capability to contribute to the real-time lumi measurement.
- **Mechanics and support:**
 - Simple structure for easy installation and removal \rightarrow potential replacement of inefficient parts possible!
- **Two module variants:**
 - 2 ROCs (1x2) or 4 ROCs (2x2)
 - Flex circuit, glued to sensors, wire-bonded to ROCs



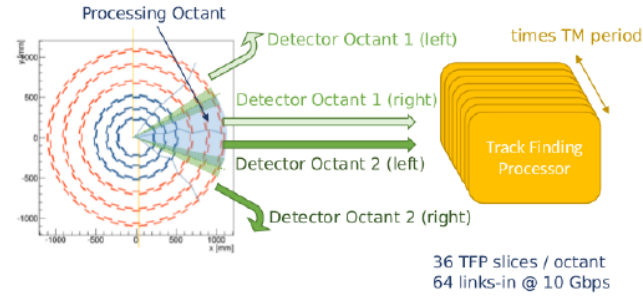
L1 tracks

- **R&D activities**

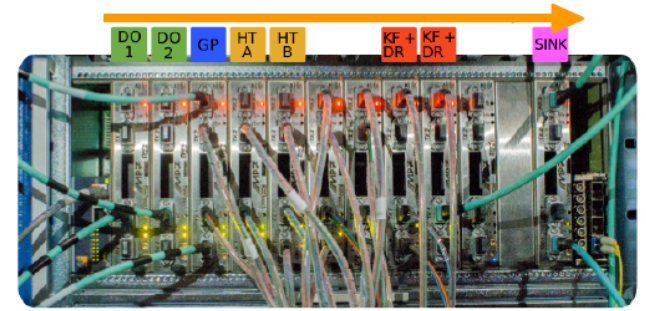
- Geometric processor
- Hough transformation on the FPGA
- Duplicate removal
- R&D on TF prototype boards
- Thermal performance
- Improvements to the architecture

- **Production**

- Assembly of a large fraction of boards
- Commissioning

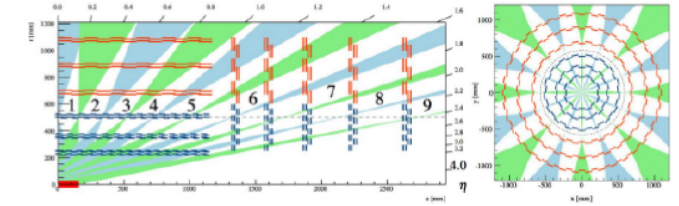


FPGA-based Hardware Demonstrator



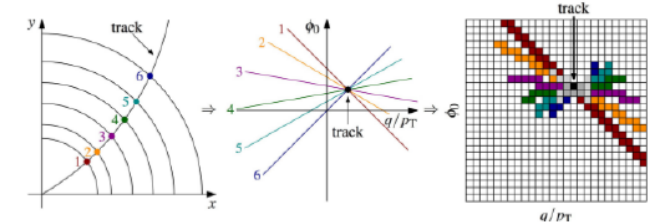
Geometric Processor GP

Processes stub data, sub-divides octant into 36 sub-sectors



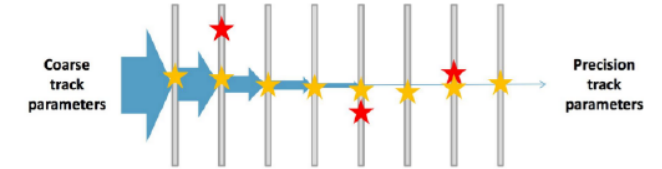
Hough Transformation HT

Track finder, identifies groups of stubs consistent with a track in $r-\phi$



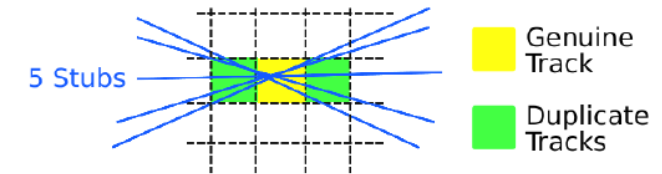
Kalman Filter KF

Candidate cleaning and precision fitting



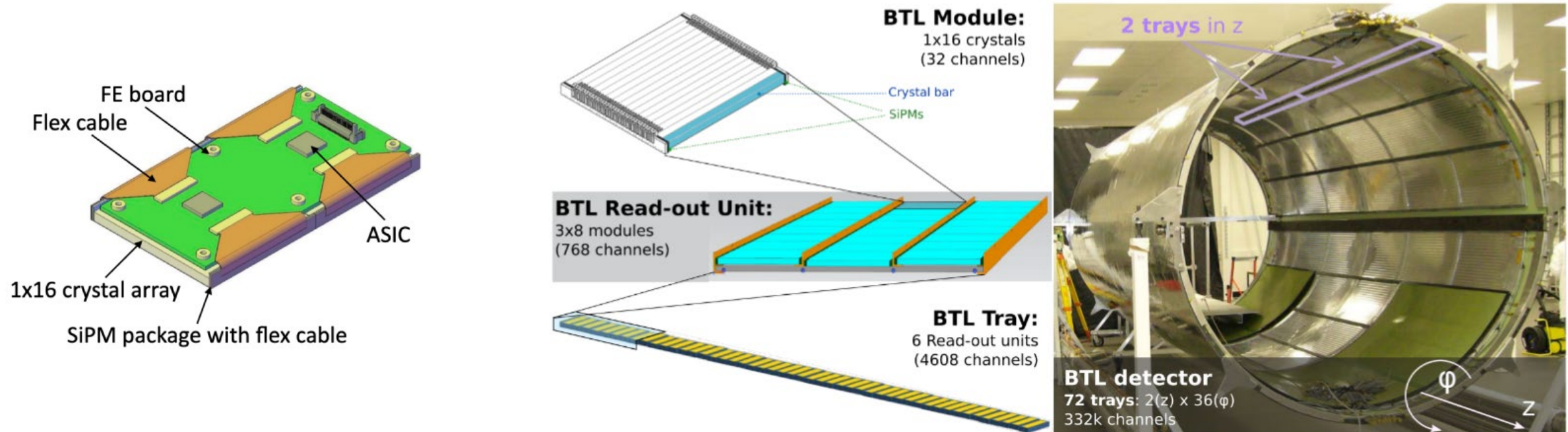
Duplicate Removal DR

Uses fit information to remove duplicate tracks generated by the HT



Data flow

The Barrel Timing Layer: Overview



Key features:

- LYSO crystals with dual end SiPM readout
- Basic unit : 1x16 array of crystals ($\sim 3 \times 3 \times 57 \text{ mm}^3$)
- Arranged in trays and segmented in readout units
- Coverage of $|\eta| < 1.45$, surface $\sim 38 \text{ m}^2$, 332 k channels
- Nominal fluence : $1.9 \times 10^{14} n_{\text{eq}}/\text{cm}^2$

Recent Progress:

- LYSO qualification and testing
- SiPM characterization and procurement
- ASIC and FE prototyping and testing.
- System tests with FE/BE integration.
- Mechanics design.

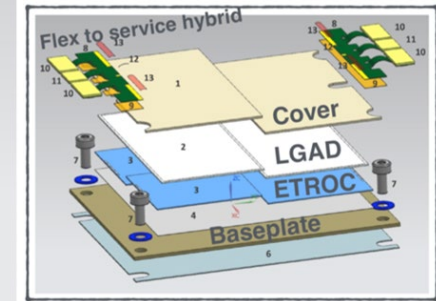
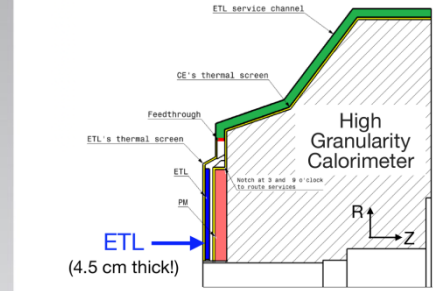
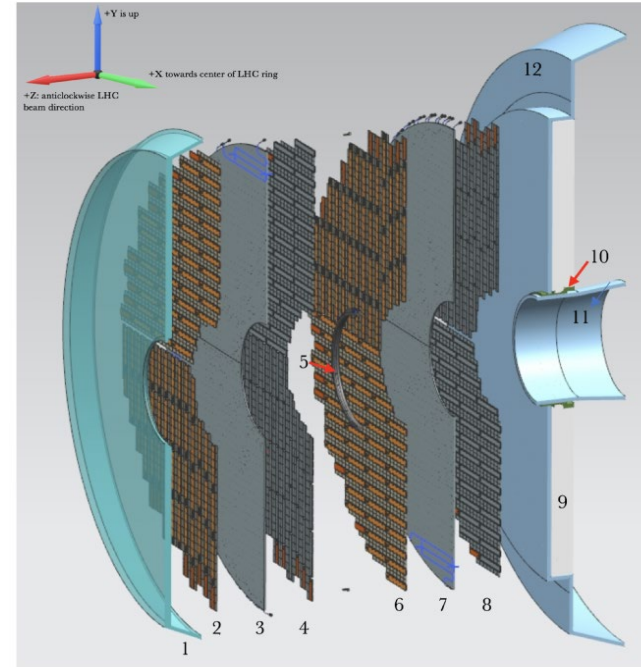
The Endcap Timing Layer: Overview

- **Key features:**

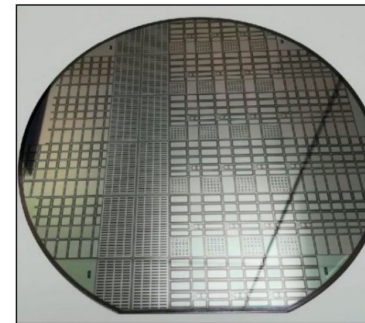
- LGAD detectors
- Basic unit : Module ($\sim 4 \times 6 \text{ cm}^2$)
 - The LGADs are bump bonded to ETROC ASICs mounted on two sides of cooling plates.
- Two disks per endcap mounted on the HGCal nose.
- Coverage of $1.6 < |\eta| < 3.0$, surface $\sim 14 \text{ m}^2$, 8.5 M channels
- Nominal fluence : $1.9 \times 10^{14} n_{\text{eq}}/\text{cm}^2$

- **Recent Progress:**

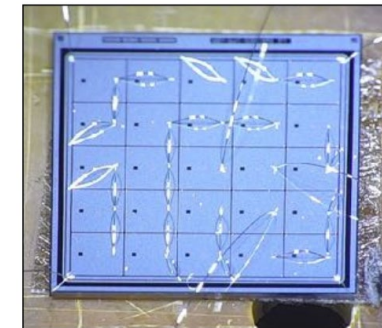
- LGAD characterization
- ASIC prototyping
- System and Mechanics design prototyping



FBK UFSD3



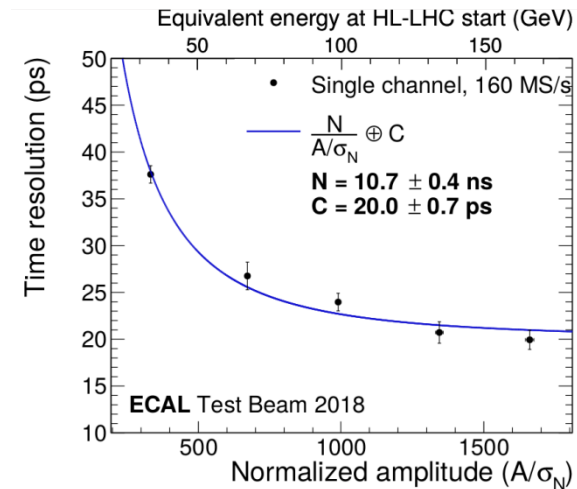
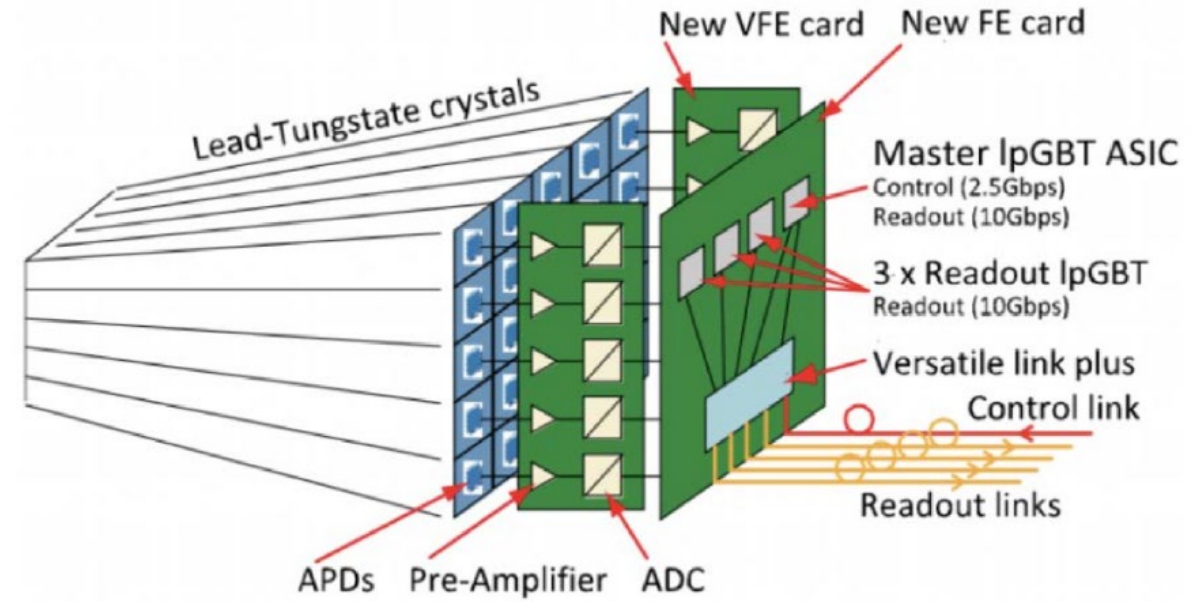
LGAD prototype sensors



5x5 array from HPK

Barrel Calorimeter (BCAL)

- **Replace all active on-detector electronics components (ECAL)**
 - Digitization at 160 MHz
 - On the fly pulse-shape discrimination o help discriminate against spikes
 - Oversampling - noise reduction, pile-up mitigation
 - Time resolution: 30 ps for $E > 50$ GeV
- **Streaming of all digitized data off-detector (using lossless compression)**
 - Lifts Phase-1 trigger latency bottleneck of $3.8 \mu\text{s}$
 - trigger primitive generation off-detector
 - Cell information - 5×5 crystals \rightarrow single crystal
- **Replace all off-detector electronics components (HCAL+ECAL – ATCA)**
- Reduce the operating temperature from 18°C to 9°C to mitigate APD ageing effects.



HGCAL: Active material

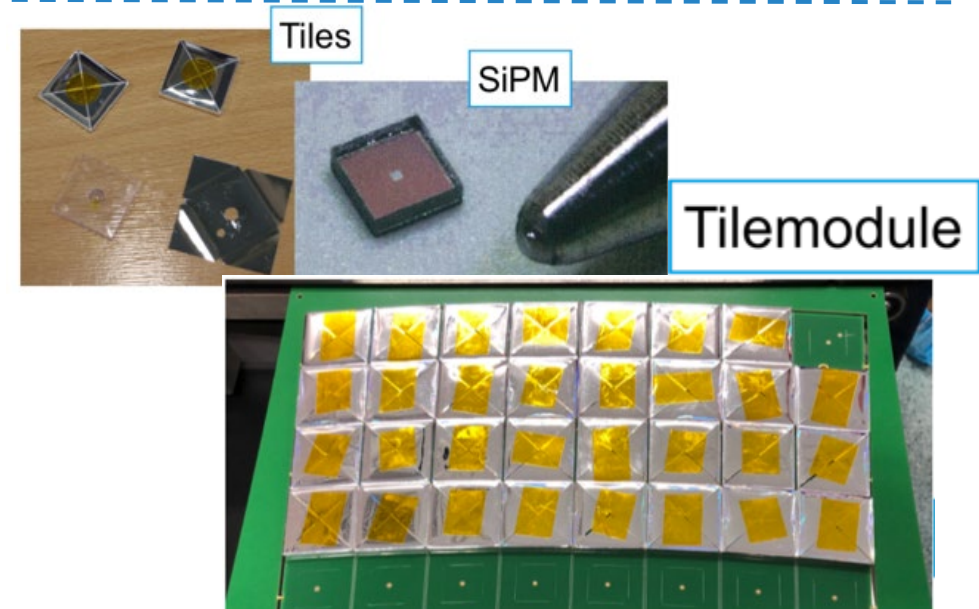
- **Silicon Sensors:**

- The Silicon sensors are first use of 8" technology for large-scale HEP Sensors
 - Hexagonal geometry to maximize use of wafer area
- Robust module constructed from a baseplate, insulating layer, silicon sensor, and readout PCB
- Comprehensive test program including full-sensor tests with custom probe cards and reactor-based neutron irradiations



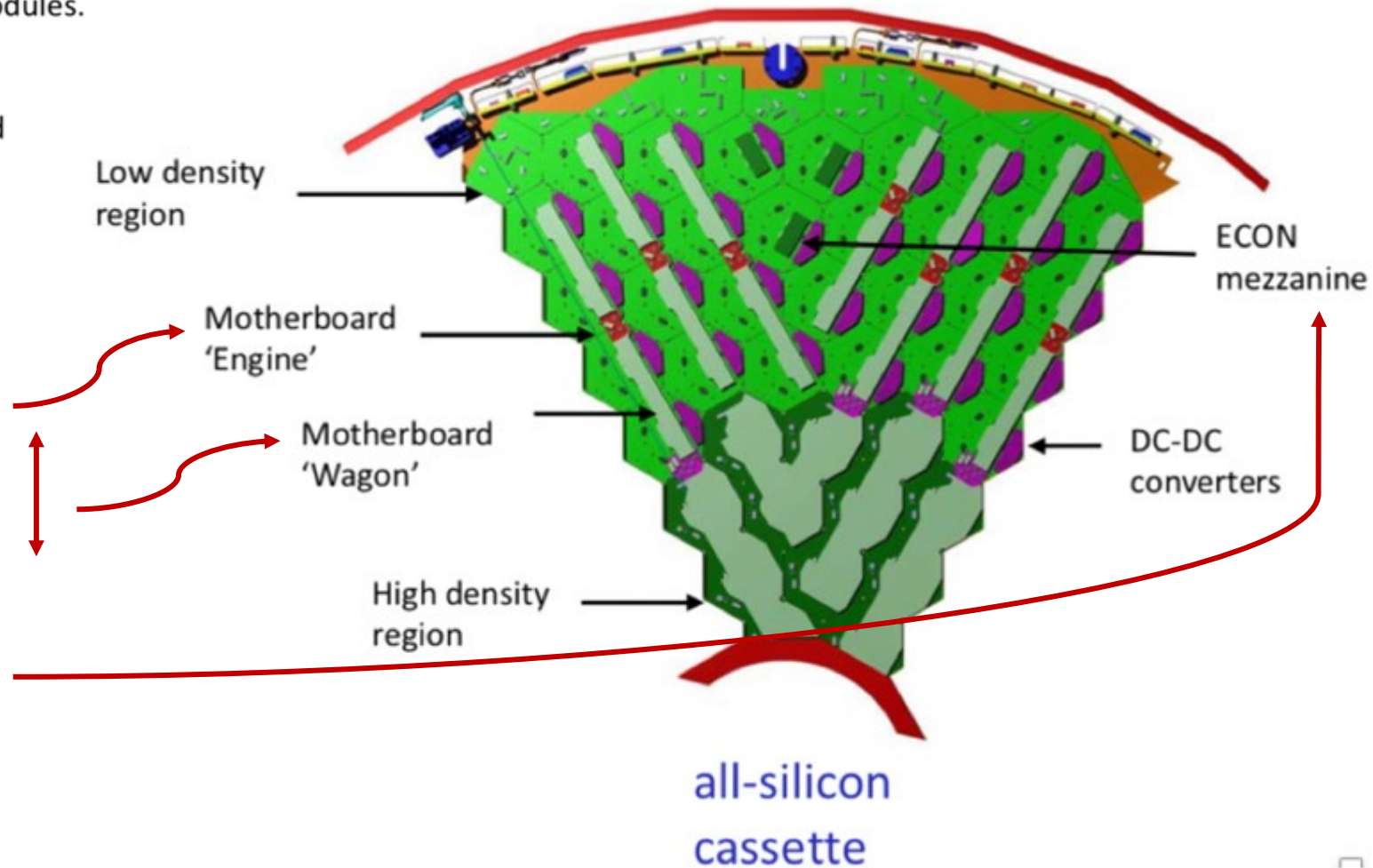
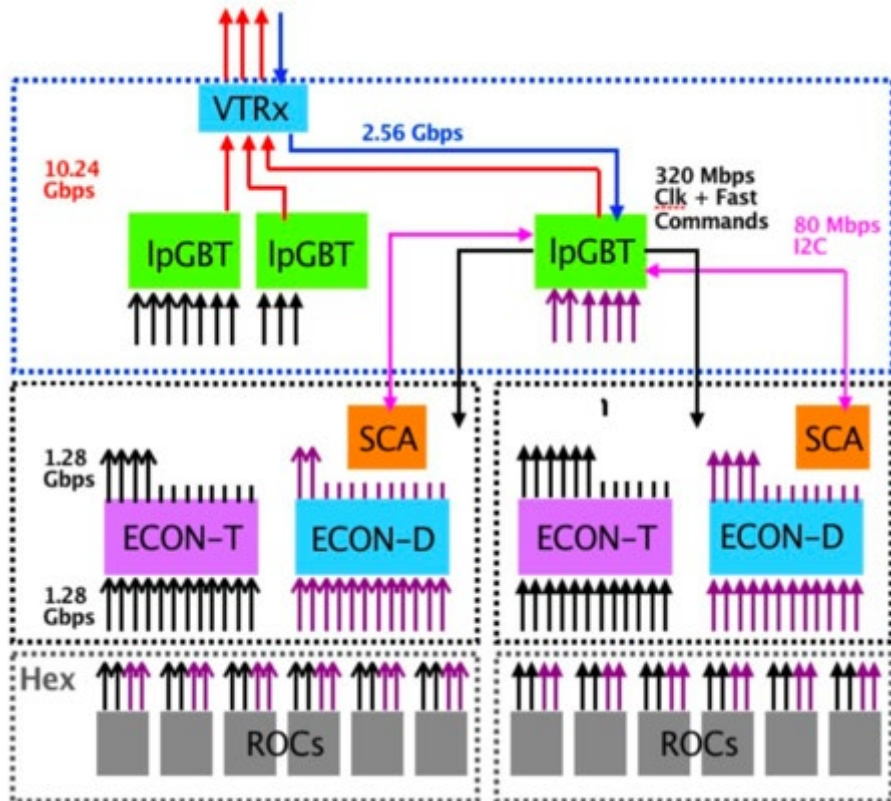
- **Scintillator Tiles and SiPMs :**

- Scintillator tiles will be produced using both injection molding and machining of cast scintillator
 - Tiles will be wrapped with ESR foil in an automated wrapping machine.
- SiPM photodetectors have been produced which provide sufficient signal-to-noise over the life of the experiment
- Tile modules constructed by automated pick-and-place of tiles onto tile PCB containing the SiPMs, readout chip, and other components



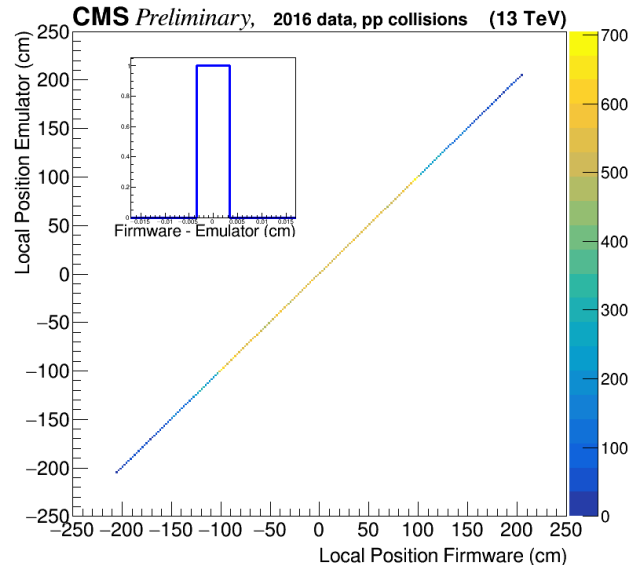
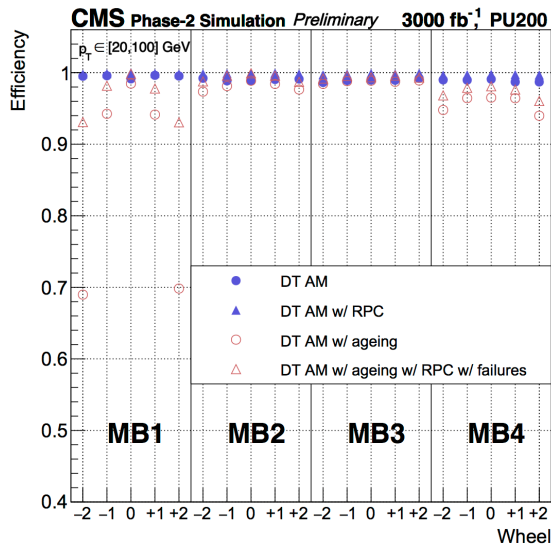
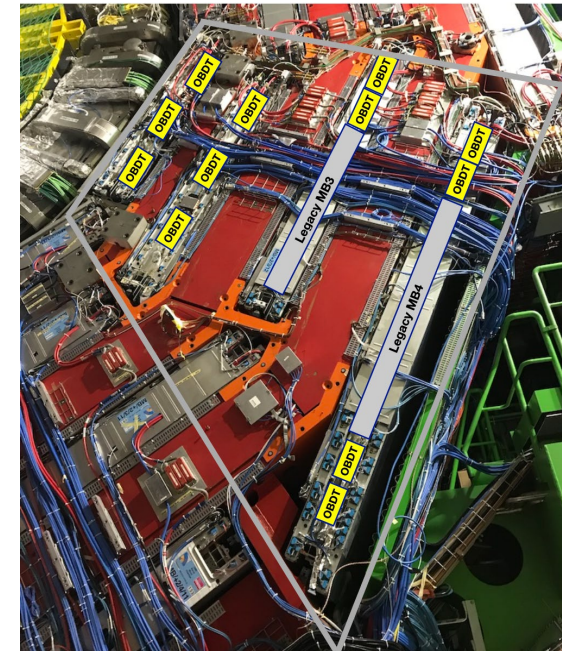
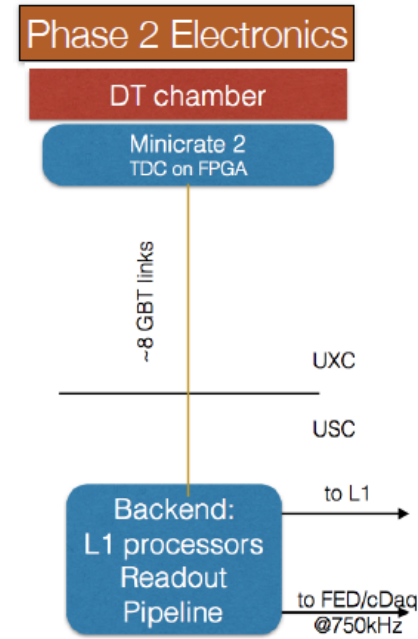
CMS HGCAL: Front-End Electronics Architecture

Active detector organized into **cassettes**
 comprised of a cooling plate with silicon and scintillator modules.
 Front-end electronics on the modules
 Readout and control through motherboards
 Powering via DCDC converters now located at the front-end



CMS Muon upgrade: DT

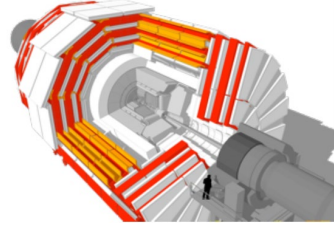
- Replace FE and BE electronics LS3
 - 40 MHz readout with improved z/t-precision
- Move trigger primitive (TP) functionality to BE:
 - more flexibility in combining DT and RPC hits to form Trigger Primitives (TP)
 - higher efficiency (use more than 4 layers for TPs)
 - better timing at L1 Trigger and improved resilience to ageing.



Slice installed during LS2: parallel ops in Run-3: DT sector has been instrumented with prototypes of OBDTs and operated with a full Phase-2 electronics demonstrator implemented on Phase-1 HW

- MB1/MB2: perform integration as in Phase-2, with only new electronics
- MB3/MB4: run legacy and Phase-2 chains in parallel to validate Phase-2 data quality using legacy information

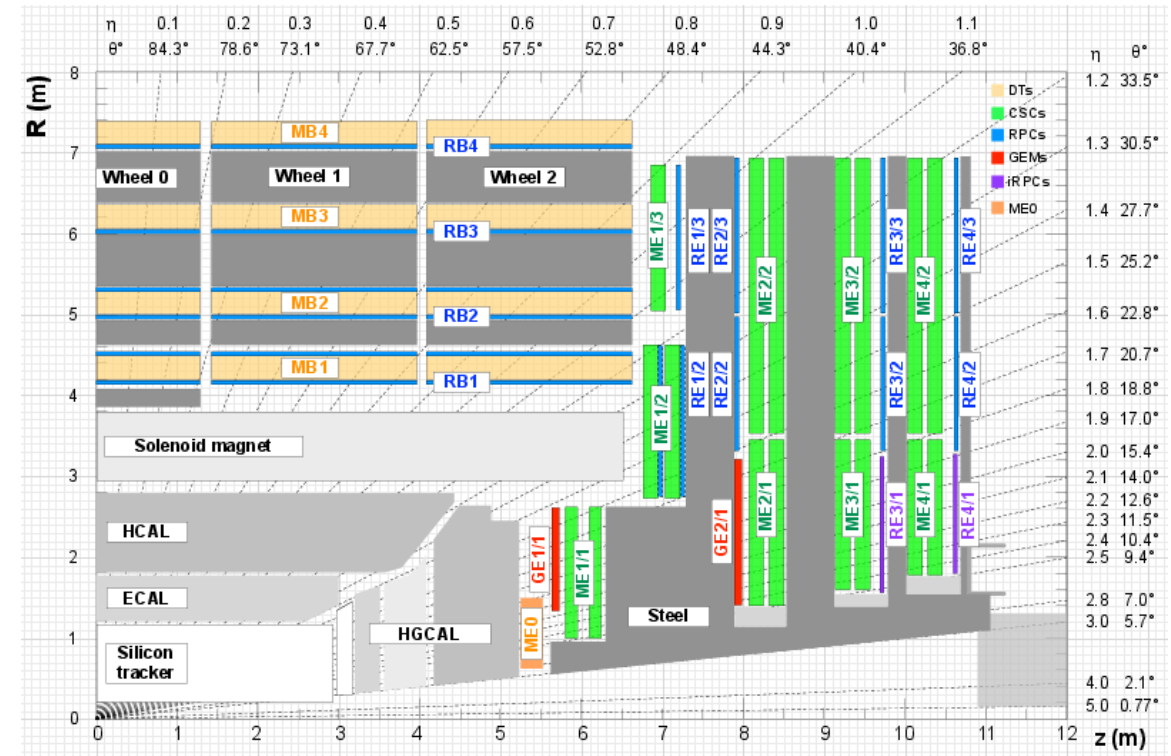
CMS Muon upgrade

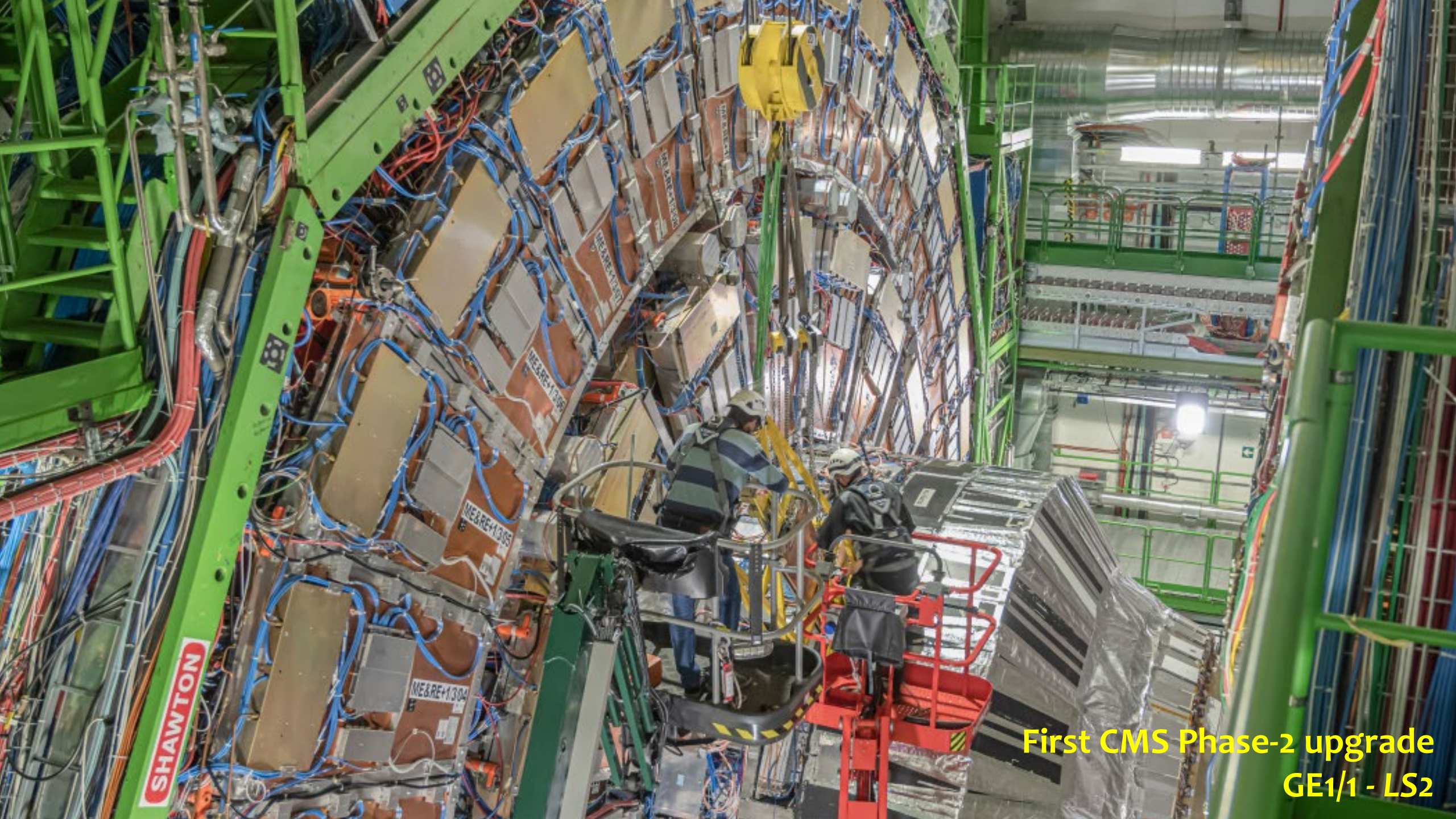


Current muon detectors are **expected to withstand HL-LHC radiation levels**.

Upgrading/replacing the electronics of the existing DTs, CSCs and RPCs to ensure longevity and improve trigger performance.

- **DT Drift Tubes barrel chambers:** 40 MHz readout with improved z/t-precision
- **RPC Resistive Plate Chambers:** readout with improved t-precision
- **CSC Cathode Strip Chambers:** readout with higher bandwidth and latency in ME234/1 using current ME1 and replace ME1 with higher radiation tolerance components
- **New stations:**
 - GEM: **GE1/1, GE2/1**, iRPC: **RE3/1, RE4/1**, $1.6 \leq \eta \leq 2.4$
 - GEM: **MEo** extended coverage $1.15 \leq \eta \leq 3$





SHAWTON

ME&RE-1304

ME&RE-1305

ME&RE-1306

ME&RE-1307

ME&RE-1308

First CMS Phase-2 upgrade
GE1/1 - LS2

L1T: Hardware prototypes

Design philosophy: Generic Processing Engines → I/O, FPGA

- FPGA : Xilinx Virtex Ultrascale / Ultrascale+
- Optics : Samtec Firefly x4 /x12 flyover
- Processors on board running commercial linux for flexible configuration and monitoring

APx:

- Powered by a VU9P FPGA with 2.5M logic cells
- 100 bidirectional links up to 28 Gb/s
- Control, management, and monitoring by an embedded linux mezzanine (ELM) (ZYNQ SoC)
- Shelf management via custom IPMI mezzanine (OS)

X2O/Octopus (OCEAN prototype)

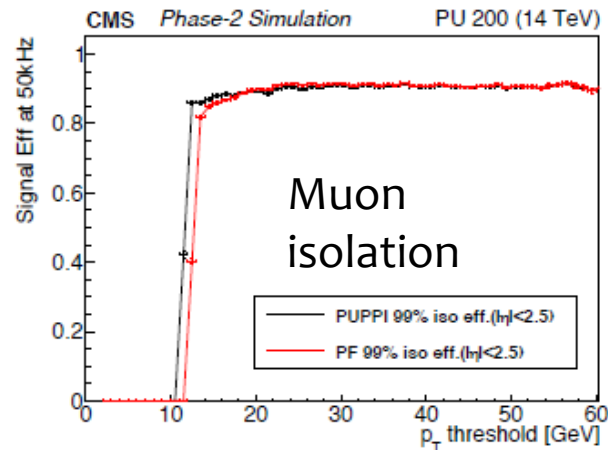
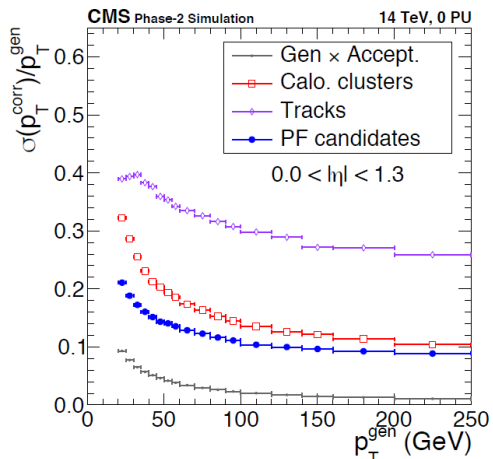
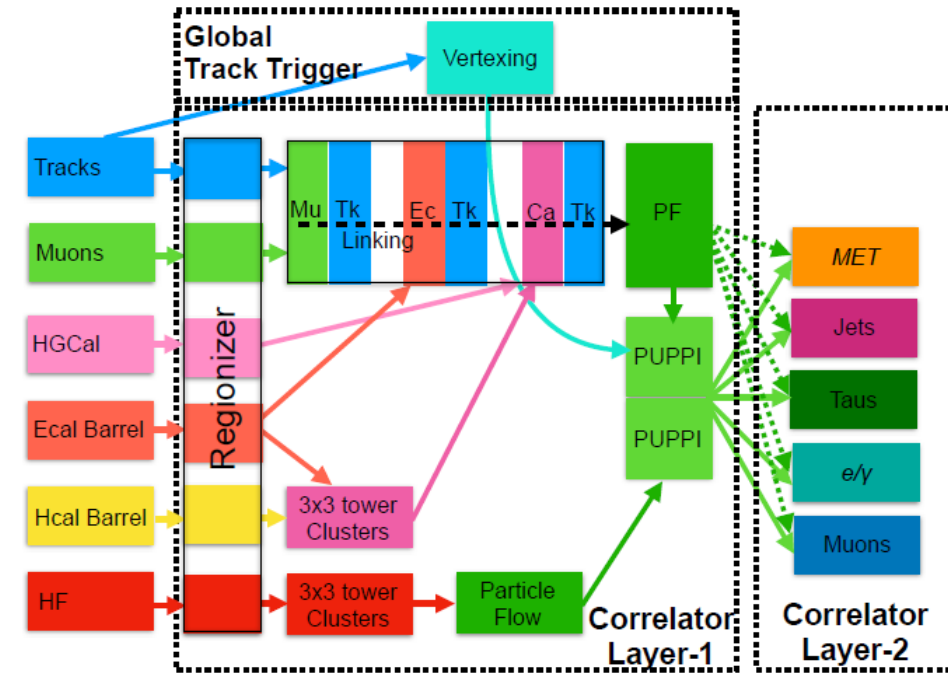
- Modular design (x2 FPGA)
- Optical Module
- Up to 112 links
- Compatible with 25G and 10G transceivers
- Power Module: Off-the-shelf ZYNQ mezzanine, DC-DC converters, IPMC running on the ZYNQ
- Inter-module connections with cables

Serenity:

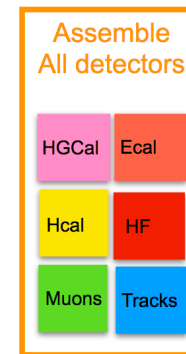
- Carrier board w/ 2 sites hosting daughter cards (any combination of FPGA)
- Up to 144 bidirectional links (extendable to 192)
- Control & Monitoring: COM express (x86 processor)
- IPMI management through CERN IPMC

L1T: Particle-Flow @L1

- Huge step forward that will vastly increase physics performance at L1
 - Availability of tracks & high-granularity calorimetry.
 - Further help to mitigate pileup.
- **Demonstrated a working PF+PUPPI algorithm**
 - PF+PUPPI hugely reduces the event complexity
 - Allows for a lot of flexibility in downstream design
 - L1 Algorithms looks like offline reconstruction
 - PF+PUPPI developed with Vivado HLS that meets HW requirements (latency, resources occupancy,...)



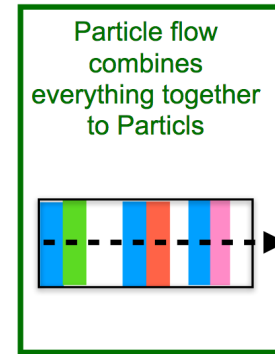
PF takes in everything



PF is local



PF Links



Can we run a local PU Algo?

