

Session Program

21-24 Sept 2020

TWEPP 2020 User Group Meetings

FPGA user group meeting

Wednesday 23 September

14:00

FPGA user group meeting

Session

14:00–14:10 Introduction**Speakers**

Ken Wyllie, Salvatore Danzeca

14:15–14:45 FPGA On-board Power Management**Speaker**

Jean-Pierre Cachemiche

14:45–15:15 FPGA testing methodologies**Speaker**

Luca Sterpone

15:20–15:40 Alice ITS FPGA radiation qualification**Speaker**

Matteo Lupi

15:45–16:05 NanoExplore Rad-Hard commercial FPGA**Speaker**

Rudy Ferraro

16:10–16:30 Radiation-tolerant Multi-gigabit Serial Links Based on FPGAs**Speaker**

Raffaele Giordano

16:35–16:55 Rad-Tol RISC-V Soft Core**Speaker**

Mattia Rizzi

17:00