

# Summary of TDAQ session

*R. Brenner and C. Leonidopoulos*

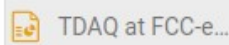
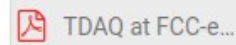


UPPSALA  
UNIVERSITET

15:30

## Event sizes @ Zpole

Speaker: Francesco Grancagnolo (Universita & INFN Lecce (IT))



15m



15:45

## Triggerless design and tracking with ultra light TPC

Speaker: David Rohr (CERN)



15m



16:00

## Readout of ultra light vertex detectors

Speaker: Gianluca Aglieri Rinella (CERN)



15m



16:15

## Long lived non-relativistic particles

Speaker: Sinead Farrington (University of Edinburgh)



15m



16:30

## LHCb TDAQ system

Speaker: Tommaso Colombo (CERN)



15m



16:45

## Unsung heroes of an experiment

Speaker: Richard Jacobsson (CERN)



15m



# The big picture - Francesco Grancagnolo



	CLD		IDEA		
	m <sup>2</sup>	RO ch.	m <sup>2</sup>	RO ch.	
vertex detector	0.53	<b>0.84 G</b>	2.2 + 18	<b>1.17 + 0.36 G</b>	
central tracker	200	<b>2.75 G</b>	175	<b>90 + 35 M</b>	
calorimeter	ECAL	4000	160 M	PS 230	1.5 M
	HCAL	8000	9.2 M	DR	130 M
muon detector	3250	3.6 M	2800	3.8 M	
TOTAL		<b>3.76 G</b>		<b>1.79 G</b>	

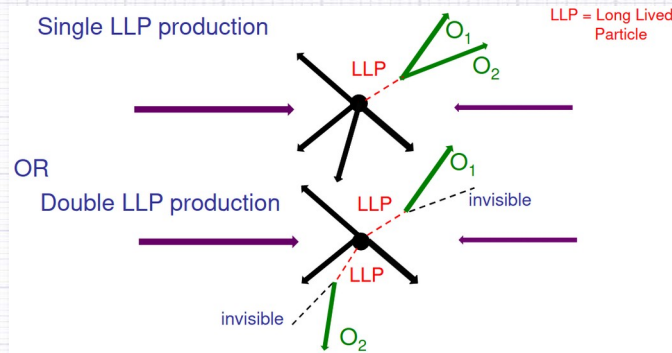
## Event rates at Z<sup>0</sup>-pole

- 100 KHz Z decays
- 30 KHz  $\gamma\gamma \rightarrow$  hadrons
- 50 KHz Bhabha
- 20 KHz beam backgrounds

II

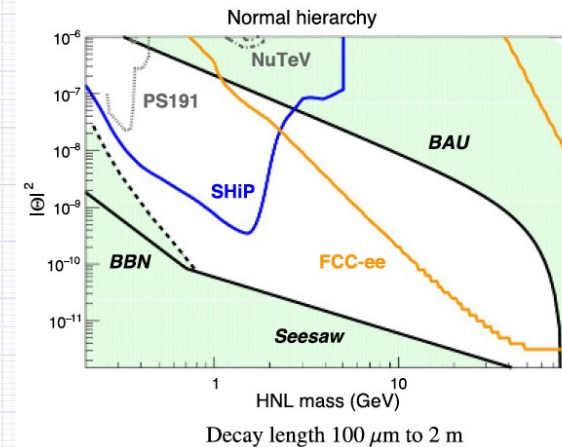
	Physics	Background/noise
CLD Vertex Detector	150 MB/s	6 GB/s
CLD Tracker	160 MB/s	10 GB/s
IDEA Drift Chamber	60 GB/s	2 GB/s
IDEA Si wrapper	32 MB/s	0.5 GB/s
<i>CEPC ECAL</i>	<i>21 GB/s</i>	/
<i>CEPC HCAL</i>	<i>1.8 GB/s</i>	/
IDEA DR Calorimeter	10 GB/s	(1.6 TB/s)
IDEA pre-shower	320 MB/s	820 MB/s
IDEA Muon Detector	4 MB/s	67 MB/s

# The unknown - Sinead Farrington



## Summary

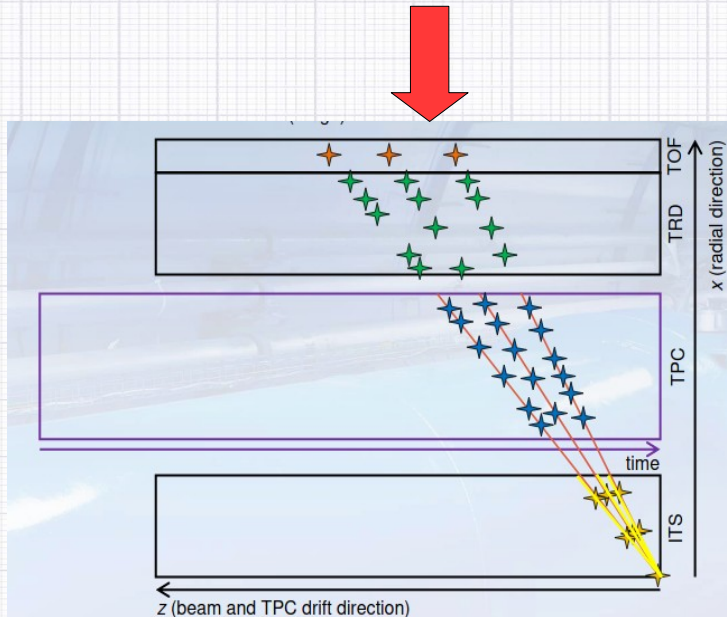
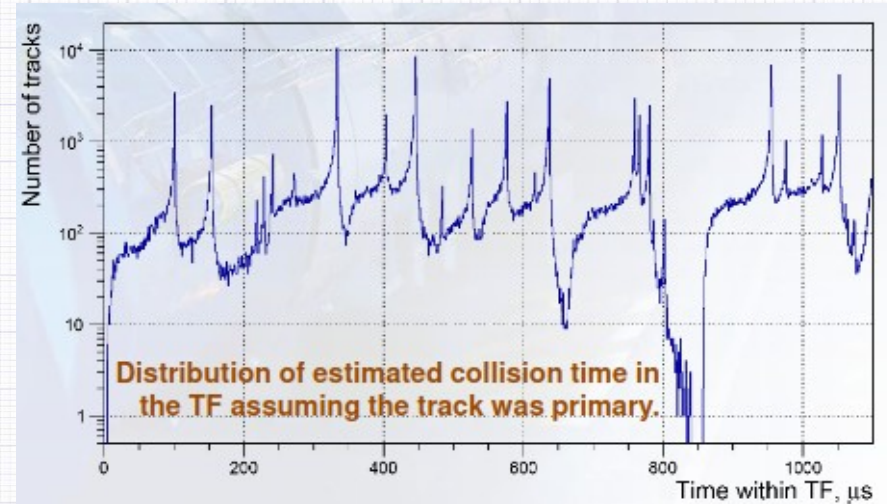
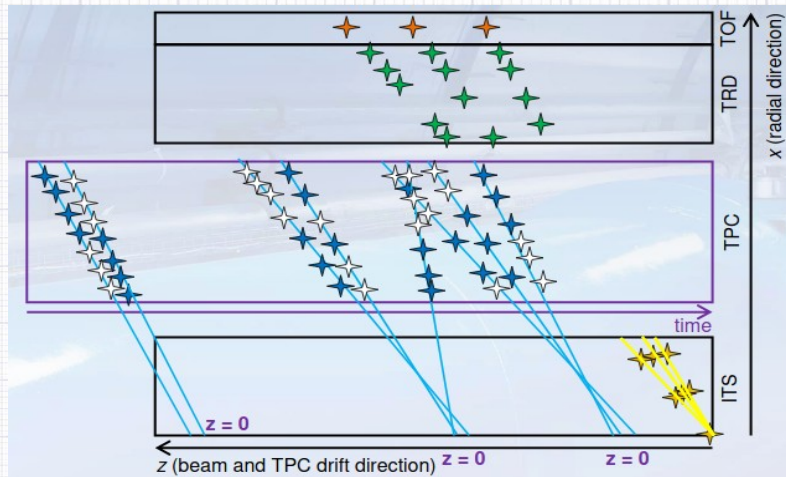
- What about the additional LLP detectors at the LHC
  - (FASER/Mathusla/CodexB) - design them in now, integral to the triggering of the main detectors
- If LLPs couple to H, FCCee will be complementary to the large Higgs samples of HL-LHC and FCChh through
  - Good vertex resolution
  - Clean environment in which to do LLP-friendly tracking and particle ID
- FCChh will give the reach to the broadest set of LLP masses, and through high statistics also to rare processes
- FCCee/hh has the opportunity to include LLP as a design requirement considering
  - Hardware that has the impact parameter resolution needed
  - Readout segmentation that lends itself to LLP triggering
  - Hardware track triggering for FCChh
- These trigger challenges suggest we need R&D now for FCChh to meet the ESU requirement to evaluate on the timescale of the next update
- As the CDR states, these issues may be crucial in determining the feasibility of constructing these detectors (in particular for FCChh)



FCCee CDR: CERN-ACC-2019-0003

# Towards new technology- David Rohr , Gianluca Aglieri Rinella, Tommaso Colombo

- Triggerless design (TPC tracking)



- Ions move very slowly  $\Rightarrow$  10-20 ms of continuous data instead of events
- Space charge distortions a big challenge
- GEM readout scales linearly with an interaction rate up to 50 kHz  $\rightarrow$  not compatible with rates at z-pole

- Readout of ultra light vertex detectors

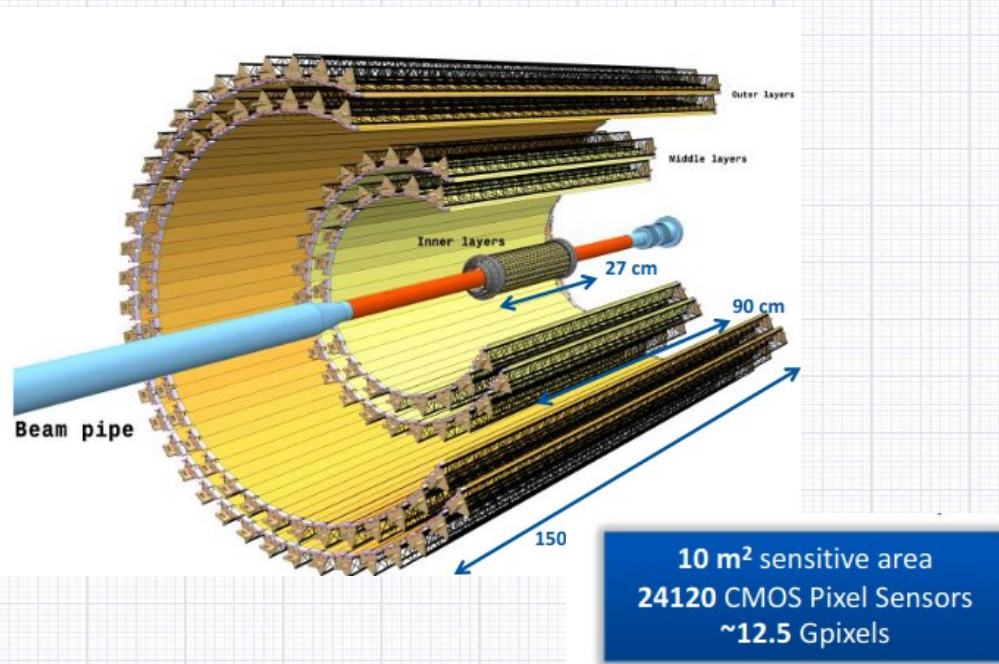
### ALPIDE *particle rate max. capabilities*

Assuming 2.5 average cluster size

Matrix to Periphery hit data transfer:  $\sim 50 \text{ MHz/cm}^2$

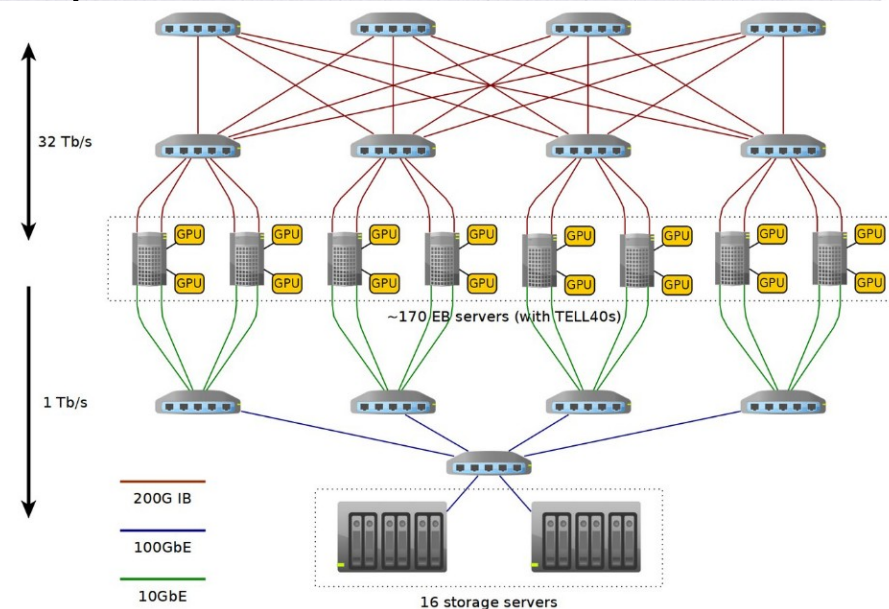
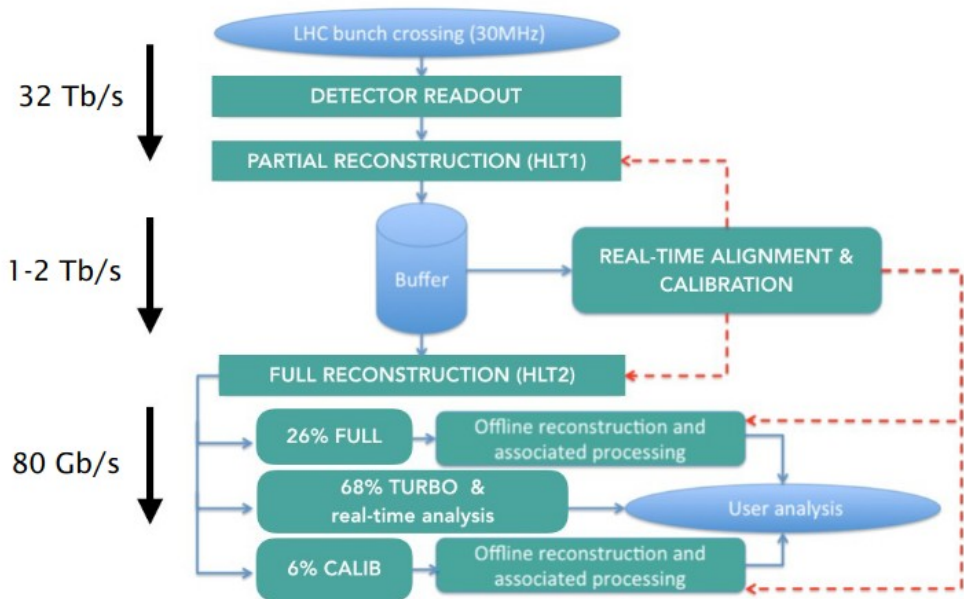
At slow frame rates. Reduces with faster frame rate

Chip output link max capacity:  $\sim 6 \text{ MHz/cm}^2$  (with on-chip data reduction),  $\sim 4 \text{ MHz/cm}^2$  (raw)

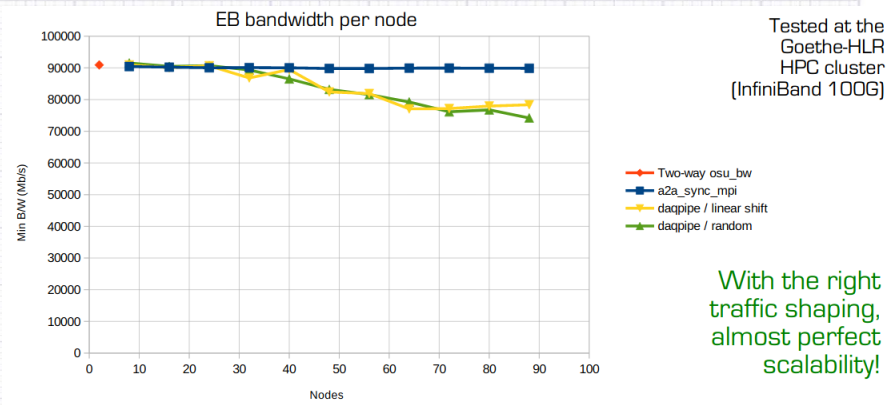


- 6 us time-stamping/event → in continuous mode hits can span across events
- The effect on readout rates when switching to air-cooling ?

# Triggerless DAQ system (LHCb)



- 2 levels of s/w filtering: from 32 Tbs to 1-2 Tbs to 80 Gbs
- Biggest worry: scalability and reliability of network
- Event-selection is entirely in software to maximize physics yield, increase the amount of data collected, flexibility and minimize cost
- The system is very well scalable, by up to 3 a factor without any substantial changes



# Have we learned anything? - Richard Jacobsson

