



# HiPIMS Nb based thin films deposited on copper

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# Why HiPIMS?











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## DC MS Nb









## DC MS Nb





## DC MS Multilayer





Large Surface Roughness





## DC MS Nb





Voided Interface



## DC MS Multilayer





## **HiPIMS Promises:**

- Excellent adhesion
- Superior density
- Decreased roughness
- And more





# Substrate Preparation "Substrate is Key"



## **Substrate Preparation**



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## Cu substrate preparation recipe:

- Mechanical polishing
- Electrolyte polishing
  - Including activation and passivation
  - $S_q = 2.58 \pm 0.26$ nm
- Degreasing Lupo 44
- MF Plasma etching







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# **Coating Setup**





- Commercial high-volume, fully automated coating tool (CemeCon CC800)
  - Deposition on small flat samples (25 mm x 25 mm Cu) and QPR sample
- Base pressure of 5x10<sup>-7</sup> mbar following 6 h 280°C bake
- Indirect heating of samples
- DC Power supply connected to Capacitor bank for HiPIMS operation.
- HiPIMS parameter range:
  - Pulse length: 0-200µs (20µs step)
  - Frequency: 0-2000Hz (100Hz step)









# **HiPIMS Nb Deposition**





## **Deposition Parameters:**

- 1. Cathode Power
- 2. Deposition Pressure
- 3. Pulse Length
- 4. Frequency
- 5. Substrate Bias
- 6. Substrate Temperature
- 7. Film Thickness
- 8. Low Duty Cycle

300 to 600W 800 to 1800mPa 80 to 200µs 800 to 2000Hz 0 to 250V 120 to 290°C 1 to 10µm 2-4%





- Surface topography similar for all samples.
  - Except for high substrate bias
  - Multiple grain structures present.







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  - Multiple grain structures present.
- Reduced surface roughness:
  - 11.19nm (HiPIMS) vs. 19.38nm (DC MS)
- Oxygen content generally 0% (EDX)
- Interface significantly improved. Zero voids











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**Duty Cycle Effects** 



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## Pulse Length











### Frequency







- Duty cycle held at 10% (100µs, 1000Hz).
- Lattice parameter closest to bulk at low bias levels.







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- Lattice parameter closest to bulk at low bias levels.
- Film surface damage at bias above 150V.
- Entry flux reaches a maximum at 100V. Max flux decreases above 100V.
- Tc increases up to 9.75K at high substrate bias.









- Duty cycle held at 10% (100µs, 1000Hz) for temp investigation and 12% for thickness investigation (120µs, 1000Hz).
- Increase in film Tc to 9.65K at high Temperature



Substrate Temp + Film Thickness LOT

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- Duty cycle held at 10% (100µs, 1000Hz) for temp investigation and 12% for thickness investigation (120µs, 1000Hz).
- Increase in film Tc to 9.65K at high Temperature
- Stable, high entry flux above 3.68µm

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## Low Duty Cycle



- Significant increase in the Max current = 55A!
  - DCMS 1.5A (constant) for ref.
- Sharper increase in current at low duty cycle





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- Appearance of self-sputtering "2<sup>nd</sup>" peak.



Time (µs)





## Low Duty Cycle



- Significant increase in the Max current = 55A!
  - DCMS 1.5A (constant) for ref.
- Sharper increase in current at low duty cycle
- Appearance of self-sputtering "2<sup>nd</sup>" peak.
- CERN HiPIMS at 1% DC for ref.
- Superconducting performance reaches max at medium duty cycle values







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4% Duty Cycle





## **Summary of HiPIMS Nb Results:**

- Surface topography similar for all samples. Damage at bias >150V
- Reduced surface roughness:  $9.5 \pm 3.06$  nm vs.  $19.38 \pm 4.33$  nm
  - Increases with increased cathode power, substrate bias and film thickness
- Oxygen content <5% in all samples. Typically 0% (EDX)
- Second peak in LDC
  - Onset of self-sputtering, no runaway
- Increased  $T_c$  at high bias and temperature (up to 9.75K). Typically = 9.3K
  - Due to increased film stresses.
- Significant maximums in entry flux and max flux.
  - Higher cathode power and pressure
  - Mid-range duty cycle (12/14%)
  - Intermediate substrate bias (50-100V)
  - Lower substrate temperature
  - Film thickness near 3.5µm





# **QPR HiPIMS Nb – CERN**

Thanks to V. Garcia (INFN) and G. Rosaz (CERN)



## **QPR HiPIMS Nb - CERN**





















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## **QPR HiPIMS Nb - CERN**



- Cu treated by SUBU
- Film thickness of 7.3µm XRF
- Surface roughness  $S_q = 50.05 \pm 2.24$  nm
- Average crystallite size: 76 nm
- SC Properties:  $B_{en}$ : 505 Oe,  $T_{c}$  = 9.2 K











## **Magnetisation Loops**









# **HiPIMS Nb Application**





- Two series of HiPIMS Nb SIS samples produced (Thickness varied from 100nm to 250nm)
  - 1. Sample 790 recipe (Highest Ben, low Tc-11.7K)
  - 2. Sample 899 recipe (Highest Tc-16.1K), low Ben)





- Two series of HiPIMS Nb SIS samples produced (Thickness varied from 100nm to 250nm)
  - 1. Sample 790 recipe (Highest Ben, low Tc-11.7K)
  - 2. Sample 899 recipe (Highest Tc-16.1K), low Ben)
- Distinct differences observed.
  - All samples coated with 790 recipe display higher Ben (>600 Oe) NbN recipe influence
- Increased Tc with thickness
  - Max Tc equal to best single film value 16.1K
  - Sample 790 recipe films show higher transition temperature in SIS films







Surface roughness of HiPIMS Nb SIS films reduced





-20



- Surface roughness of HiPIMS Nb SIS films reduced
- Microstructure of films changed compared to single layer films









- Surface roughness of HiPIMS Nb SIS films reduced
- Microstructure of films changed compared to single layer films
- HRTEM shows lattice matching (heteroepitaxial growth)



Courtesy: Ying Li































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## **Extra Slides**





- Current increases with increasing power. Rate . of increase is also greater. Peak position shifts earlier.
- Corresponding increase in deposition rate.
- Lattice parameters are relatively equal. Slight • decrease at high power.
- SC Measurements completed with VSM. ٠ Entry flux and Max flux show an increase with increasing cathode power.







30

25

20

15 10

5

0

-5 <del>]</del> -25

Current (A)

800mPa

1000mPa 1400mPa

1800mPa

0

25

50

Time (µs)

75

100

125



- Current increases with increasing pressure. Rate of increase is similar. Peak position shifts later.
- Deposition rate remains the same.
- Lattice parameters approach bulk at intermediate values. Crystallite sizes increase with increasing pressure.
- Entry flux value at 1.4E-02 presumed incorrect. Remeasure to be completed.







