

# Integration of Hyper-Kamiokande Electronics and Test in Super-Kamiokande

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for the Hyper-Kamiokande Collaboration

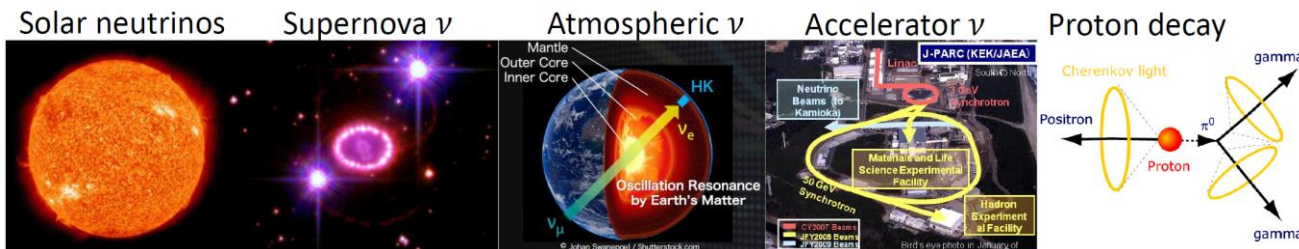
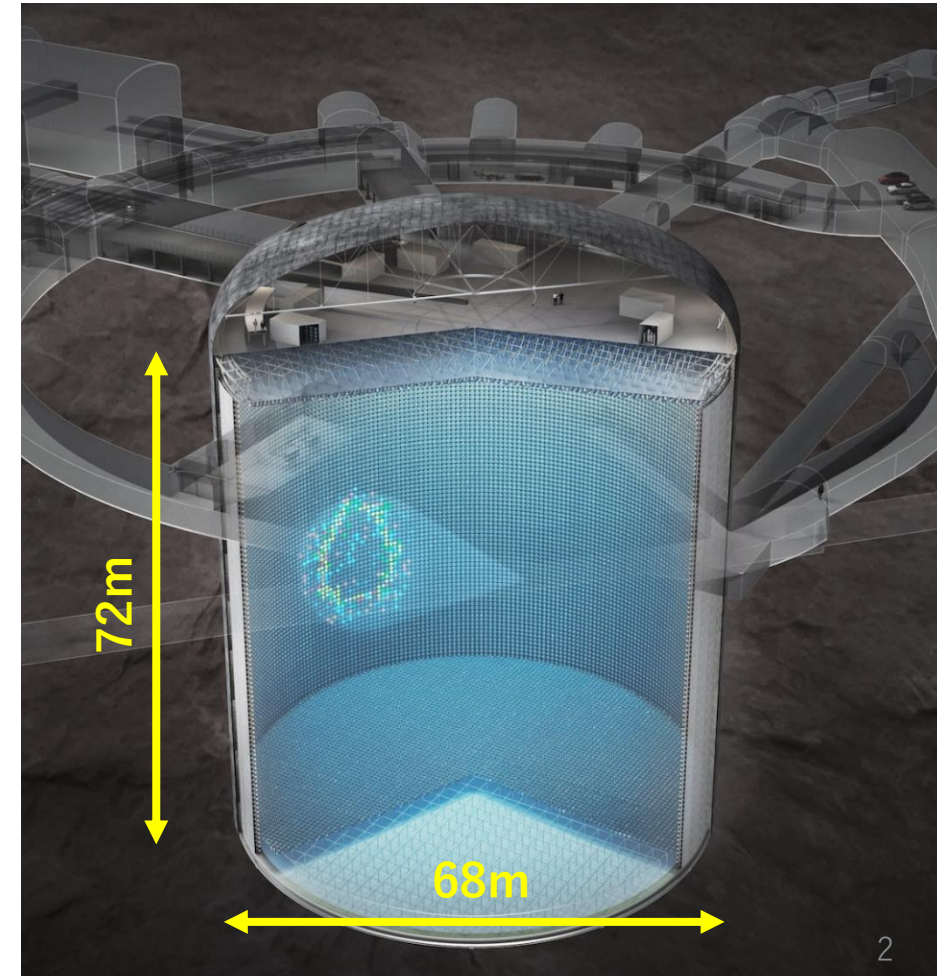
# Hyper-Kamiokande

- **Next generation neutrino experiment starts in 2027**
  - ✓ 260kton water tank (FV=190kton) .. *~8 times larger than SK*
  - ✓ 20" HK Box&Line PMT (~20,000 PMTs)
    - .. *performance improved by a factor 2*
    - (detection efficiency, Q/T resolution)
  - ✓ also a part of long base line experiment
- **Rich physics programs**
  - ✓ Neutrino physics (Atmospheric, accelerator)
  - ✓ Proton decay
  - ✓ Astrophysics neutrino (Supernova, relic SN, Solar, etc)

20" HK PMT  
(R12860)



HK water tank



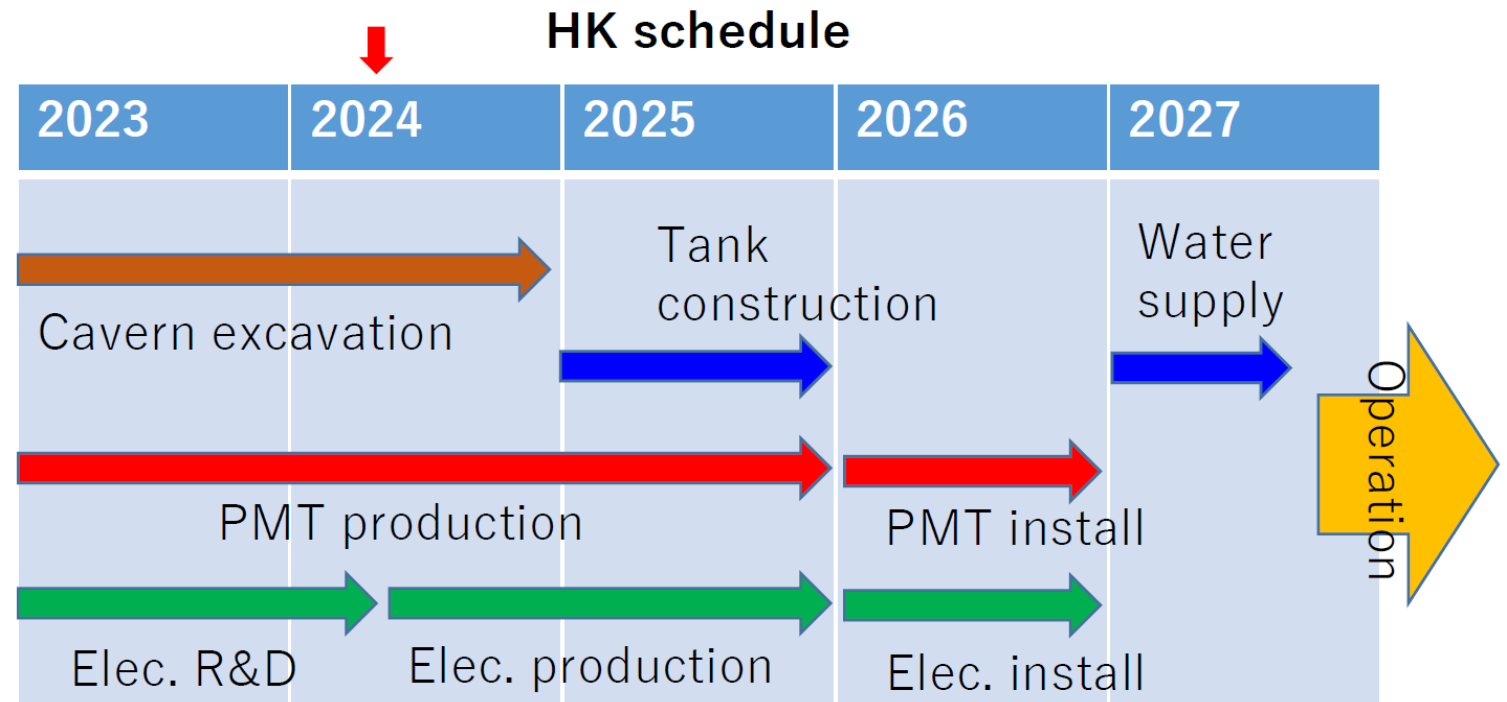
# HK Construction Schedule

- **HK construction is on-going**
  - ✓ main cavern is under excavation
  - ✓ PMT mass production is on-going since 2020
  - ✓ We are finalizing electronics design, and after final tests we move on mass production

main cavern (under excavation)

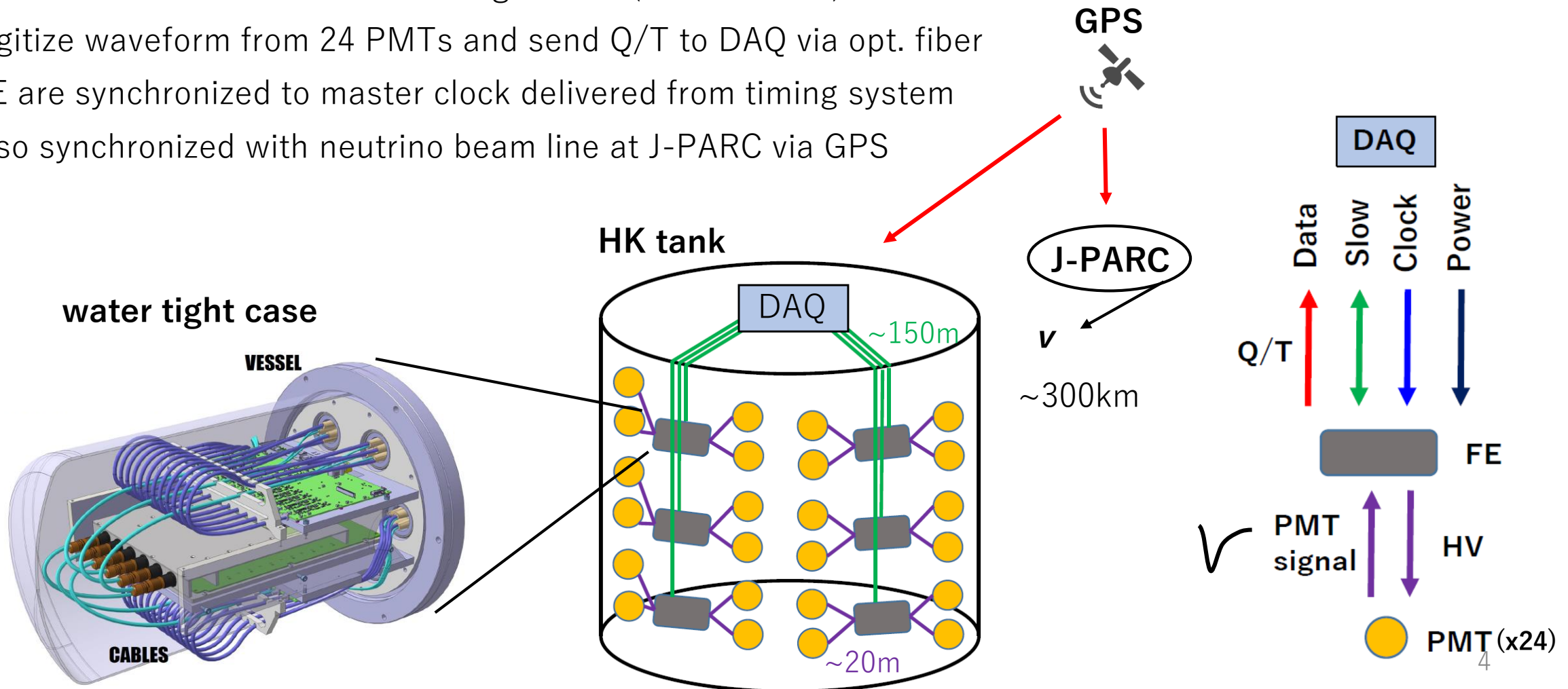


PMT mass prod. (quality check)



# Concept of HK Electronics

- **Frontend electronics is placed under water inside HK tank** (↔ outside of tank in SK)
  - ✓ install FE in 1MPa tolerant water tight case (~1000 cases)
  - ✓ digitize waveform from 24 PMTs and send Q/T to DAQ via opt. fiber
  - ✓ FE are synchronized to master clock delivered from timing system
  - ✓ also synchronized with neutrino beam line at J-PARC via GPS



# HK Elec. Requirements

- Rich physics programs require wide range in energy and event rate
  - High performance HK PMT requires high resolution in charge and timing
  - Underwater environment sets additional constraints (water-tight, heat control, failure rate, etc)
- HK Elec. is requested to have equal or better performance than SK Elec.

## HK requirements

| Items                                 | Requirements   |   |
|---------------------------------------|--|---|
| single photon detection               | Thr.<1/6pe   | 1pe~2pC~6mV                                 |
| <b>Deadtime, rate tolerance</b>       | <b>&lt;500ns, 2MHz</b>                               | for $\mu$ decay e, neutron, SN burst        |
| <b>Charge resolution</b>              | <b>&lt;10%@1pe</b>                                   | PMT~30%@1pe                                 |
| <b>Timing resolution</b>              | <b>&lt;300ps@1pe</b>                                 | PMT~1ns@1pe                                 |
| <b>Charge range, linearity</b>        | <b>~1250pe, &lt;1%</b>                               | MeV~100GeV event                            |
| Temperature dependence                | <0.1%/°C   | with on-board calibrator                    |
| Synchronization                       | <100ps   | for chrenkov ring reconstruction            |
| Buffer & Through-put                  | O(GB), 1Gbps   | for SN burst ~200M events in 10s            |
| <b>10 years operation under water</b> | <b>failure rate&lt;1%/yr<br/>discharge tolerance</b> | water-tight, heat control, full remote, etc |

## difference to SK Elec.

|                 | HK Elec.     | SK Elec.                    |
|-----------------|--------------|-----------------------------|
| digitizer       | Discrete ICs | ASIC                        |
| <b>deadtime</b> | <b>500ns</b> | <b>1 <math>\mu</math> s</b> |
| time digit      | 0.25ns       | 0.5ns                       |
| <b>buffer</b>   | <b>8GB</b>   | <b>~100MB</b>               |
| readout         | 1Gbps        | 100Mbps                     |

# Frontend Boards

- FE consists of some boards, and prototypes are all ready.

## Digitizer board (12ch) x 2

- ✓ digitize PMT waveform to Q/T
- ✓ made by discrete analog components
- ✓ integrator & FADC
- ✓ FPGA TDC

## Data Process board

- ✓ process digitizer hits  
and communicate to DAQ with 1Gbps links
- ✓ slow control
- ✓ timing distribution

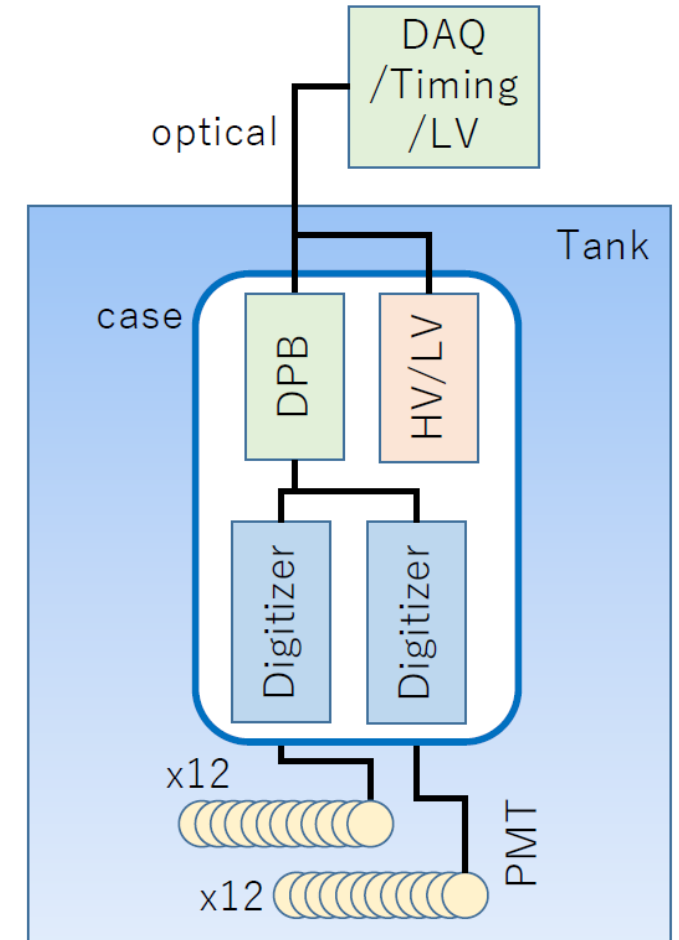
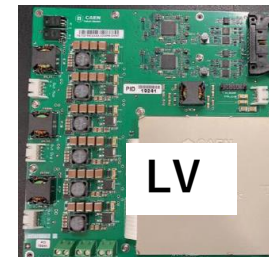
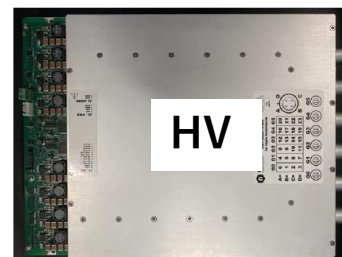
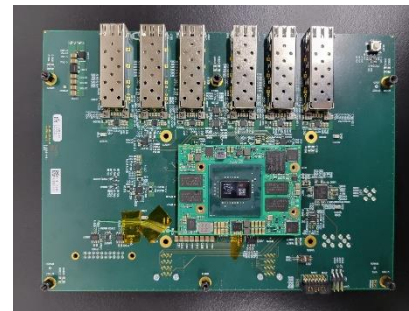
## HV/LV board

- ✓ 48V input (2A) <100W
- ✓ HV ~2.5kV for 24 PMTs

HK digitizer board

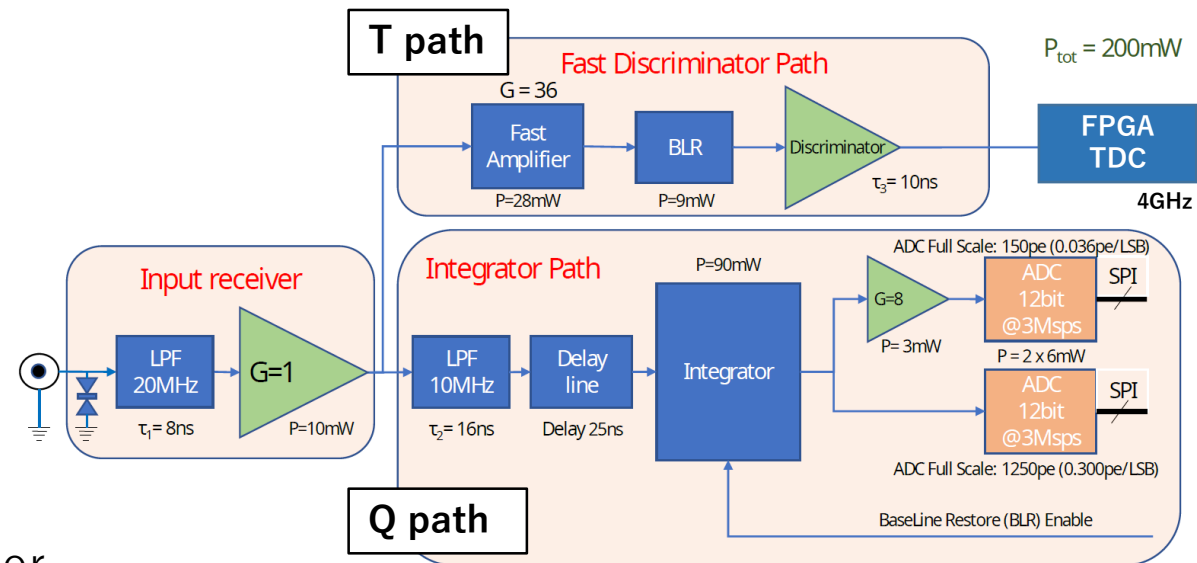


DPB

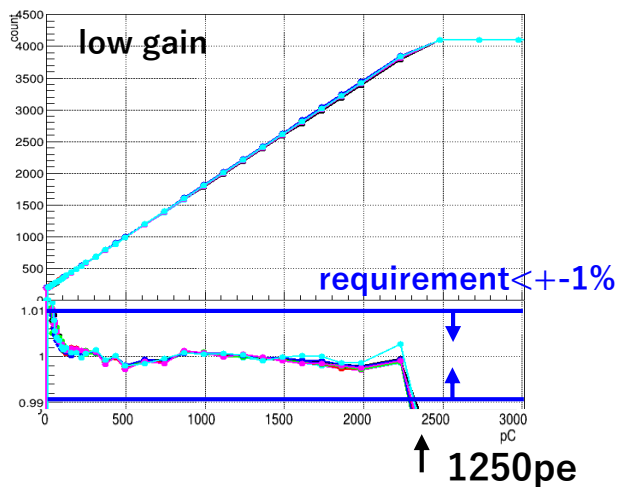


# HK Digitizer

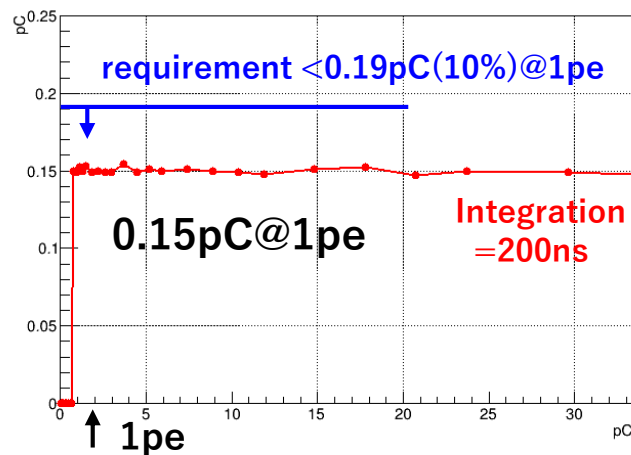
- Discrete design (non-ASIC) with Q/T paths
- ✓ **Q path:** Integrator and FADC,
  - 2 ranges to cover 2350pC with 1% linearity
- ✓ **T path:** Discriminator and FPGA TDC
  - multi phase TDC: 500MHz x 8 phases ~ 4GHz
- confirmed to meet requirements by pulse generator



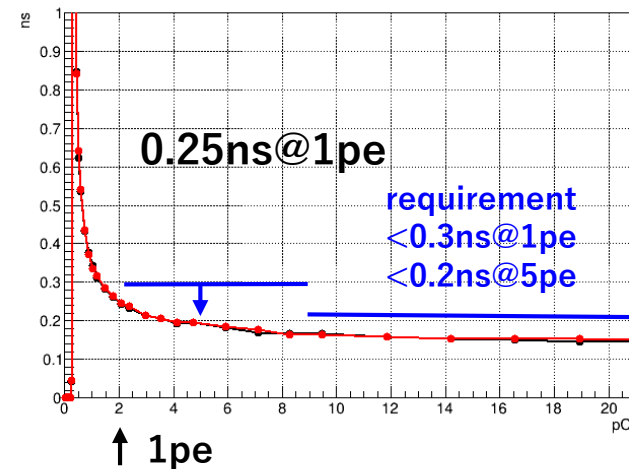
Charge Linearity



Charge resolution



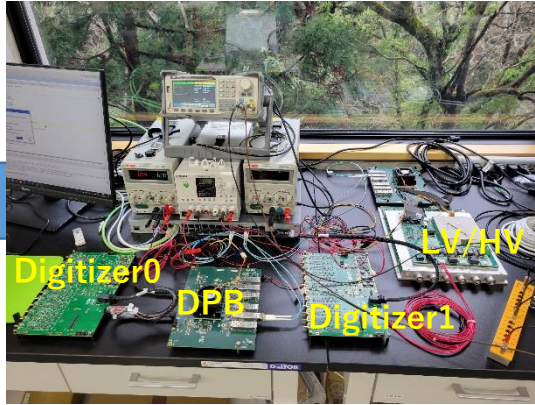
Time resolution



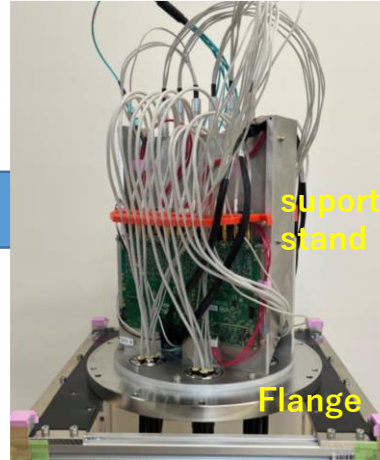
# Integration and Tests

- FE prototype boards have been integrated to work as a system

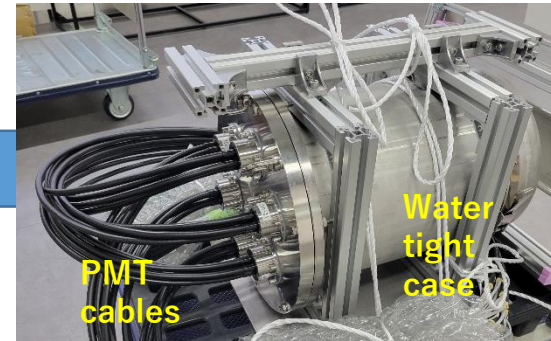
**Integration of boards**



**Mounting on stand**



**Installed in water tight case**



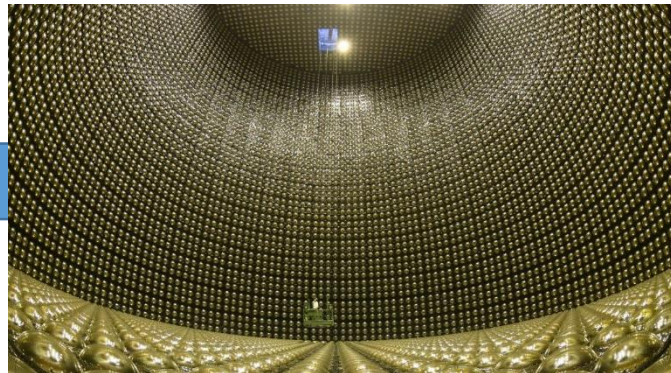
- Tests in realistic environment is on going

**focus of this talk**

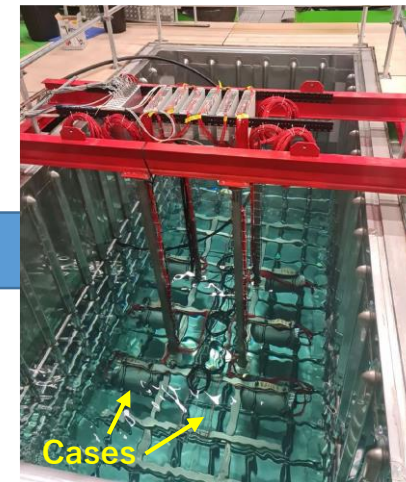
**Test with HK PMT**



**Test with SK**



**Under water test**



**ready to production**



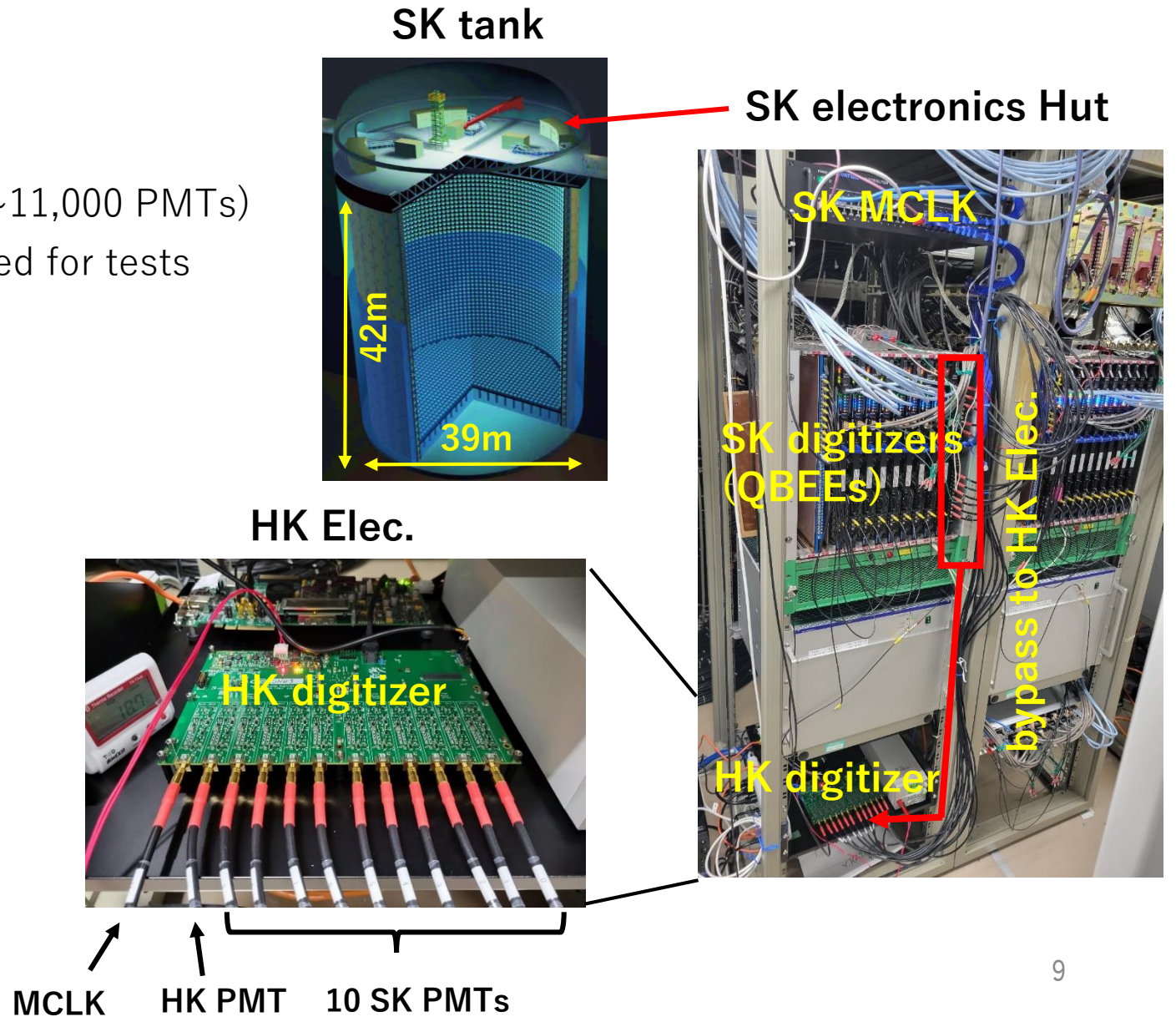
# Test with Super-Kamiokande

- **Realistic environment**

- ✓ real Cherenkov detector with 20" PMTs (~11,000 PMTs)
- ✓ small number of HK PMTs are also installed for tests
- ✓ calibration sources are available in tank
- ✓ easy to compare SK Elec. (QBEE)

- **Setup**

- ✓ FE is in electronics hut outside of SK tank
- ✓ starts from single HK digitizer board (move on full setup in the near future)
- ✓ replace 1 SK board (/500) with HK board
- ✓ connected to **10 SK PMTs, 1 HK PMT**, also SK master clock to synchronize



# Single photon detection

- **Noise level**

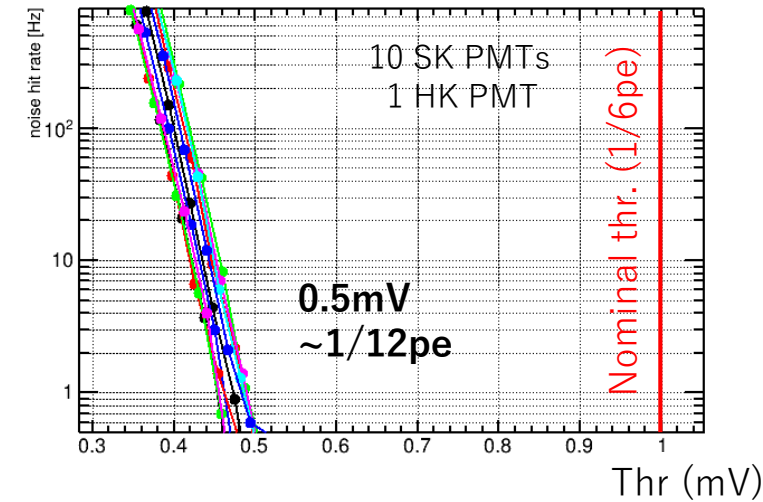
- ✓ Noise level confirmed to be 0.5mV ~ 1/12pe (<requirement 1/6pe)

- **Charge measurement (by dark hits)**

- ✓ observed good resolution of HK PMT ~ 32% @ 1pe (SK PMT ~ 50%)

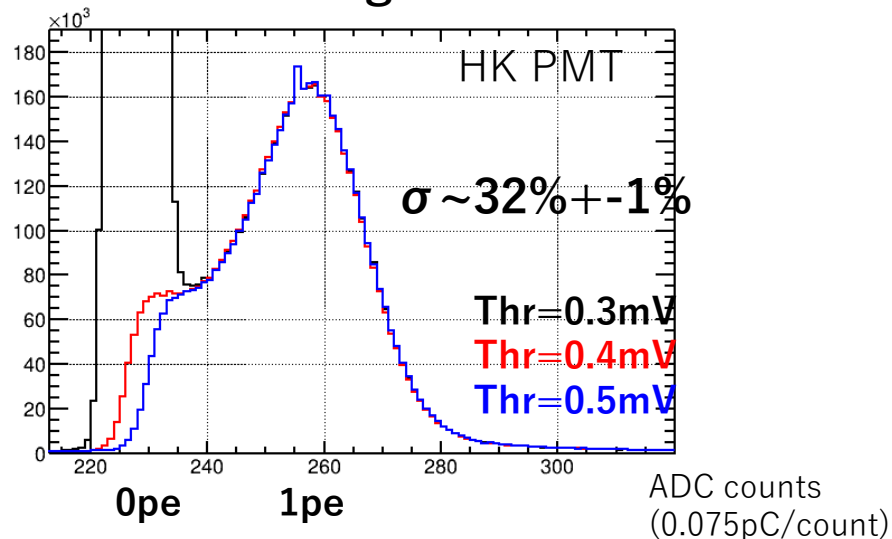
- ✓ no difference with SK electronics (resolution of electronics ~ 10% @ 1pe)

## Noise hit rate



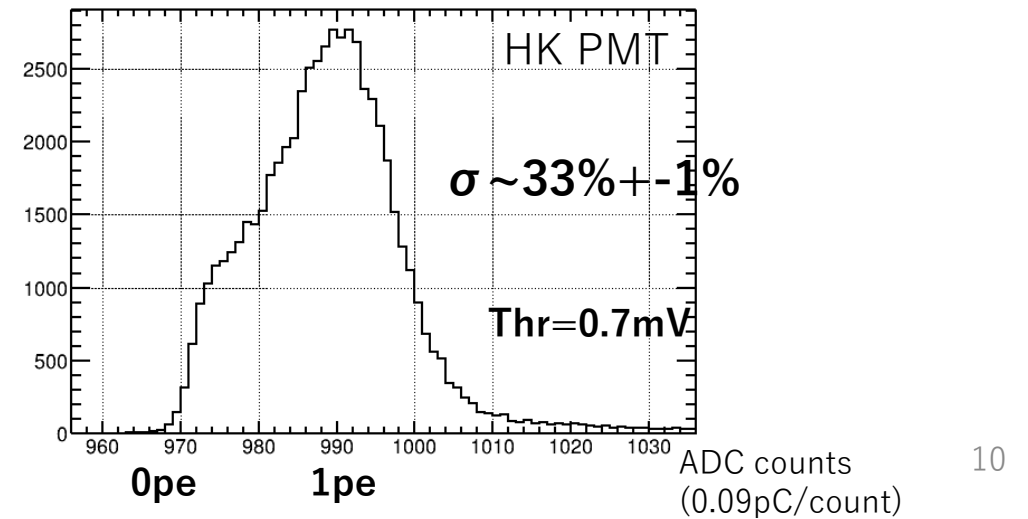
## HK Elec.

### Charge



## SK Elec.

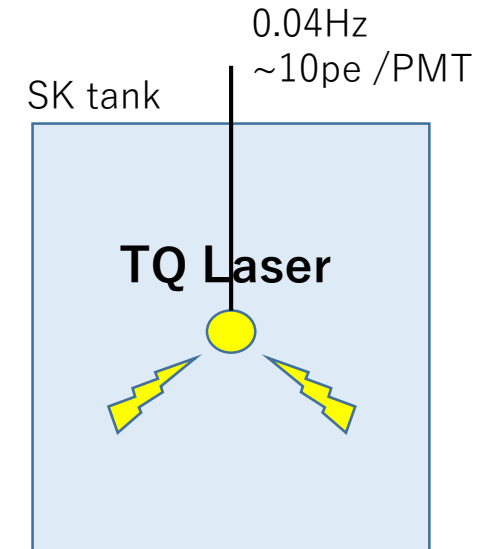
### Charge



# Timing performance

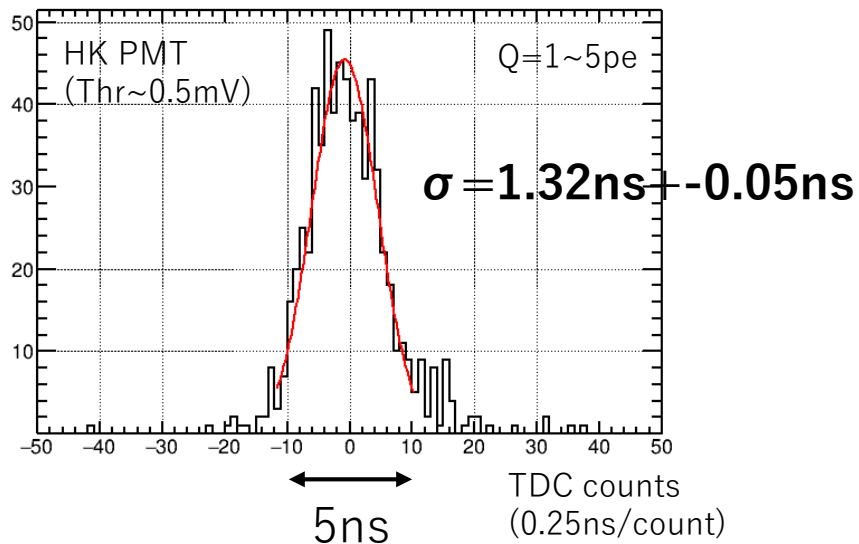
- **Timing measurement (by laser)**

- ✓ make use of calibration laser source in SK tank
- ✓ observed good resolution of HK PMT ~ 1.3ns (SK PMT ~ 2ns)
- ✓ no difference with SK electronics (resolution of electronics ~0.2ns)



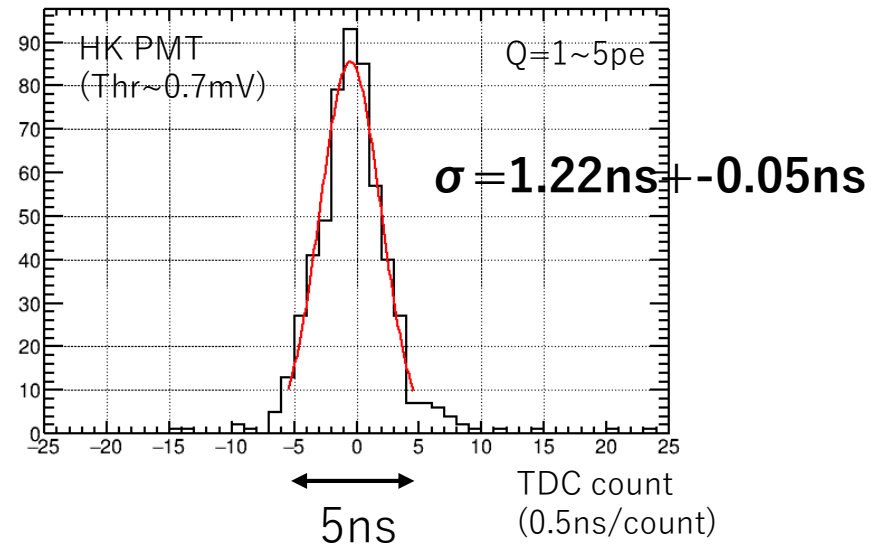
## HK Elec.

### Timing



## SK Elec.

### Timing



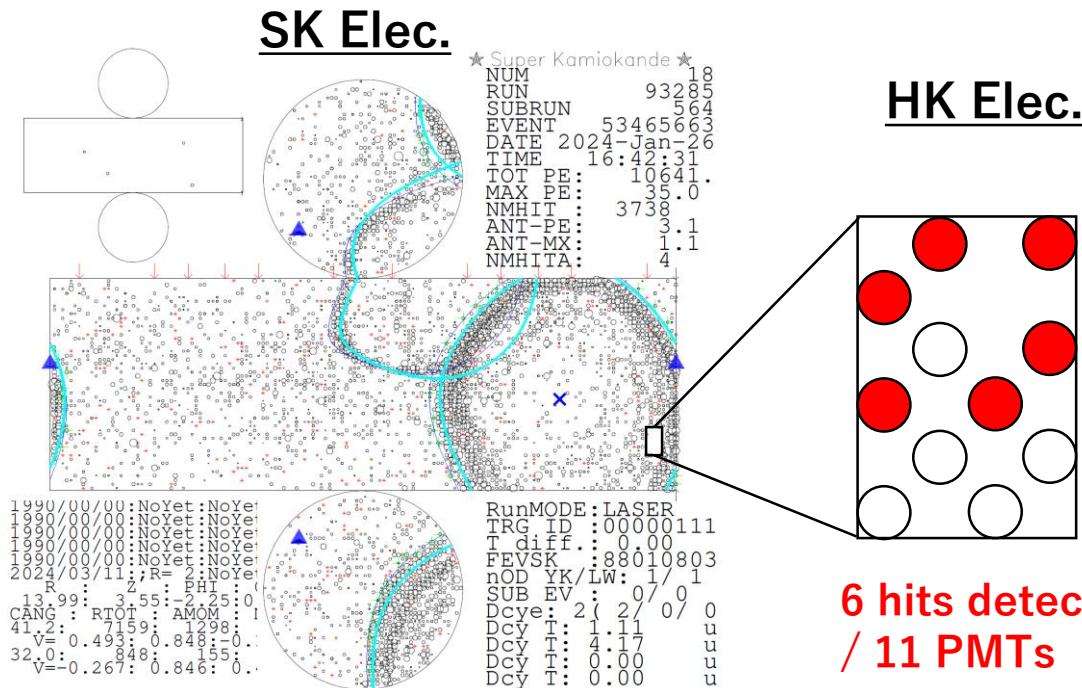
# Neutrino events

## First observation of Neutrino signal events

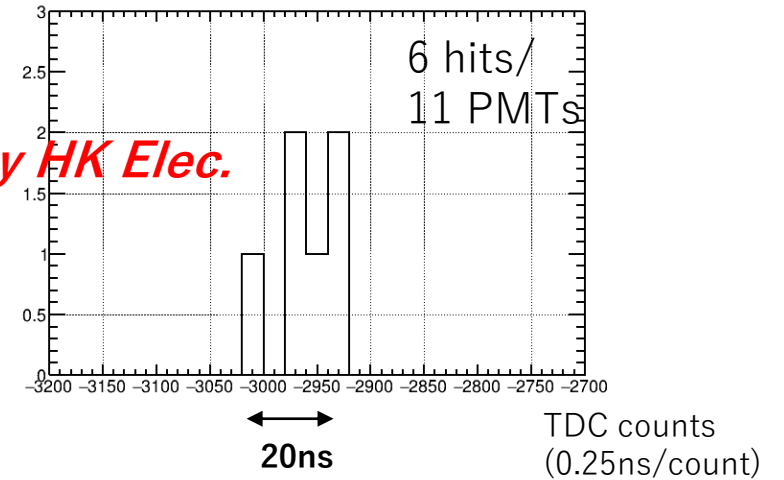
- neutrino events selected by SK Elec. data.  
→ observed coincidence hits in HK elec.

*First observation of Neutrino event by HK Elec.*

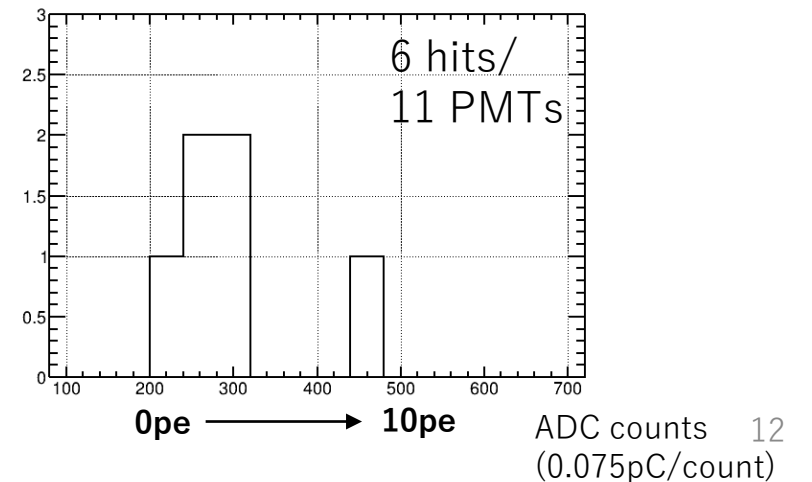
- An example of neutrino event



## Timing



## Charge



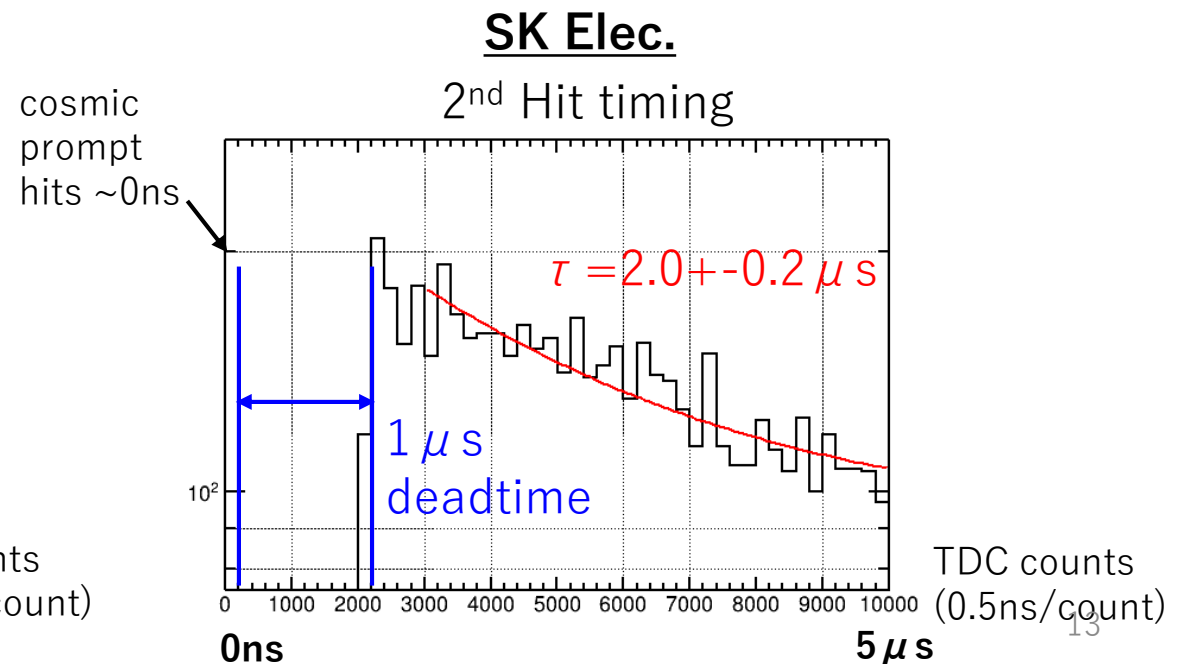
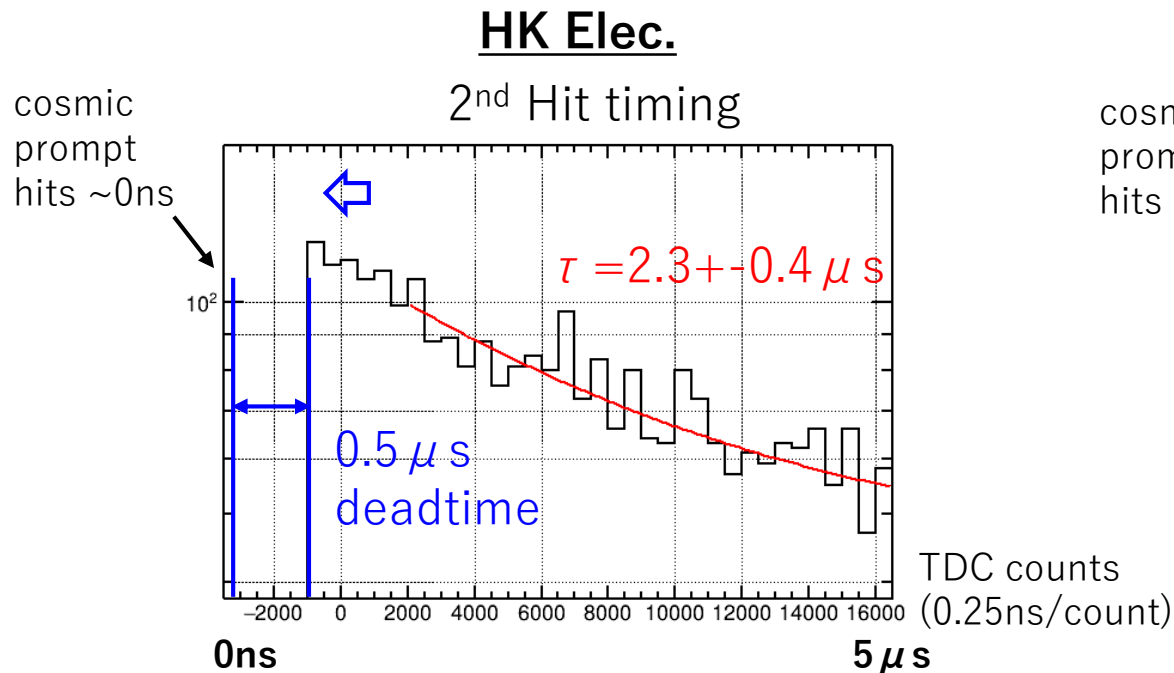
# Consecutive hits

- **Consecutive hits in short period**

✓ important to detect  $\mu$  decay, neutron capture, supernova burst, etc

- **$\mu$  decay in cosmic events**

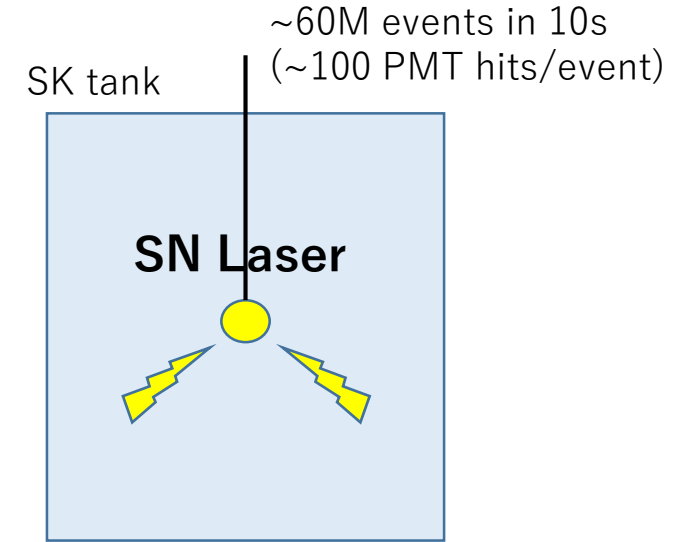
✓ confirmed to detect  $\mu$  decay e hits up to  $0.5 \mu\text{s}$ (deadtime) in HK Elec.,  
improved from  $1.0 \mu\text{s}$ (deadtime) in SK Elec.



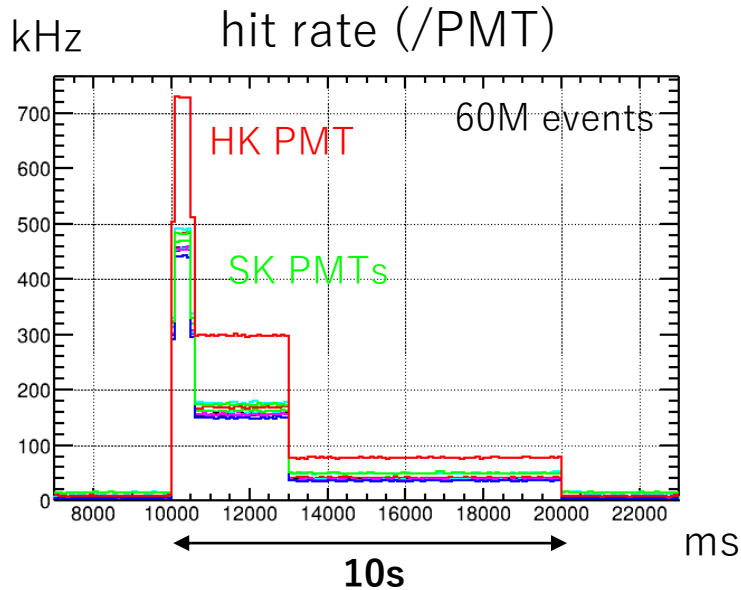
# SN burst test

- **SN burst test by laser**

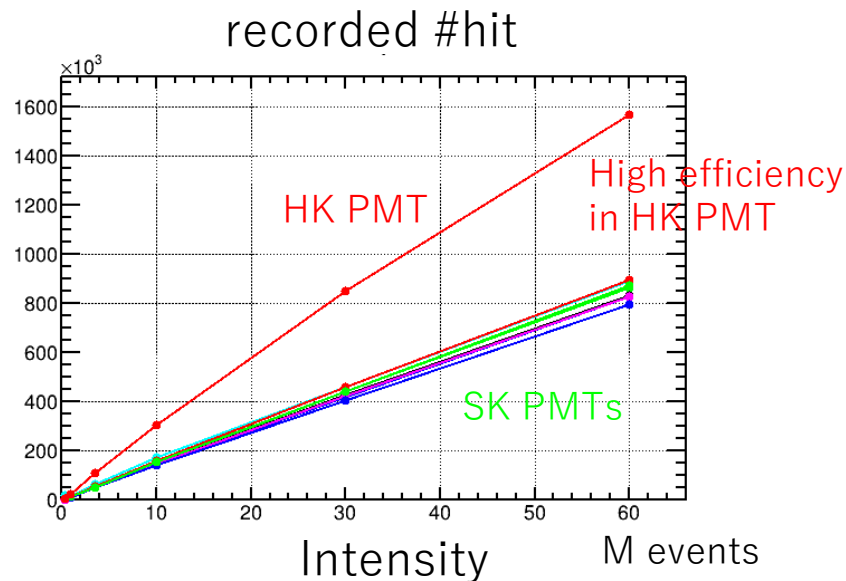
- ✓ emulating SN burst time profile (~10s)
  - ✓ intensity up to 60M events > nearby supernova (betelgeuse)
  - ✓ confirmed to process ~700kHz /channel without error
  - ✓ confirmed to record all 1.6M hits /10s
- (in SK Elec., hits are presampled before buffer full)



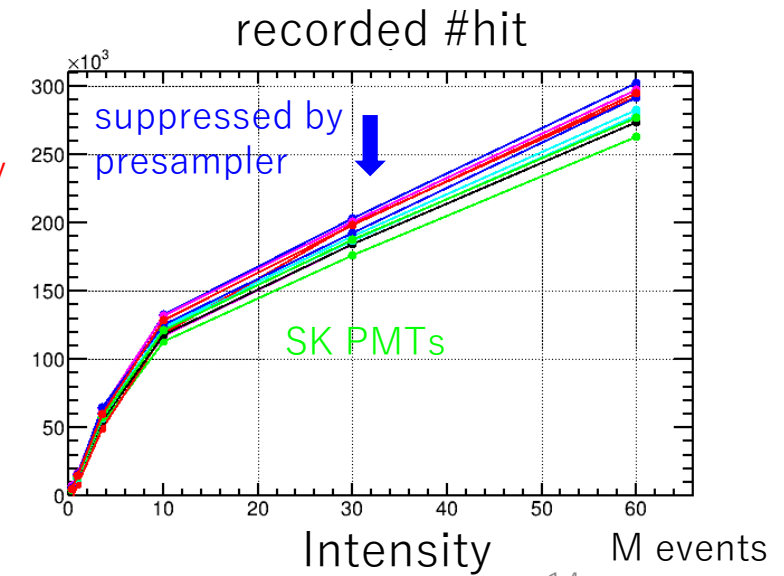
## HK Elec.



## HK Elec.



## SK Elec.



# Summary

- We have developed HK frontend electronics and final tests are on-going before mass production.
- SK provides realistic environment for the final tests.
- HK Elec. worked in SK as expected and observed neutrino events for the first time. (compatible to SK Elec. and improved in some points, deadtime, high rate, etc)
- We are preparing test with full setup of HK Elec. in SK. (including data process board, timing system, LV/HV, etc)