

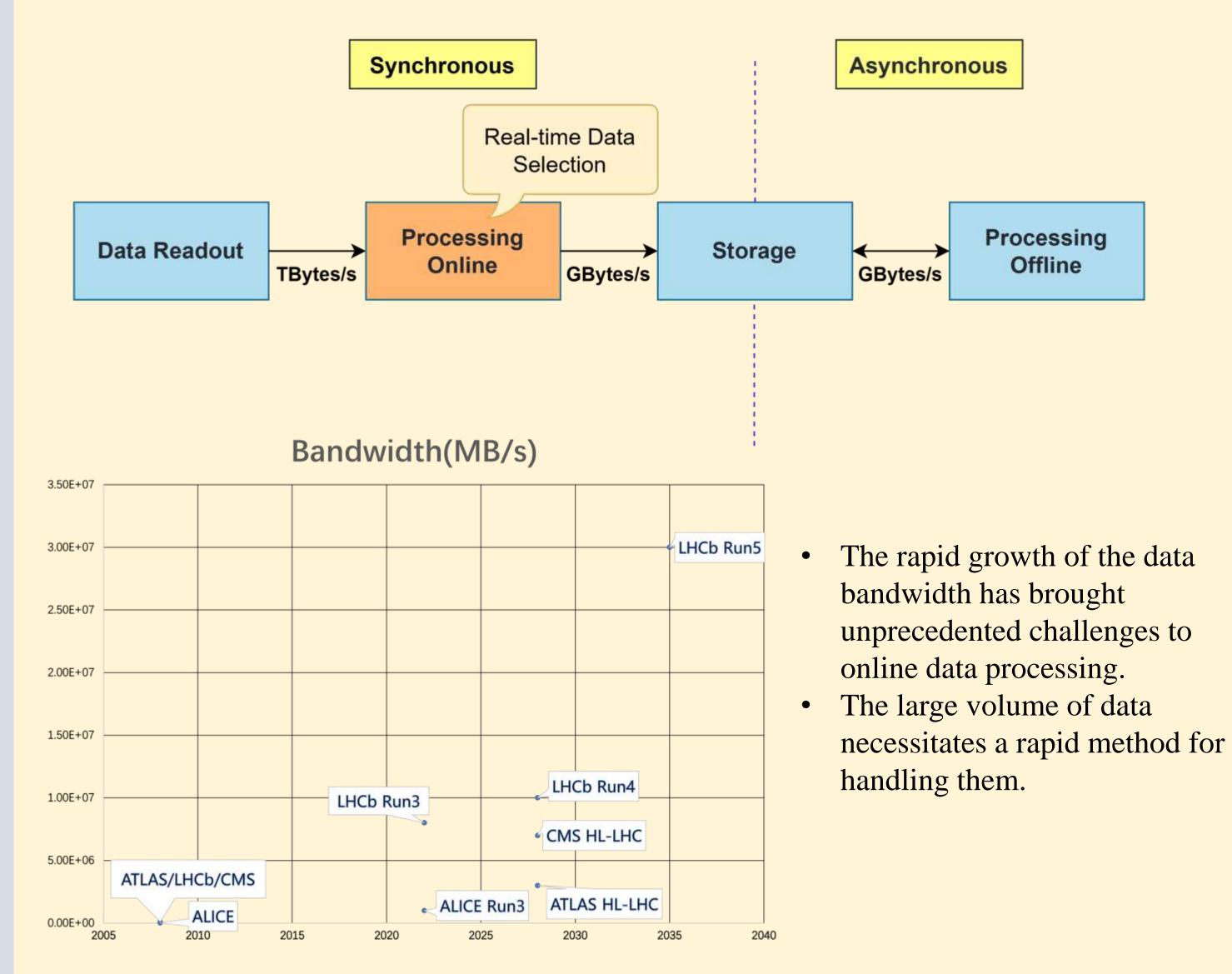
# Research and Application of SIMD-Based Online Data Processing Acceleration Technology

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# MOTIVATION

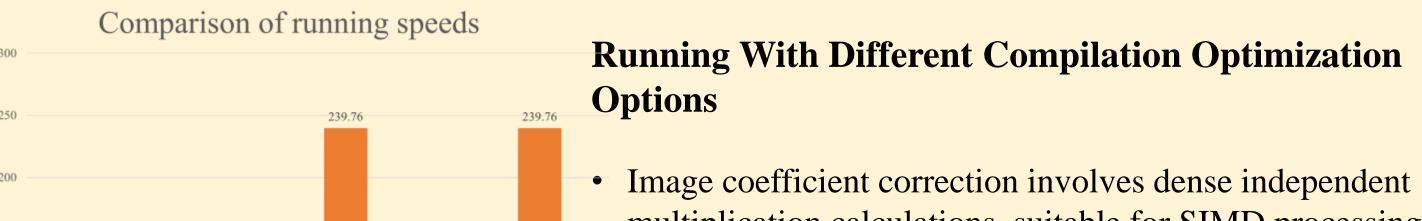
**Online Data Processing Challenge** 





(OS:Linux CentOS 8 CPU:Intel Xeon Platinum 8462Y+ GPU:Nvidia A30)

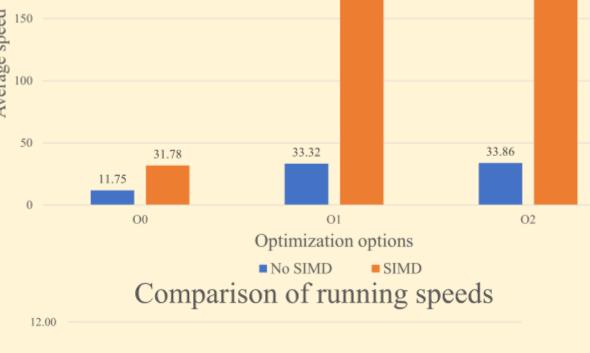
### **Research On CPU SIMD Applied In Image Coefficient Correction**





# INTRODUCTION

**CPU Single Instruction Multiple Data** 





-No SIMD -SIMD The number of threads

multiplication calculations, suitable for SIMD processing.

- The tested pixel array size is  $256 \times 576$ .
- In a single-threaded scenario, the performance improvement reaches a maximum of 719.57%.

#### **Running on different numbers of threads**

- The tested pixel array size is 1000×1000, tested with O0 compilation optimization option.
- The performance improvement is affected by the number of threads, with the highest improvement reaching 293.68%

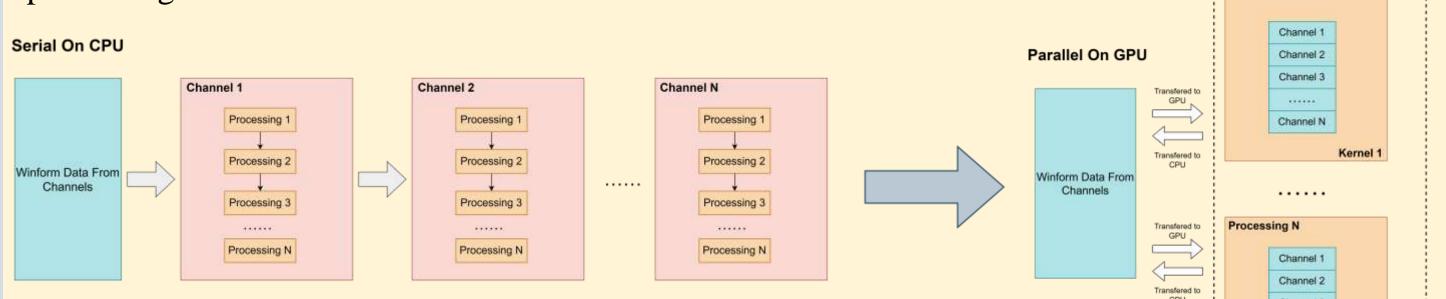
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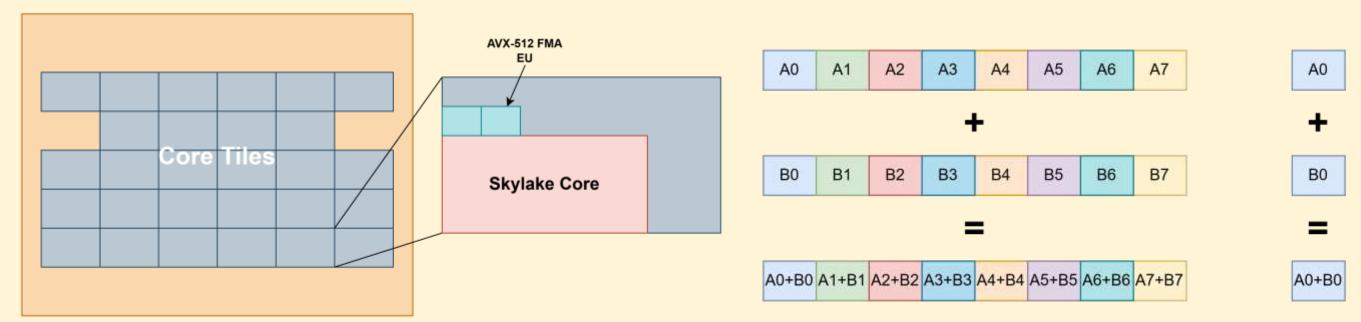
No SIMD SIMD

### **Research On GPU SIMD Applied In Waveform Point Processing**

#### **Waveform Point Processing parallelized on GPU**

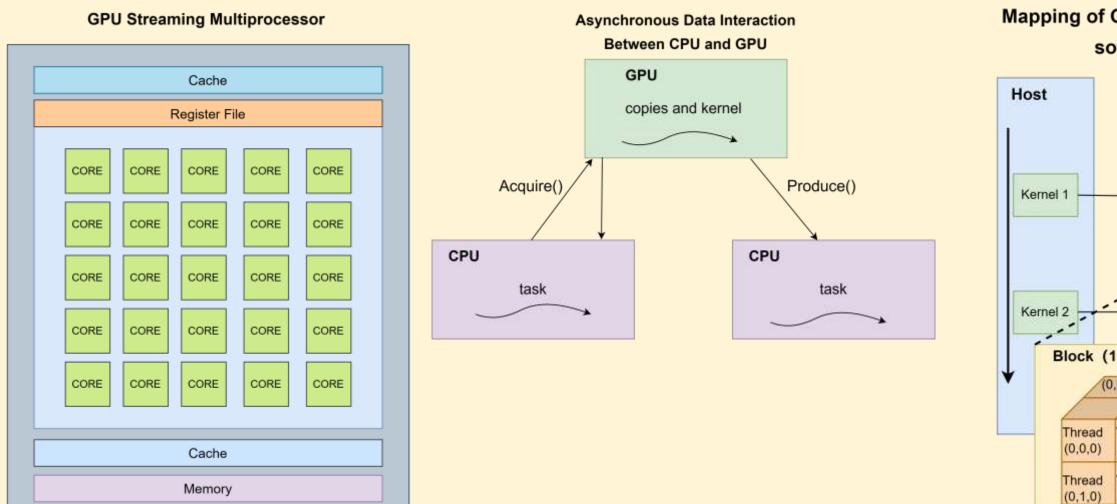
Inspired by JUNO OEC, the implementation of parallelized waveform point processing on GPU is conducted .

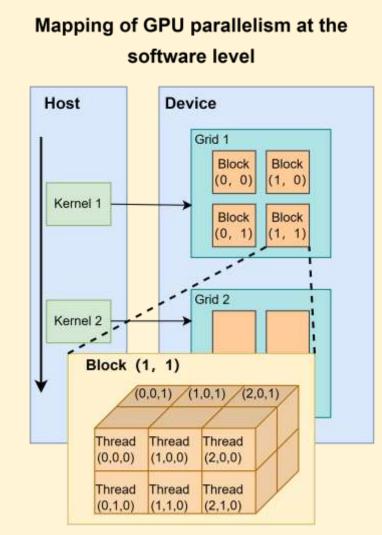




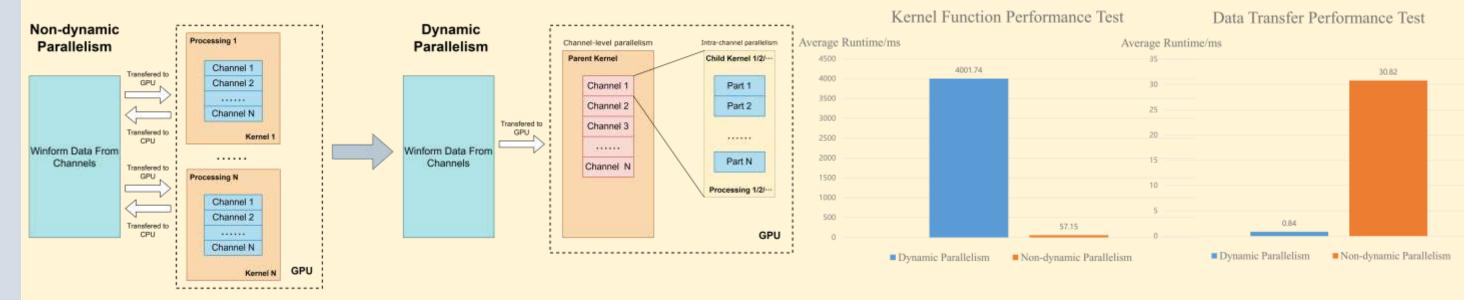
- The selected Intel CPU core is equipped with a dedicated register set for parallel data processing.
- Each register for the Intel AVX-512 instruction set can simultaneously process 8 doubleprecision or 16 single-precision numbers.

### **GPU Single Instruction Multiple Data**





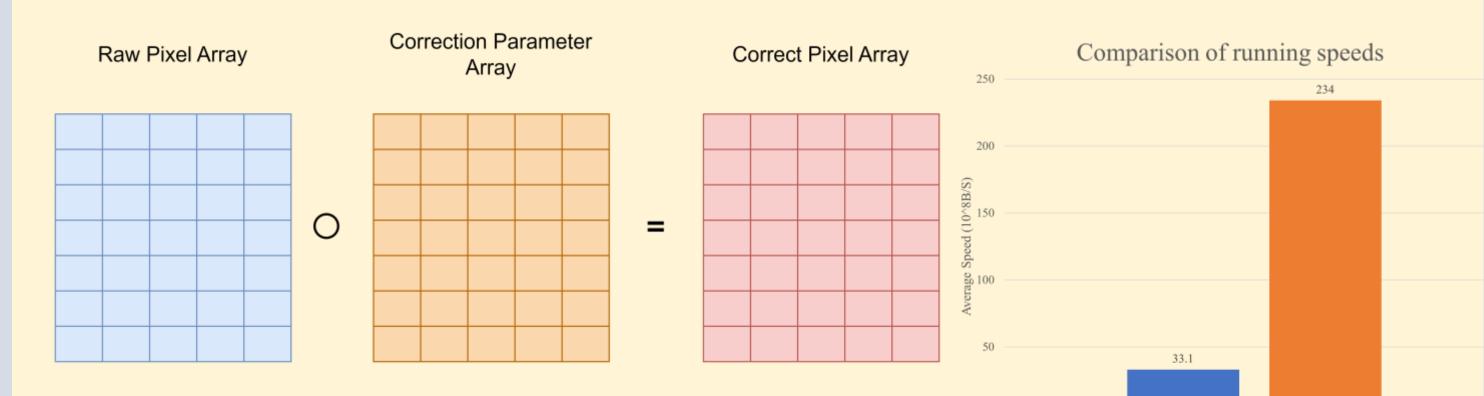
**Research On Dynamic Parallelism Applied In Waveform Point Processing** To try to fully utilize the computational resources of the GPU, the research on dynamic parallelism is conducted.



• According to the test result of processing 1800 channels  $\times$  1000 points, this method is suitable for scenarios where data transfer time accounts for a relatively large proportion.

# IMPLEMENTATION

### SIMD Technology Implemented In Silicon Pixel Detector DAQ



- GPU computing is a variant of SIMD, with more computing cores, achieving highperformance parallel data processing through grid and thread block invocation.
- Typically, heterogeneous computing can be achieved as the CPU asynchronously transfers data with the GPU.
- Combined with the O2 compilation optimization option, the performance of flat field correction processing for 256×576 silicon pixels is improved to 708.09%.

## SUMMARY

- The SIMD (Single Instruction Multiple Data) acceleration technology based on CPU and GPU can effectively accelerate various online data processing scenarios in highenergy physics experiments, and has great potential for practical application.
- The subsequent plan is to further explore the potential application of SIMD acceleration in online data processing, in order to improve the real-time performance and throughput of online data processing, and provide effective solutions for future large-scale high-energy physics experiments.

