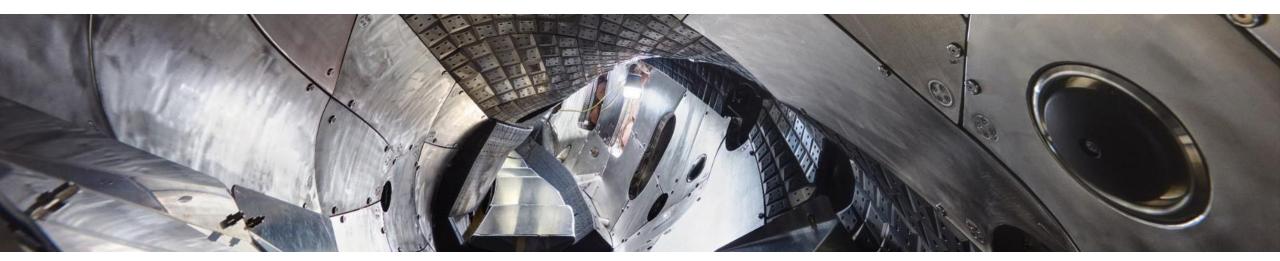
### Short oral ID #19:

# Status and further development of the Trigger Time Event system for fusion experiment Wendelstein 7-X

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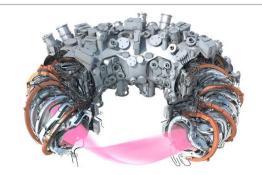
#### **Outline**

- > Introduction
- > Status of Wendelstein 7-X
- > TTE system

## **Introduction: W7-X operation status**

## Status of the superconducting stellarator experiment W7-X:

- 4 operational phases have been successfully completed, in which a total of 6,559 plasma discharges with a total plasma time of 473 min have been generated.
- The next operational phase (OP2.2) will begin with scientific plasma operation in September 2024.
- Commissioning of the Wendelstein 7-X facility has been underway since the beginning of 2024.
- The first plasma discharge in OP2.2 will be in June 24.
- The Trigger Time Event (TTE) system of W7-X as part of the CoDa system has functioned successfully during the operating phases to date.



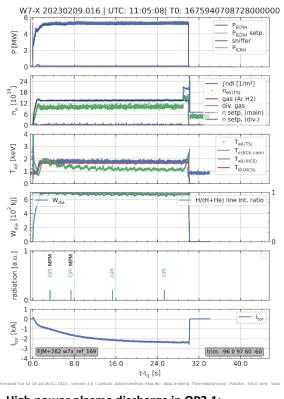
CAD model of W7-X

2023



View on W7-X in the torus hall





Plasma discharge W7-X control room High power plasma discharge in OP2.1: (high power 5 MW / 30 s / 170 MJ) Operational phase OP2.2 **OP2.2 Plasma operation OP2.2 Plasma operation** Maintenance phase MP2.2 **Device commissioning** Operation pause OP2.2/SO-1 OP2.2/SO-2 25.02.24 - 18.06.24 15.07.24 - 30.08.12.24 06.06.2023 - 24.01.24 19.06.24 - 12.07.24 02.09.24 - 13.12.24

2024

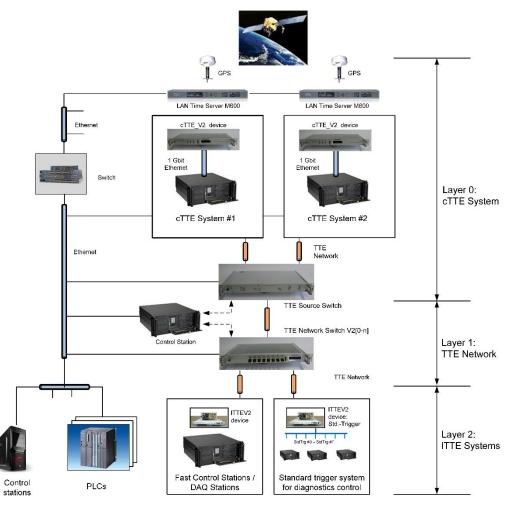
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## **TTE system functions**

#### **Functions of the TTE system:**

- Generation of a global time for all CoDa components of W7-X,
- II. Synchronization of all local time counters of the ITTE systems of the technical components and diagnostics,
- III. Providing event messages processing and standard trigger signals,
- IV. Providing time and trigger related functions like:
  - 1. Time capturing,
  - 2. Pulse sequence generation,
  - 3. Time delays,
  - 4. Impulse counter,
  - 5. Event message / trigger signal processing.

#### TTE system architecture



## TTE system modifications / updates

#### Development of a new cTTE\_V2 device:

- Based on ITTEV2 FPGA card,
- 1 Gbit/s Ethernet interface,
- Oven stabilized controllable oscillator,
- Bidirectional optical network TTE network interface:
  - Send synchronization and time information,
  - Send / receive event messages,
- Transmission delay measurement mode,
- Update FPGA configuration of ITTE\_V2 device:
  - Bug fixing,
  - New pulse sequence generator device,
- TTE network switch device:
  - 8 uplink ports,
  - 1 downlink port,
- IRIG-B converter device:
  - Conversion of cTTE time information into an IRIG-B signal for synchronization of DAQ electronics.



cTTEV2 device: front view.



cTTEV2 device: top view.



TTE network switch device.



IRIG-B converter device.

### Thanks for attention!

# Please contact me during the poster session! Poster session, Poster ID #19

