

24th IEEE Real Time Conference - ICISE, Quy Nhon, Vietnam

Wednesday, 24 April 2024

Invited Talk, Oral presentations, Mini-Orals (09:00 - 12:35)

time	[id] title	presenter
09:00	[160] End-to-end Codesign of Hessian-aware Quantized Neural Networks for FPGAs	LONCAR, Vladimir
09:40	[152] Science Rate Acceleration with ESnet-JLab FPGA Accelerated Transport Load Balancer	GOODRICH, michael
10:00	[177] Development of the Proton Computed Tomography (pCT) prototype	SONGMOOLNAK, Arnon
10:20	Coffee Break	
10:50	[226] Mini Orals Session III	
11:35	[35] Enhancing Real-time Data Monitoring Display through Video Streaming Technology	TAN JEROME, Nicholas
11:55	[127] FPGA Tracking with oneAPI	HENNESSY, Karol
12:15	[179] Studies on track finding algorithms based on machine learning with GPU and FPGA	DI BELLO, Francesco Armando