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NIM+: an FPGA-based Replacement to Legacy NIM in Test Beams +

Monday, 8 February 2021 18:00 (20 minutes)

An FPGA-based system has been developed in collaboration with FNAL and CERN to eliminate legacy NIM units and crates currently used in test beams, cosmic test telescopes, etc. Eight PM or SiPM inputs are conditioned by a Lemo-fed daughter card to a Zedboard, driving 4 NIM- and 4 TTL- level coupled outputs. Any Boolean combination of inputs can be selected for the outputs. All is controlled by a GUI on a laptop, designed to be operated by a STEM undergraduate, with no recourse to the internal VHDL.

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