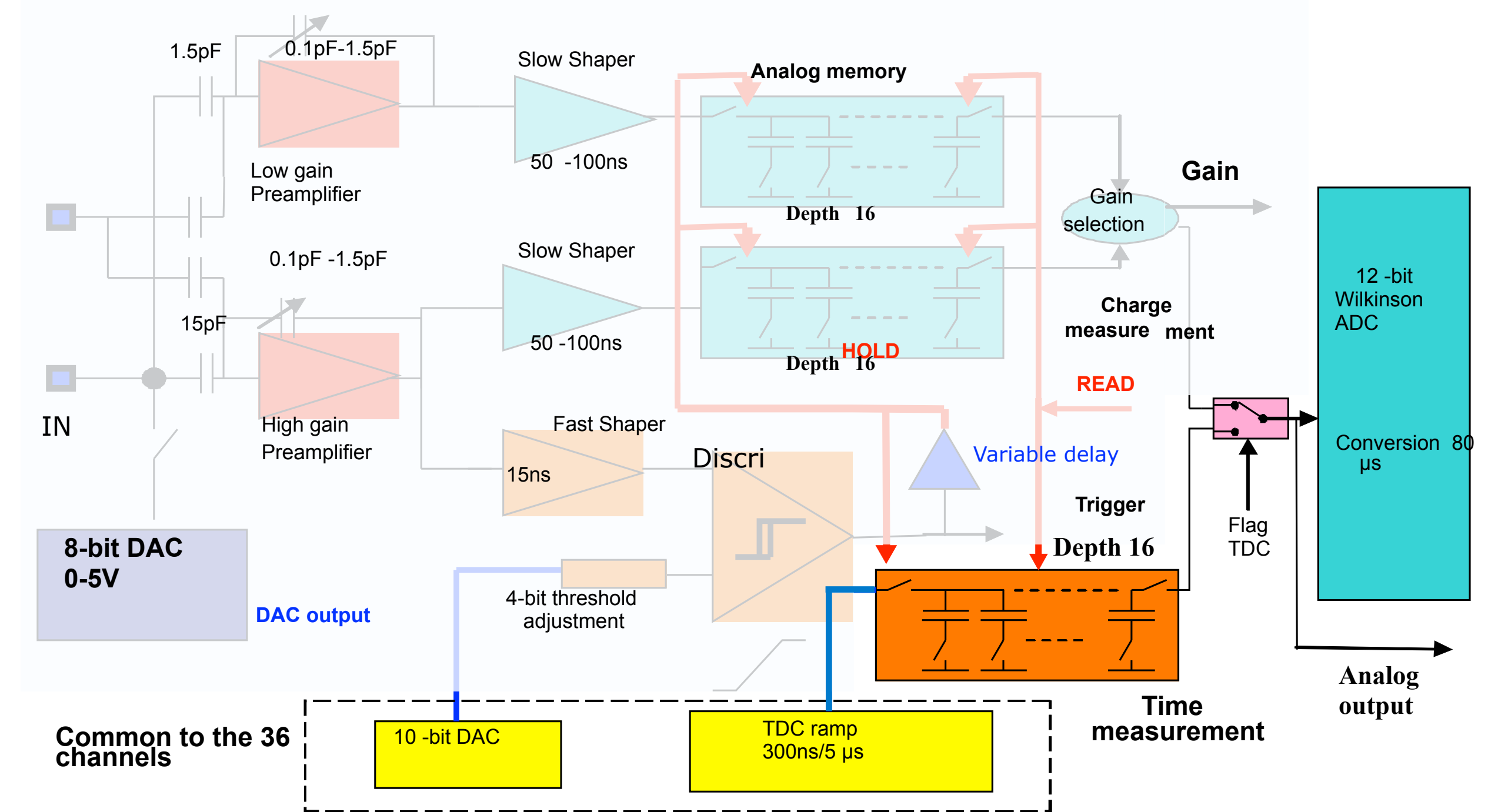
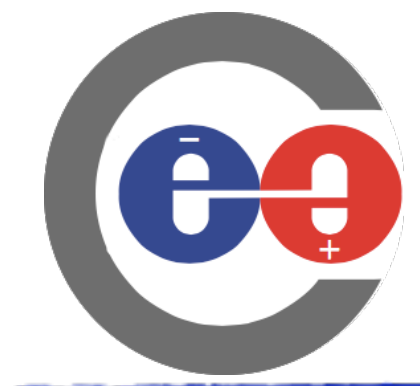


Timing in the AHCAL: SPIROC2E

SPIROC2E provides:

- Ramp TDC for single channel timing
- 16 switched capacitor memory cells per channel
- Clock speed of 250kHz (test beam mode) and 5MHz (ILC mode)

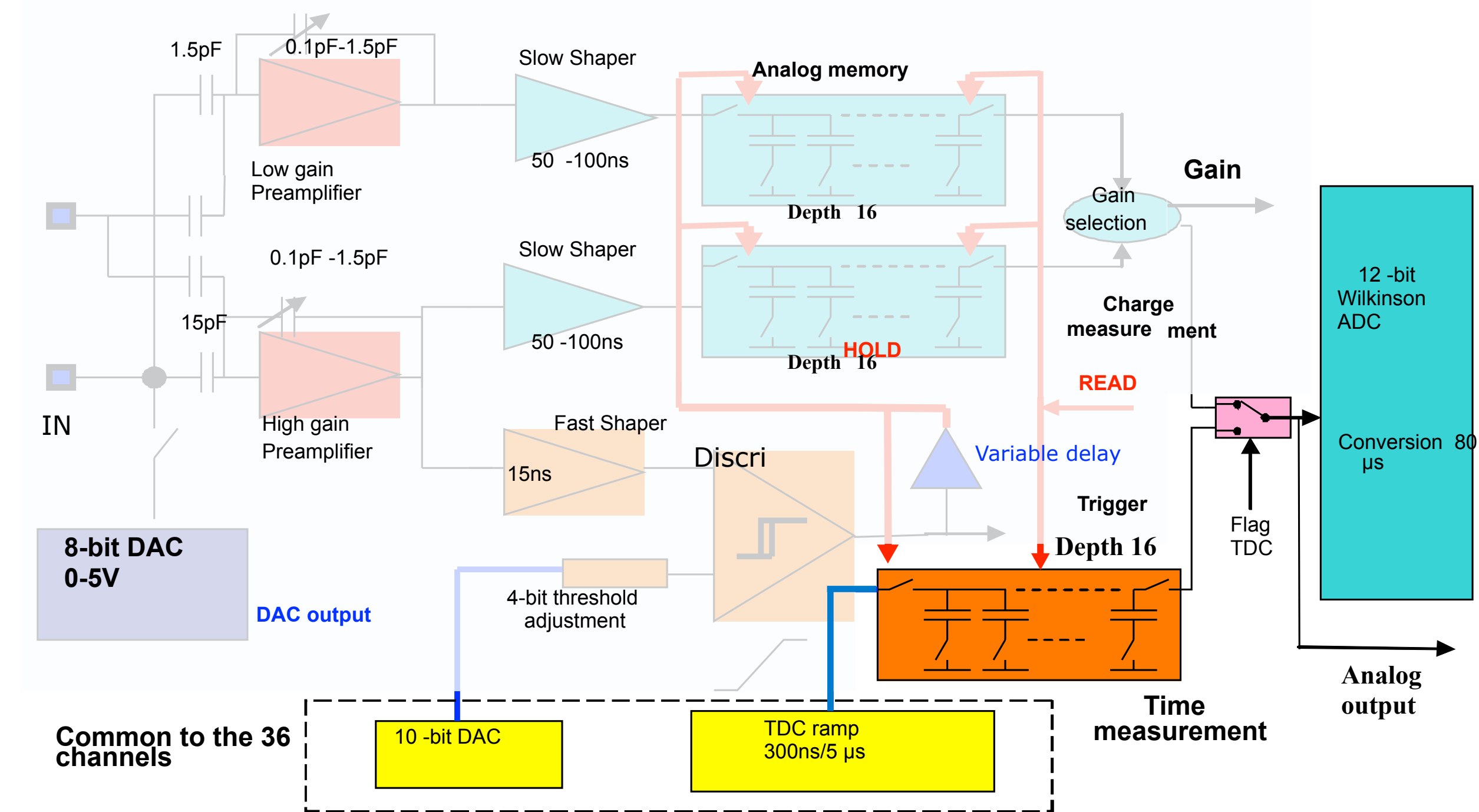




Timing in the AHCAL: SPIROC2E

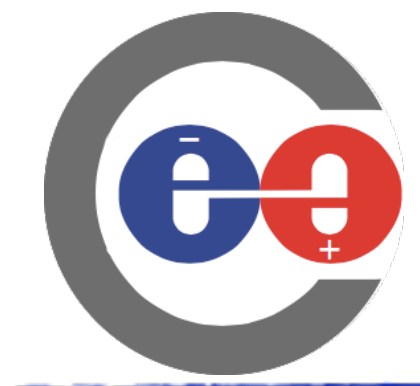
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Time resolution of the electronics:

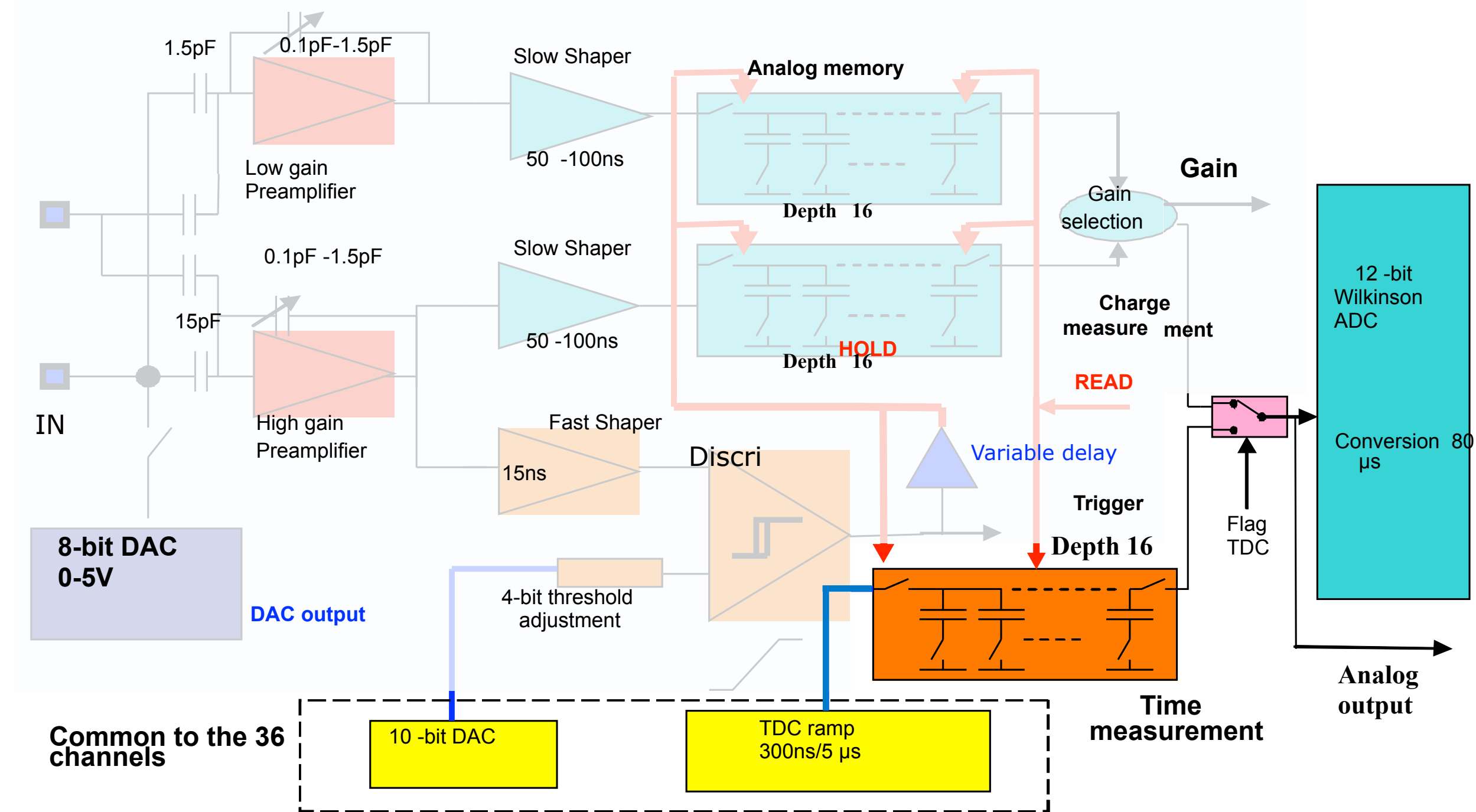
- 204.3ps (ILC mode)
- 2.7ns (test beam mode)



Timing in the AHCAL: SPIROC2E

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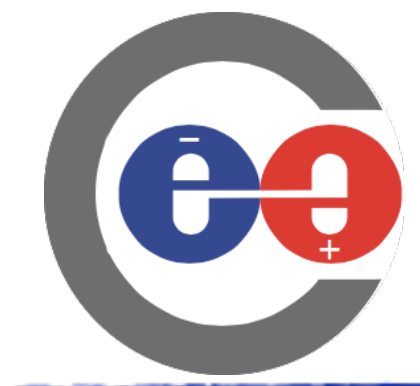
- 204.3ps (ILC mode)
- 2.7ns (test beam mode)

TDC Slope:

- ~ 80 ps / TDC bin (ILC mode)
- ~ 1.6 ns / TDC bin (test beam mode)



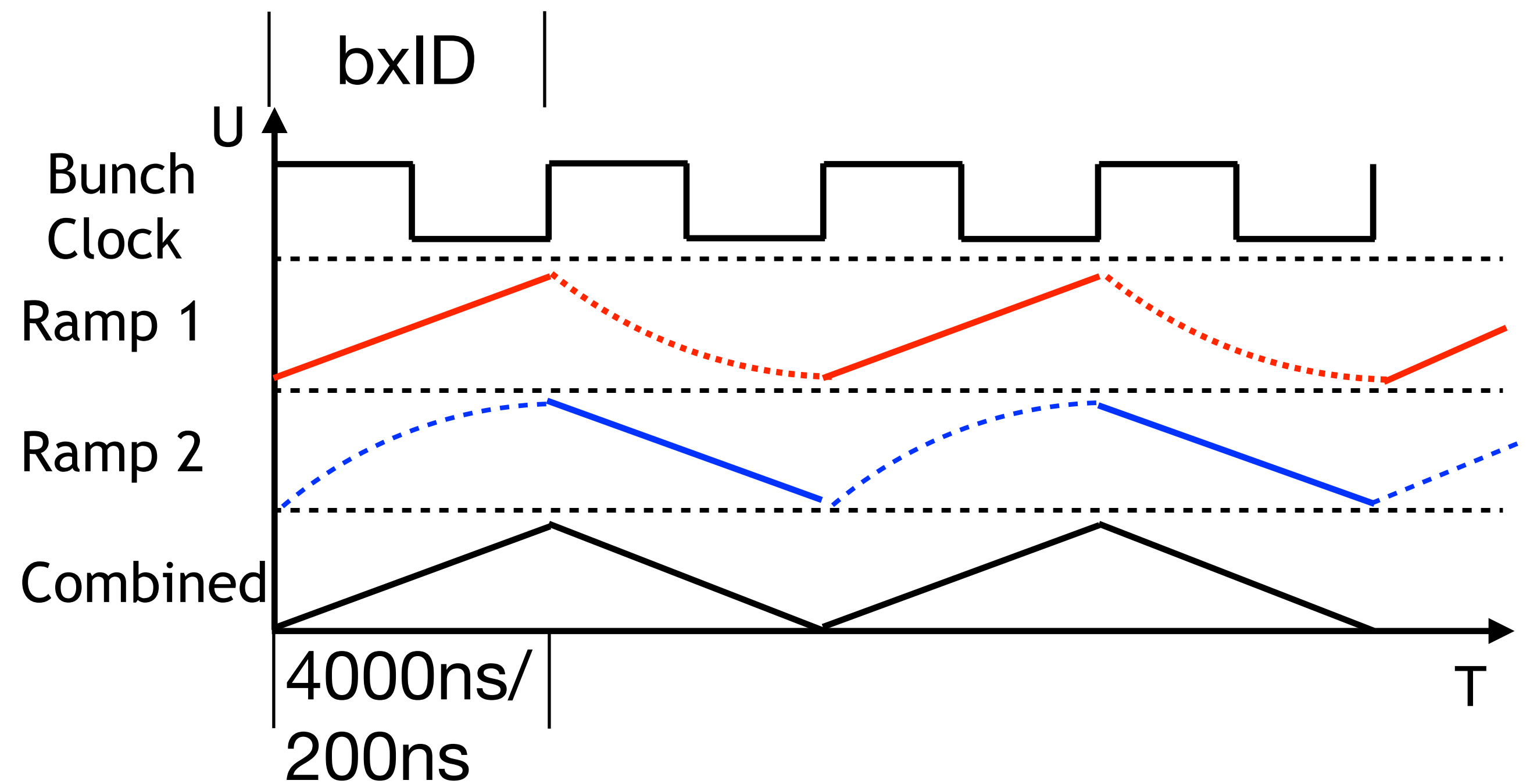
Time Calibration

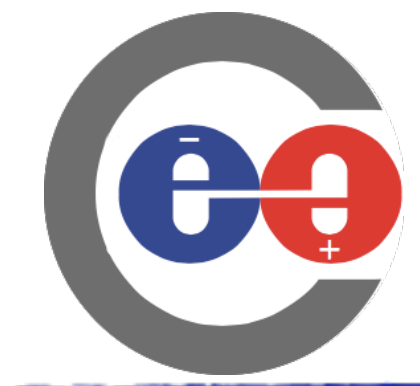


Time Calibration

Time measurement with SPIROC2E:

1. External trigger with $\sim 1\text{ns}$ resolution
2. Ramp up voltage during one bunch crossing ID

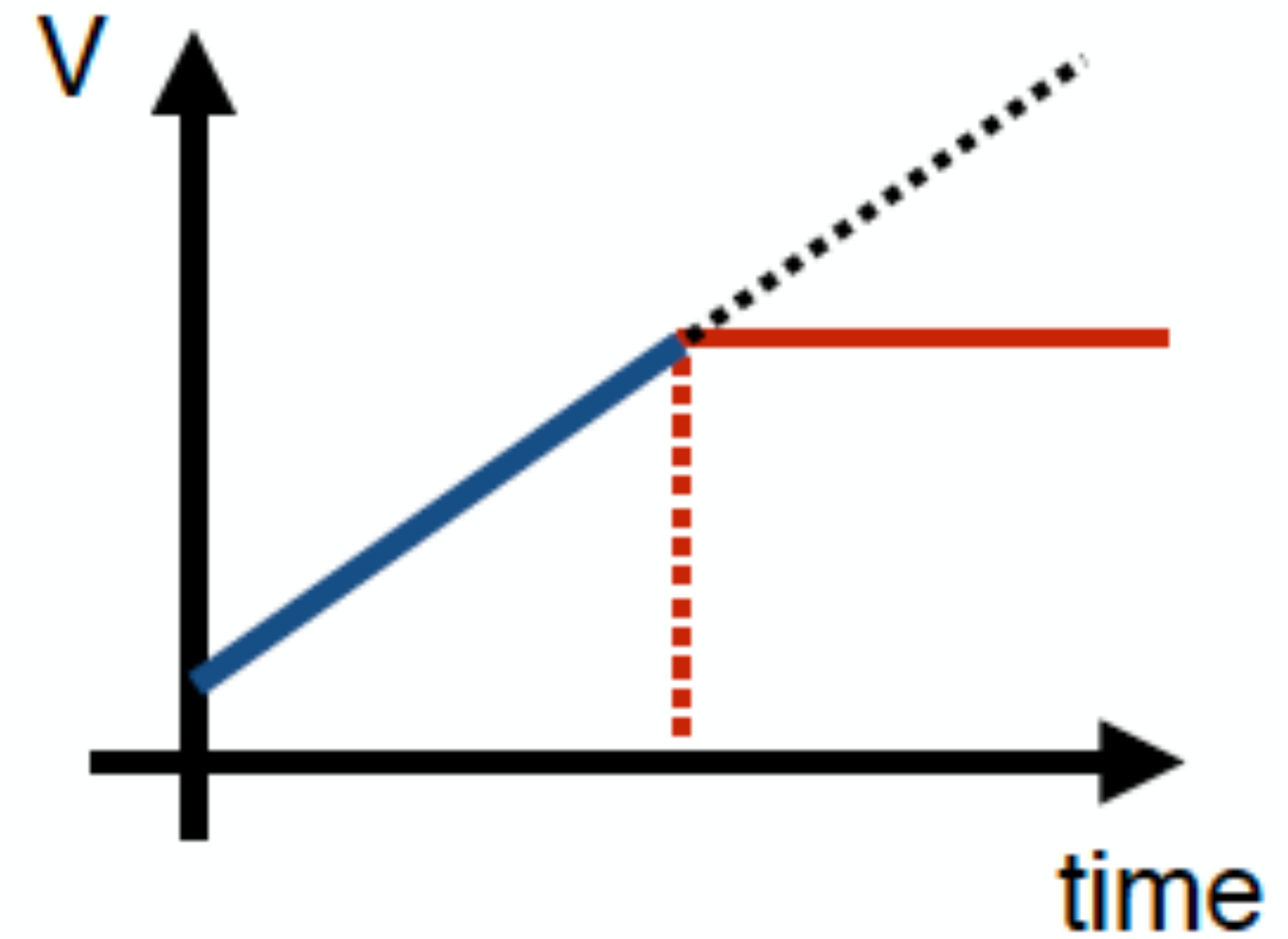


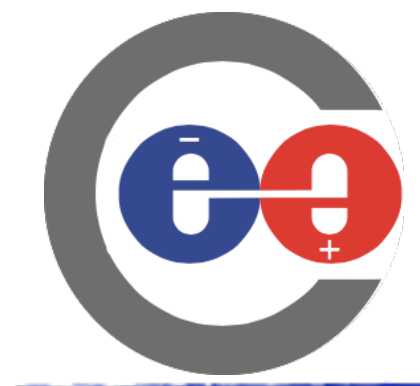


Time Calibration

Time measurement with SPIROC2E:

1. External trigger with ~ 1 ns resolution
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3. On hit, the current voltage is stored in one of 16 memory cells

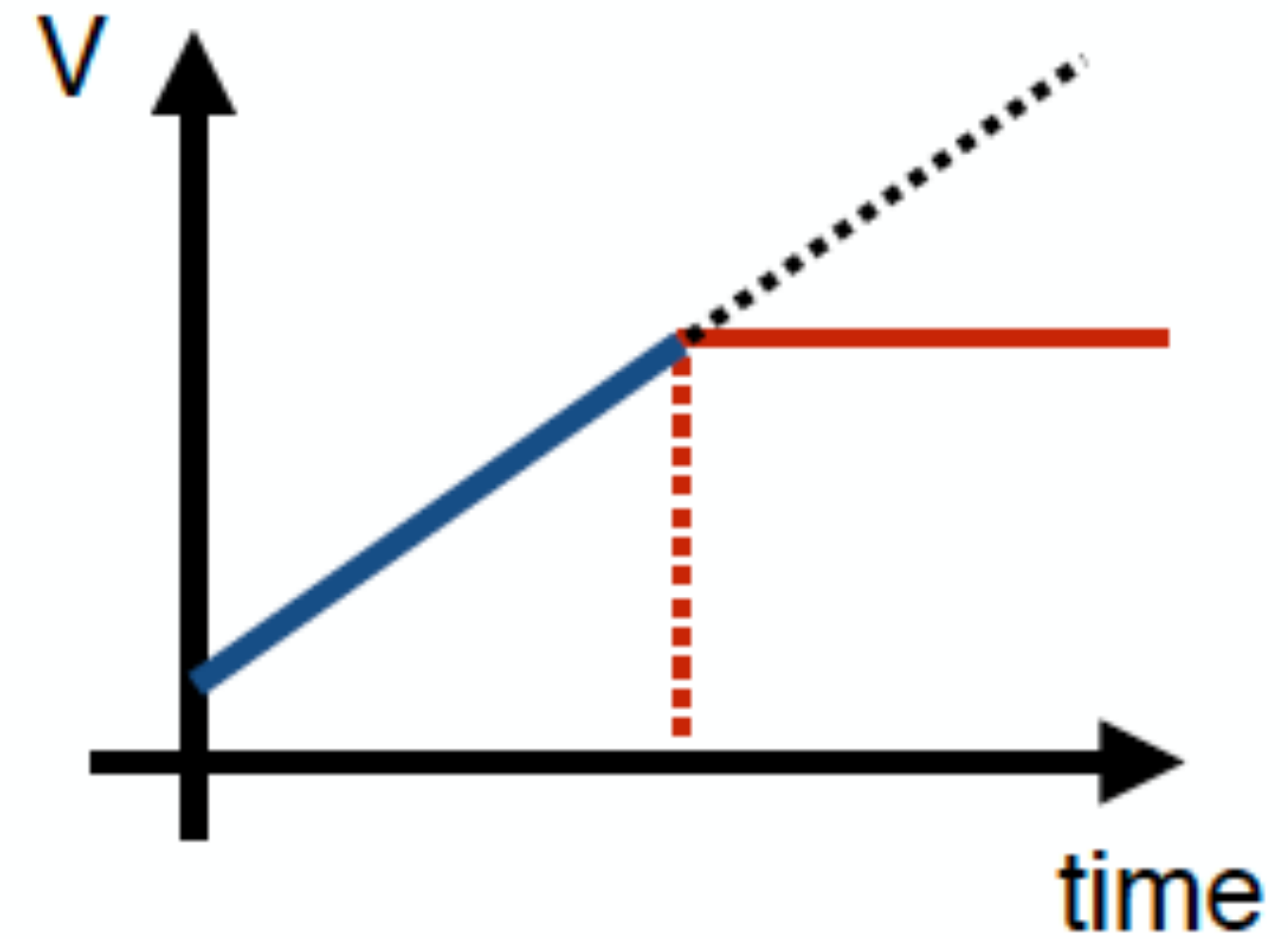


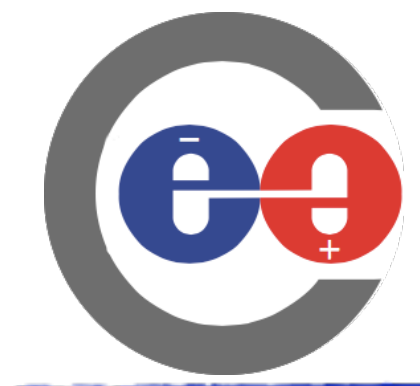


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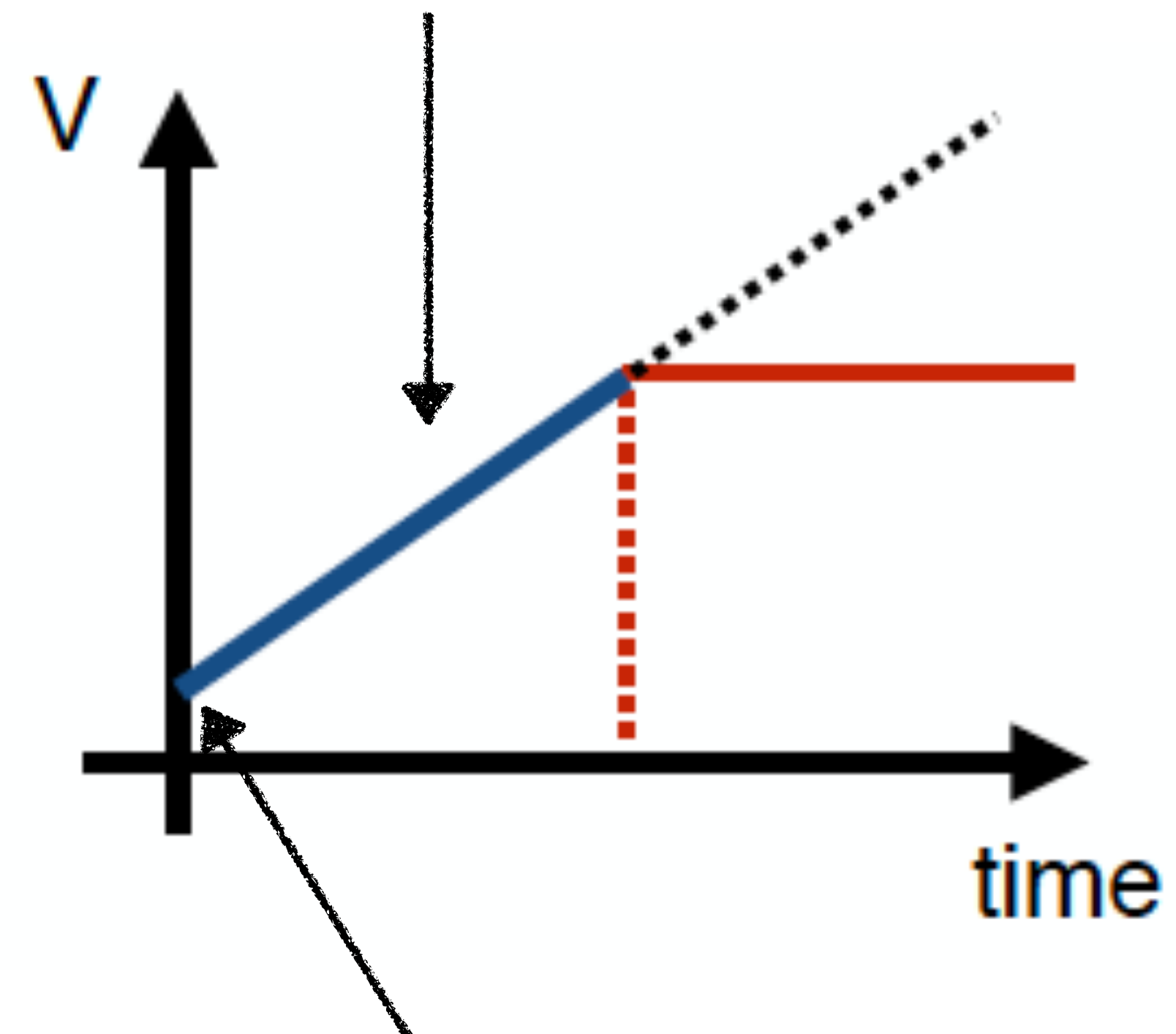


Time Calibration

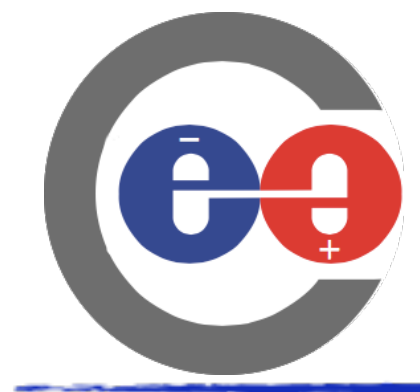
Time measurement with SPIROC2E:

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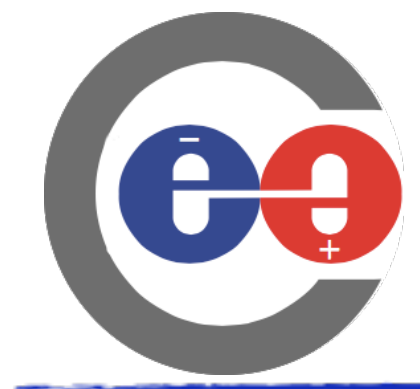
Slope is common to all channels on a chip



Offset is extracted for every memory cell

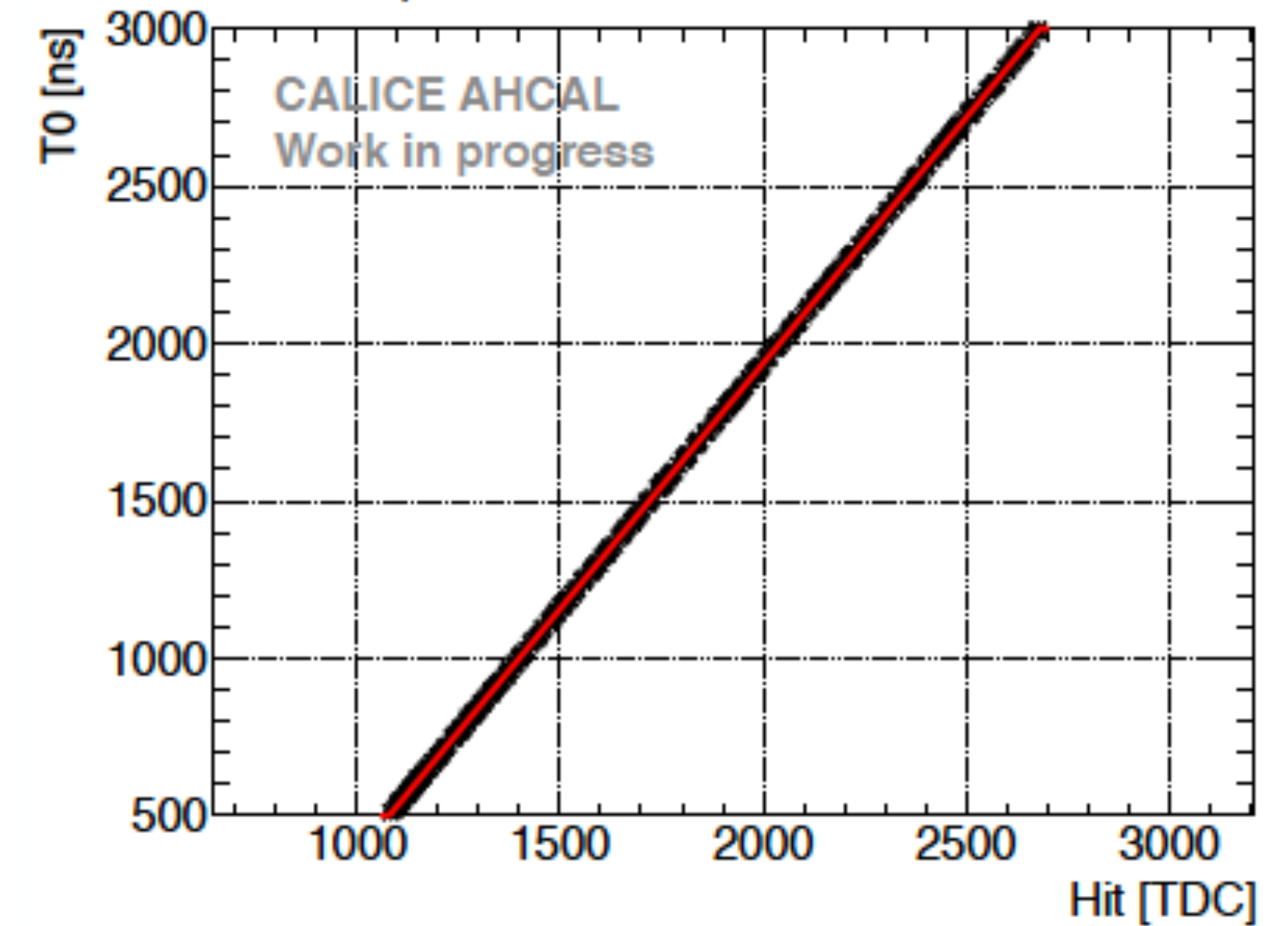


AHCAL Timing Performance



AHCAL Timing Performance

1. Extract slope by plotting trigger time against TDC readings
2. Fit with linear function

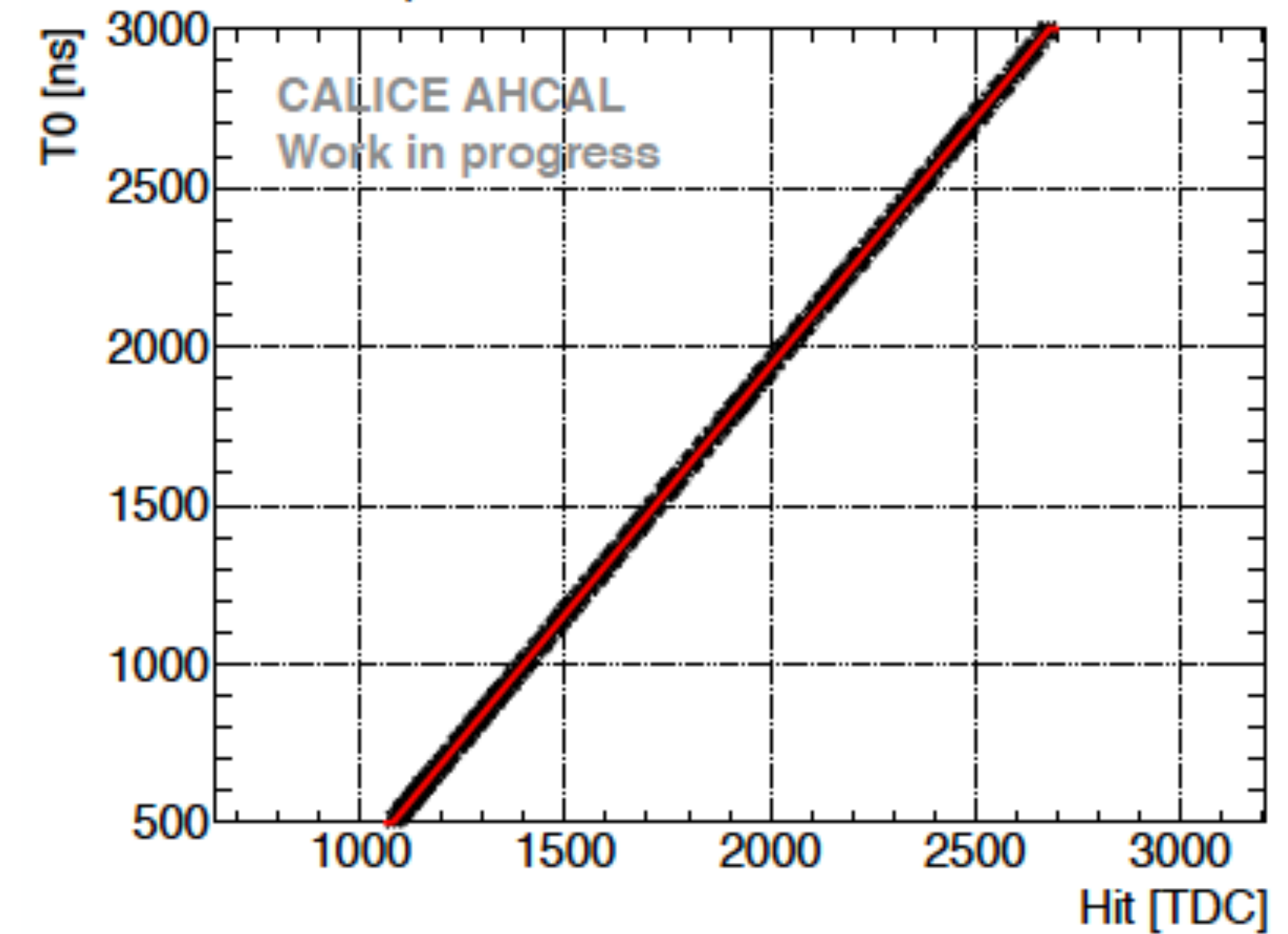




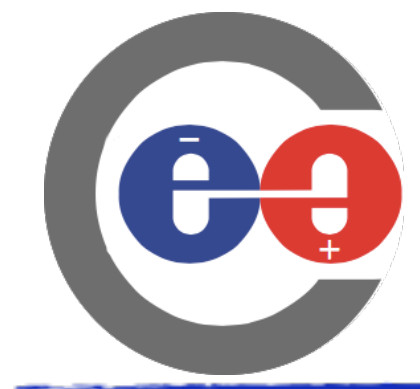
AHCAL Timing Performance

1. Extract slope by plotting trigger time against TDC readings
2. Fit with linear function
3. Calculate hit time by

$$t_{hit}[\text{ns}] = \text{TDC}_{hit} \cdot \text{Slope} \left[\frac{\text{ns}}{\text{TDC}} \right] + \text{Offset} [\text{ns}] - T_0$$



Design goal: ~1ns

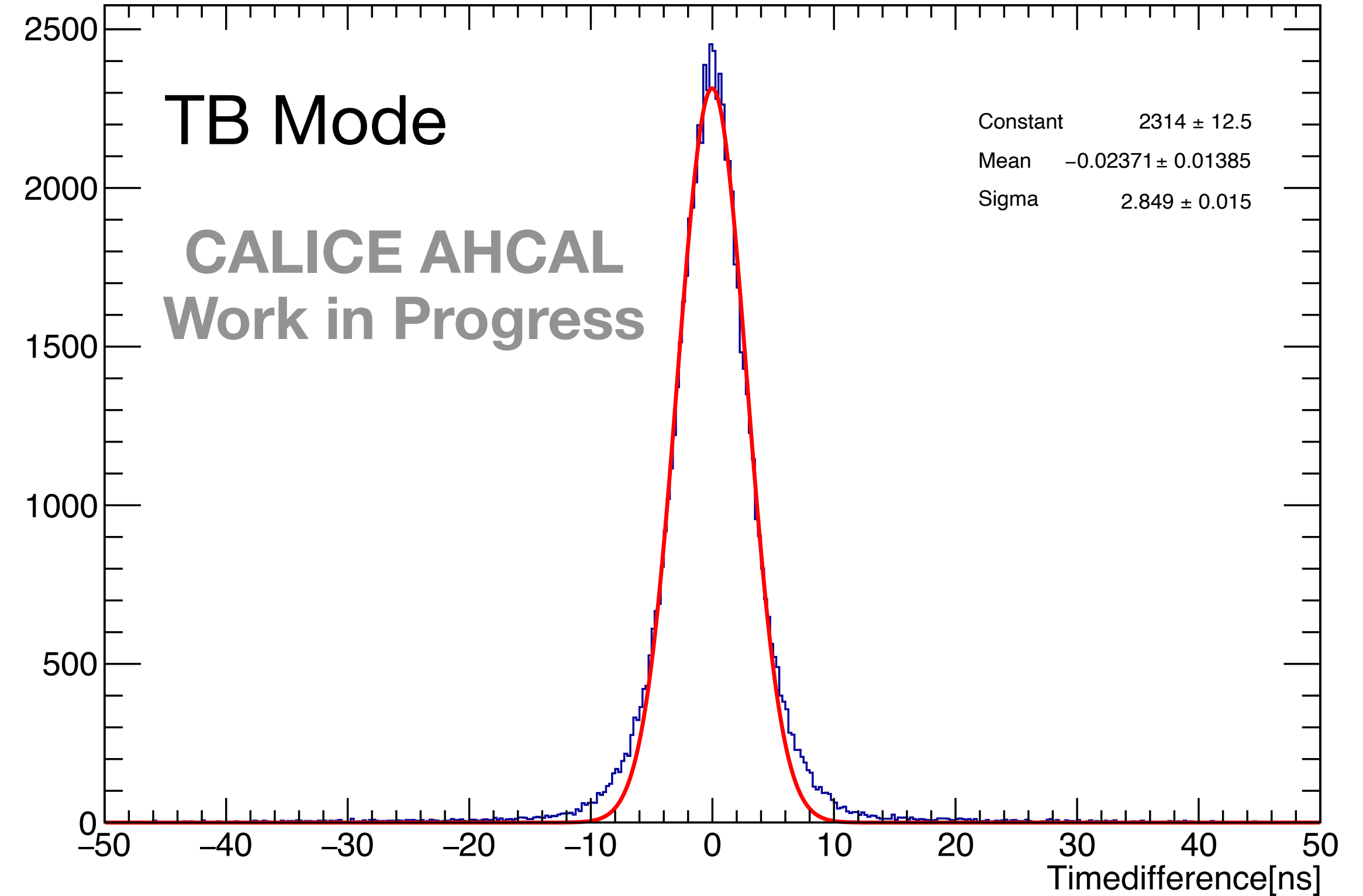


AHCAL Timing Performance

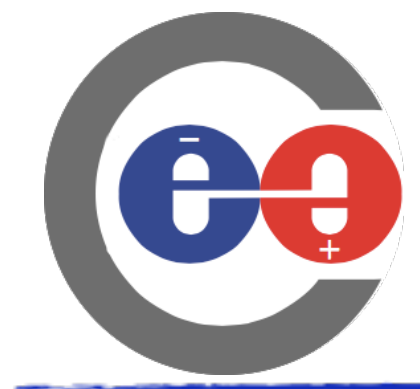
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Design goal: ~1ns



Single channel resolution: $2.859/\sqrt{2} = 2.014\text{ns}$

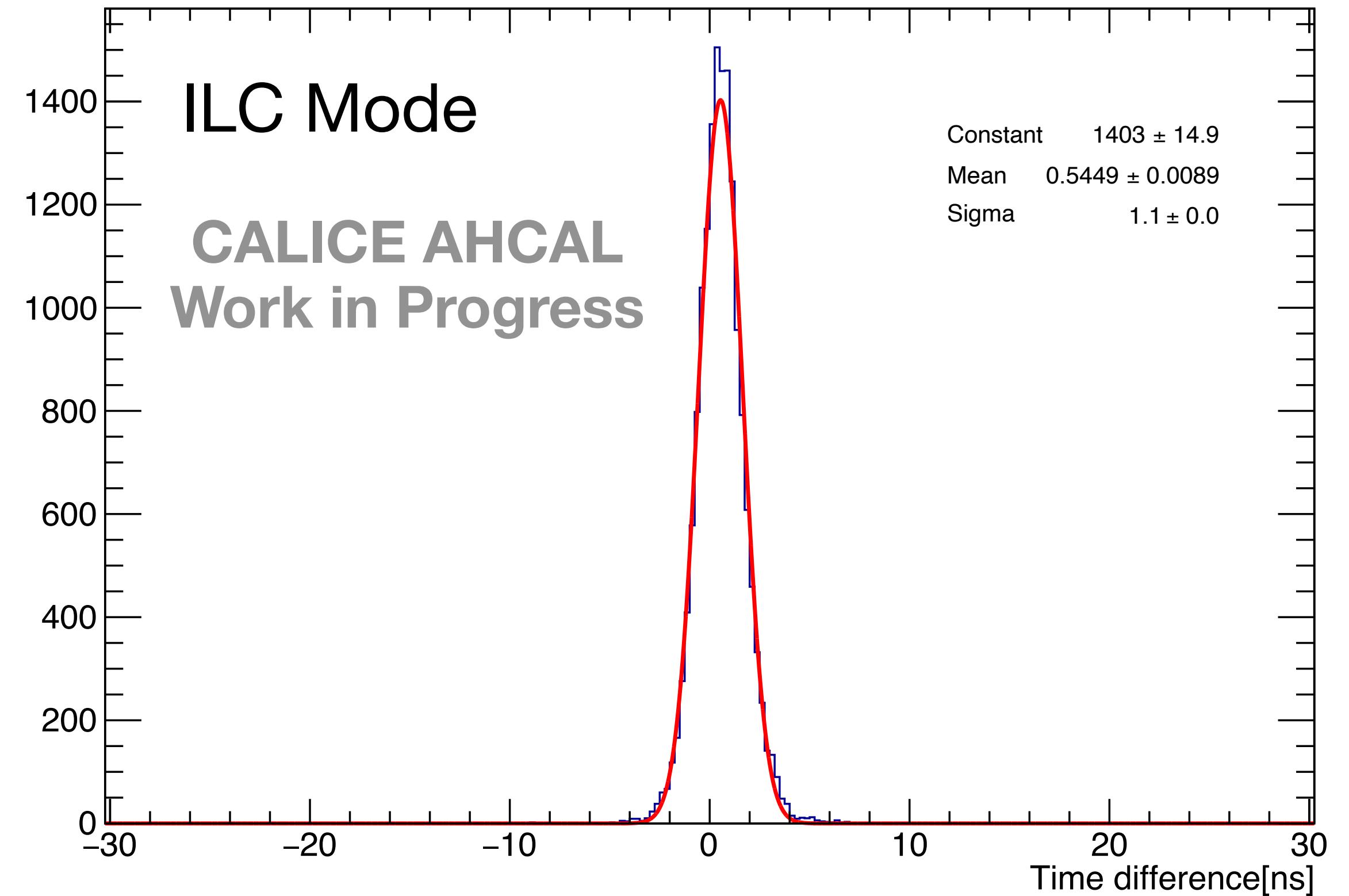


AHCAL Timing Performance

1. Extract slope by plotting trigger time against TDC readings
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$$t_{hit}[\text{ns}] = \text{TDC}_{hit} \cdot \text{Slope} \left[\frac{\text{ns}}{\text{TDC}} \right] + \text{Offset} [\text{ns}] - T_0$$

Design goal: $\sim 1\text{ns}$



Single channel resolution: $1.1/\sqrt{2} = 0.78\text{ns}$

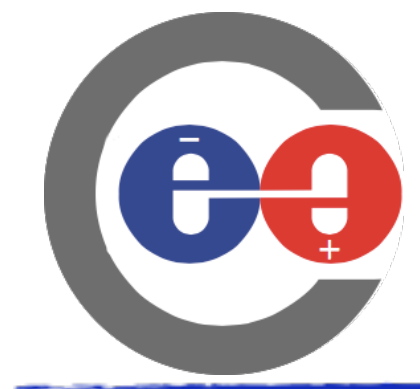


Scintillator Timing Setup



Scintillator Timing Setup

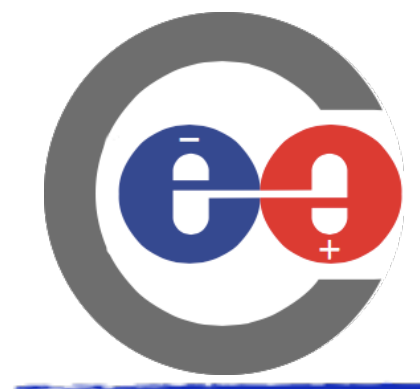
Goal 1: Measure the time resolution of the SiPM-on-Tile technology:



Scintillator Timing Setup

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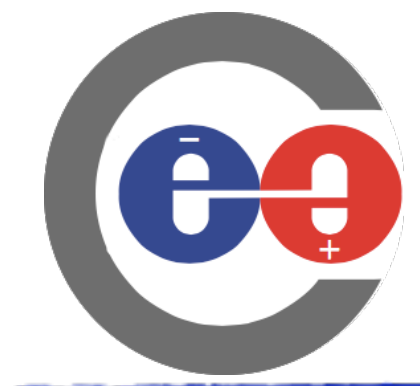
- Independent of the AHCAL electronics and DAQ



Scintillator Timing Setup

Goal 1: Measure the time resolution of the SiPM-on-Tile technology:

- Independent of the AHCAL electronics and DAQ
- In a simple but modular setup



Scintillator Timing Setup

Goal 1: Measure the time resolution of the SiPM-on-Tile technology:

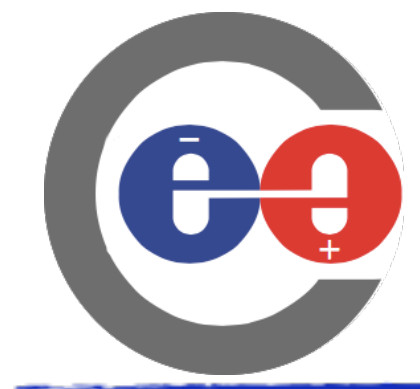
- Independent of the AHCAL electronics and DAQ
- In a simple but modular setup
- Without involved calibration and reconstruction procedures



Scintillator Timing Setup

Goal 1: Measure the time resolution of the SiPM-on-Tile technology:

- Independent of the AHCAL electronics and DAQ
- In a simple but modular setup
- Without involved calibration and reconstruction procedures
- With high particle rate and controlled energies

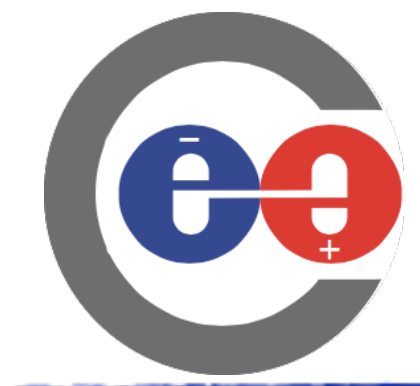


Scintillator Timing Setup

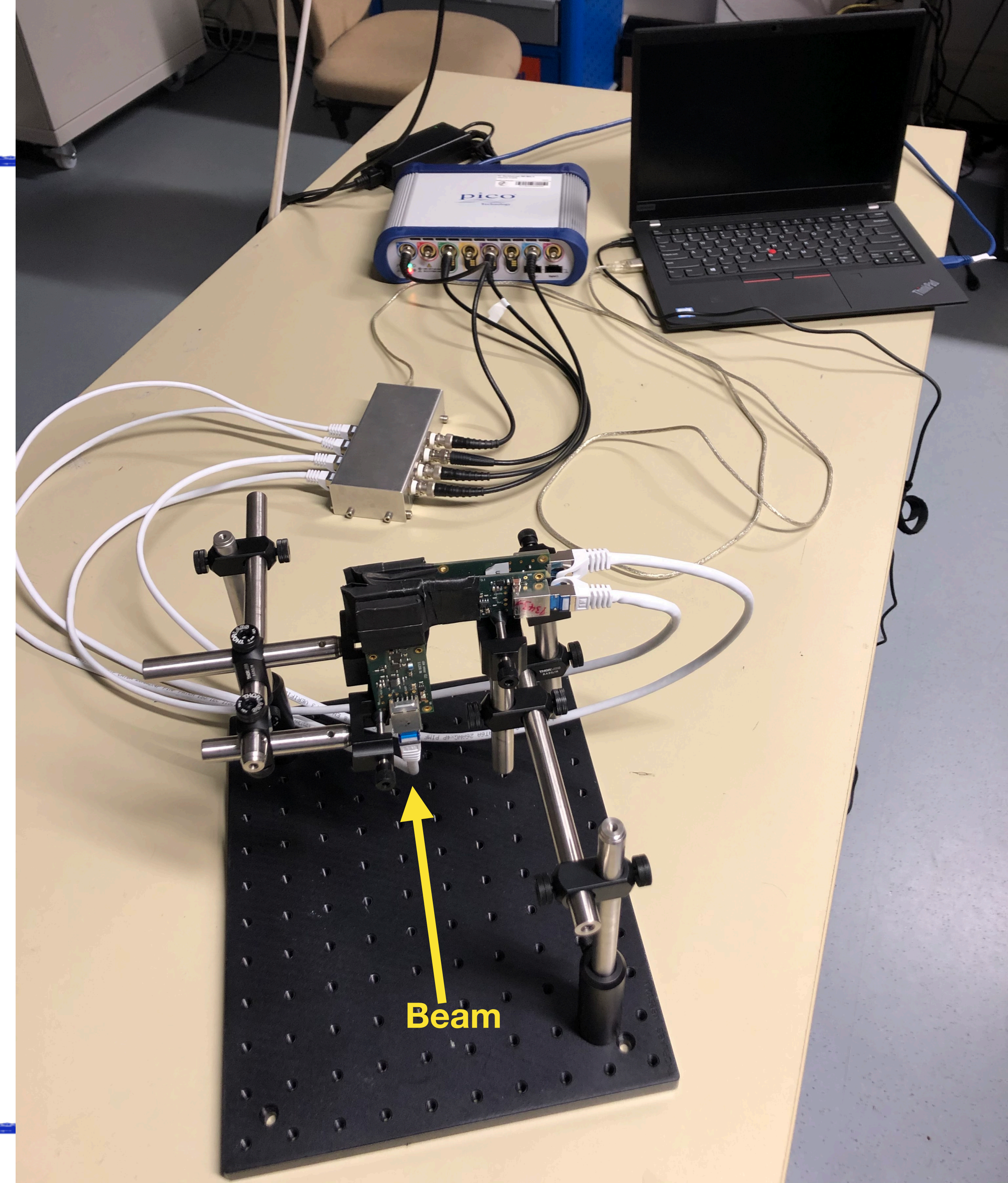
Goal 1: Measure the time resolution of the SiPM-on-Tile technology:

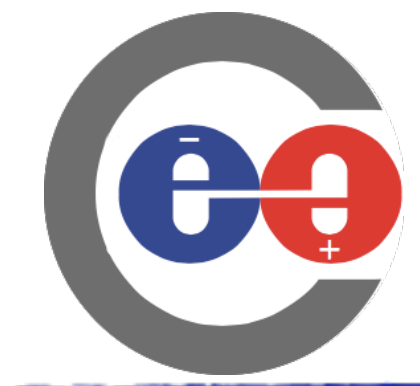
- Independent of the AHCAL electronics and DAQ
- In a simple but modular setup
- Without involved calibration and reconstruction procedures
- With high particle rate and controlled energies

Goal2: Identification of limiting factors of the time resolution of the AHCAL



Setup





Setup

Stack of 4 Tiles:

- BC408 or Polystyrene (AHCAL)
- Hamamatsu S13360-1325PE

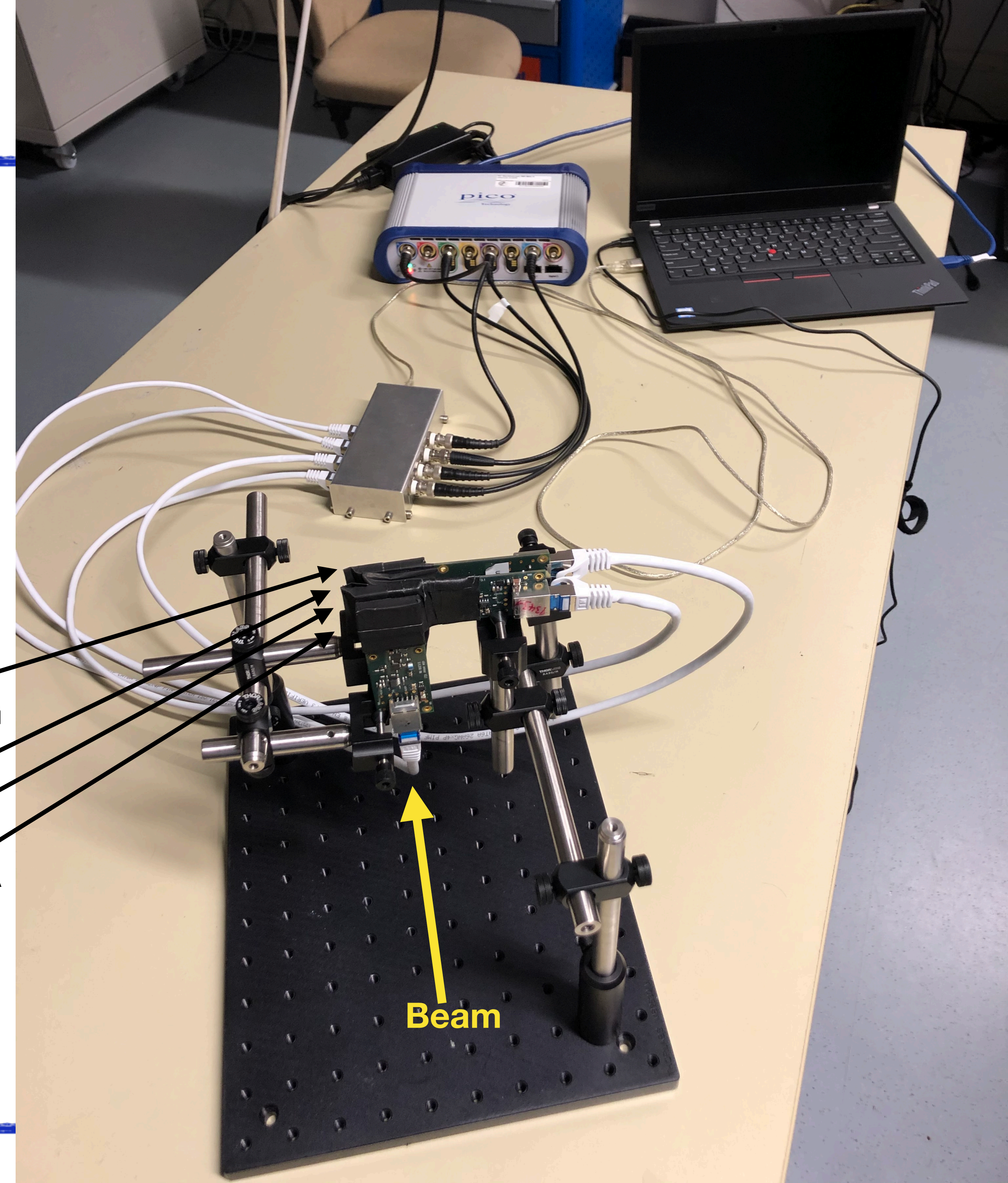
Trigger Channel G

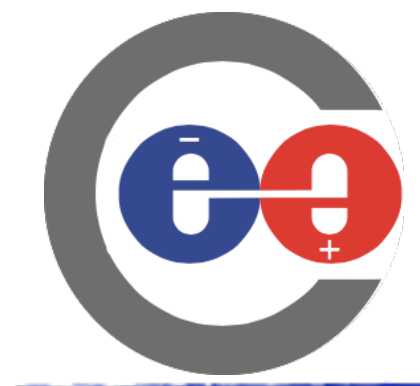
Tile Channel E

Tile Channel C

Trigger Channel A

Beam





Setup

Stack of 4 Tiles:

- BC408 or Polystyrene (AHCAL)
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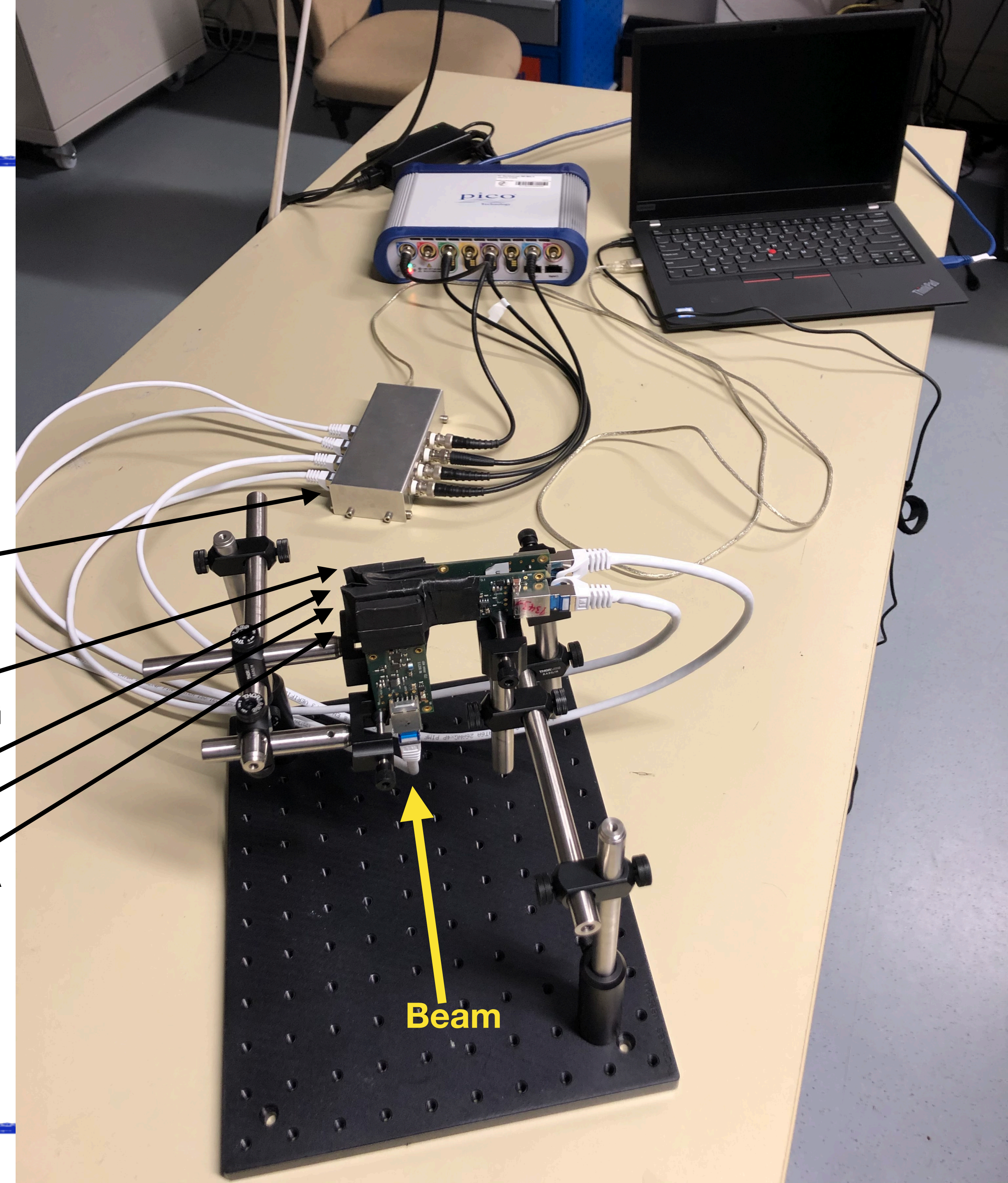
↓ Ethernet Cat 7

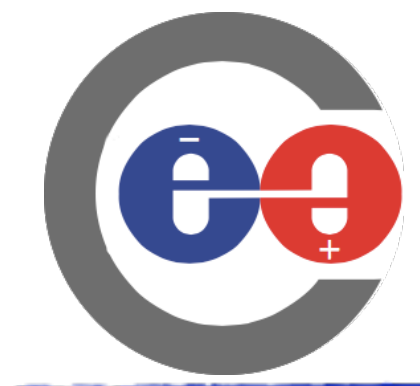
Receiver Box:

- USB controlled power supply
- Split signal and power lines

Receiver Box
Trigger Channel G
Tile Channel E
Tile Channel C
Trigger Channel A

Beam





Setup

Stack of 4 Tiles:

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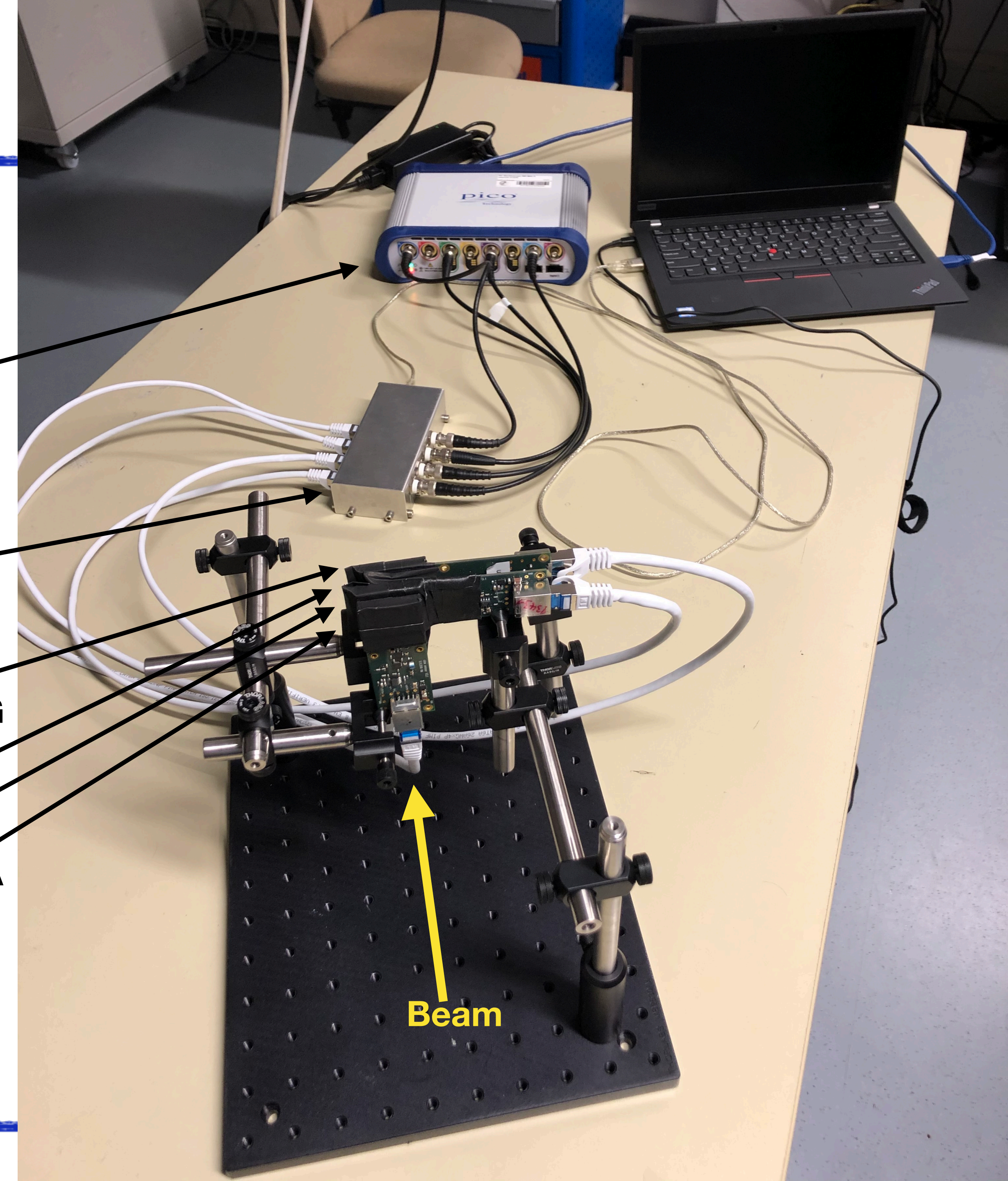
Receiver Box:

- USB controlled power supply
- Split signal and power lines

↓ BNC

Picoscope:

- Up to 2.5GHz sampling rate on 4 channels
- 300kHz peak trigger rate
- Save complete analog waveform



Picoscope

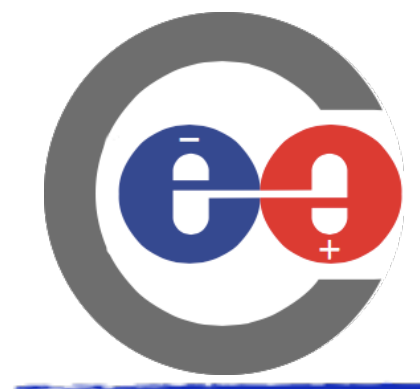
Receiver Box

Trigger Channel G

Tile Channel E
Tile Channel C

Trigger Channel A

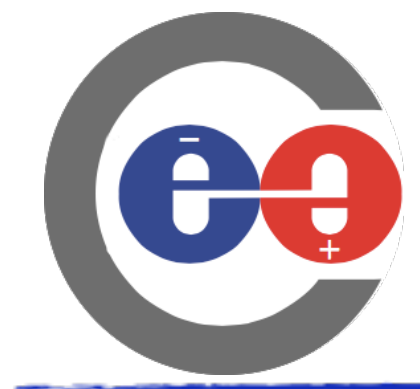
Beam



Data Taking at the DESY Testbeam

MIP spectrum for amplitude resolved time resolution measurement:

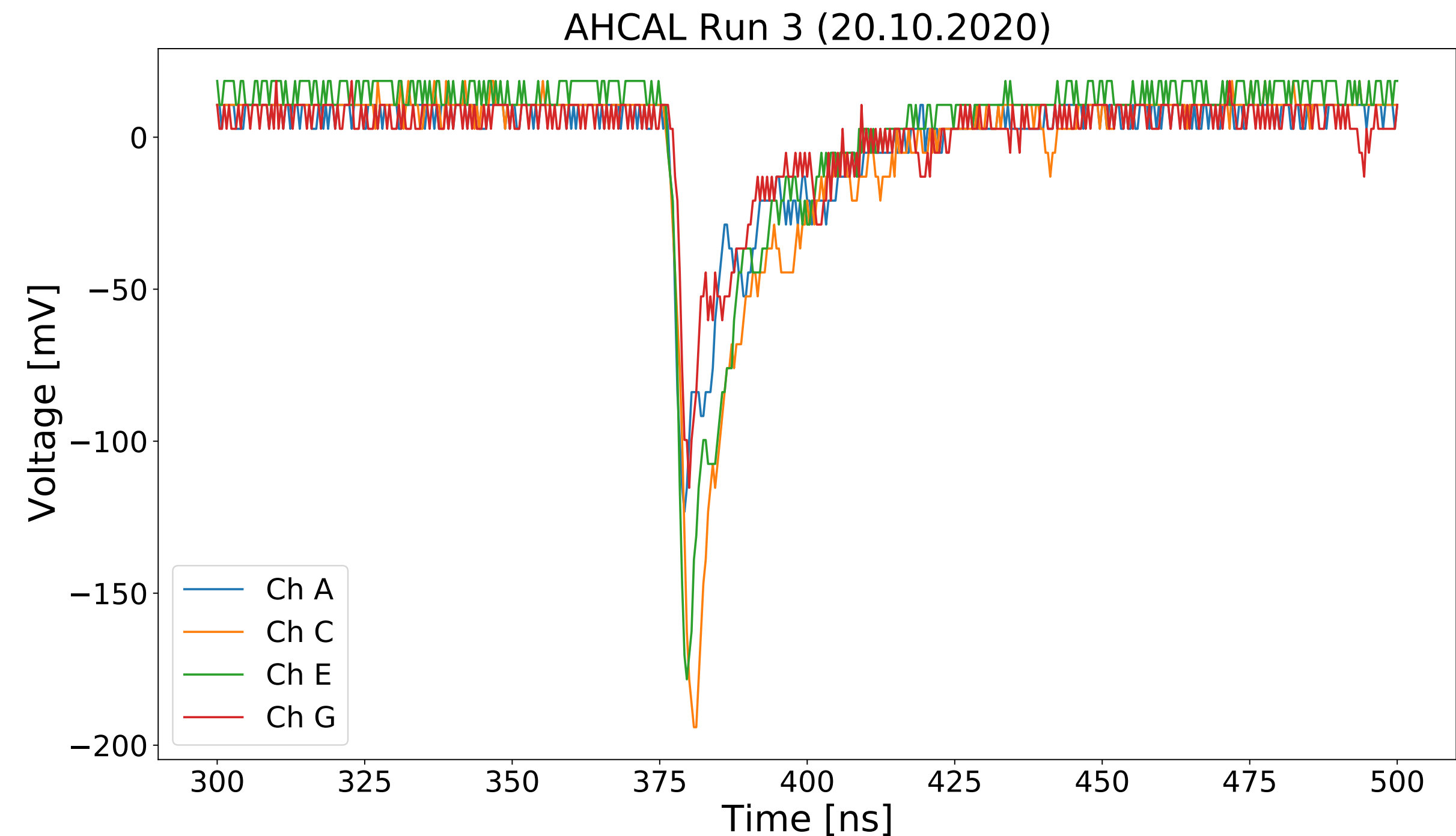
- Polystyrene scintillator and BC408 scintillator for higher light yield
- 2.5GHz and 1.25GHz sampling rate
- Tile size of $3 \times 3 \text{cm}^2$ and $2 \times 2 \text{cm}^2$



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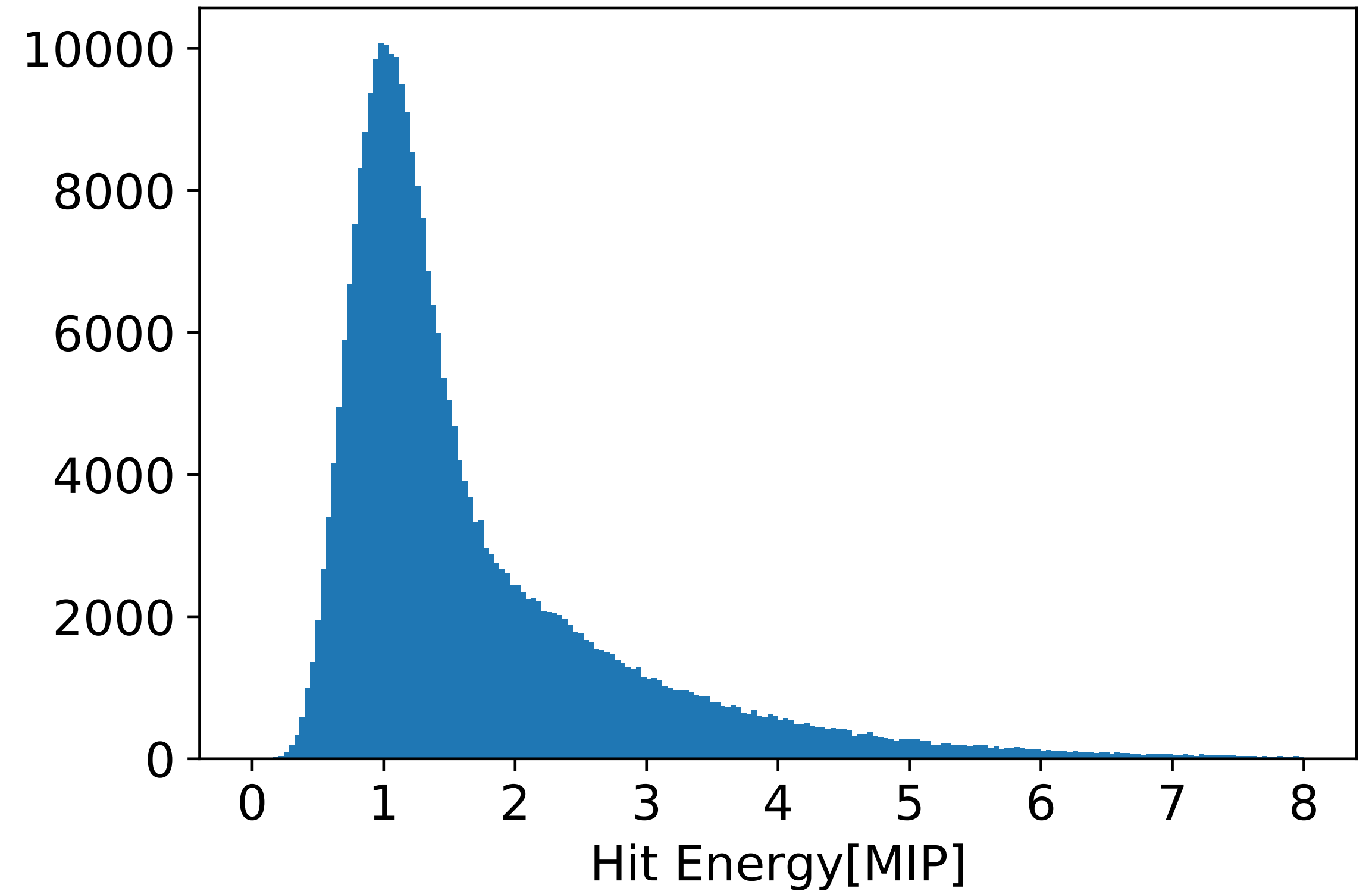


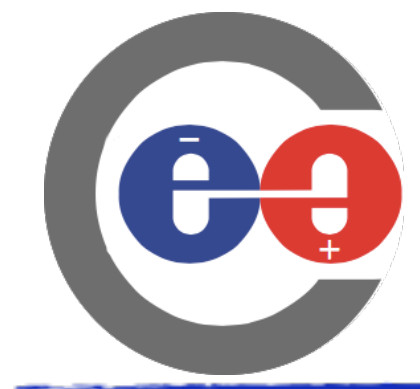


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Data Taking at the DESY Testbeam

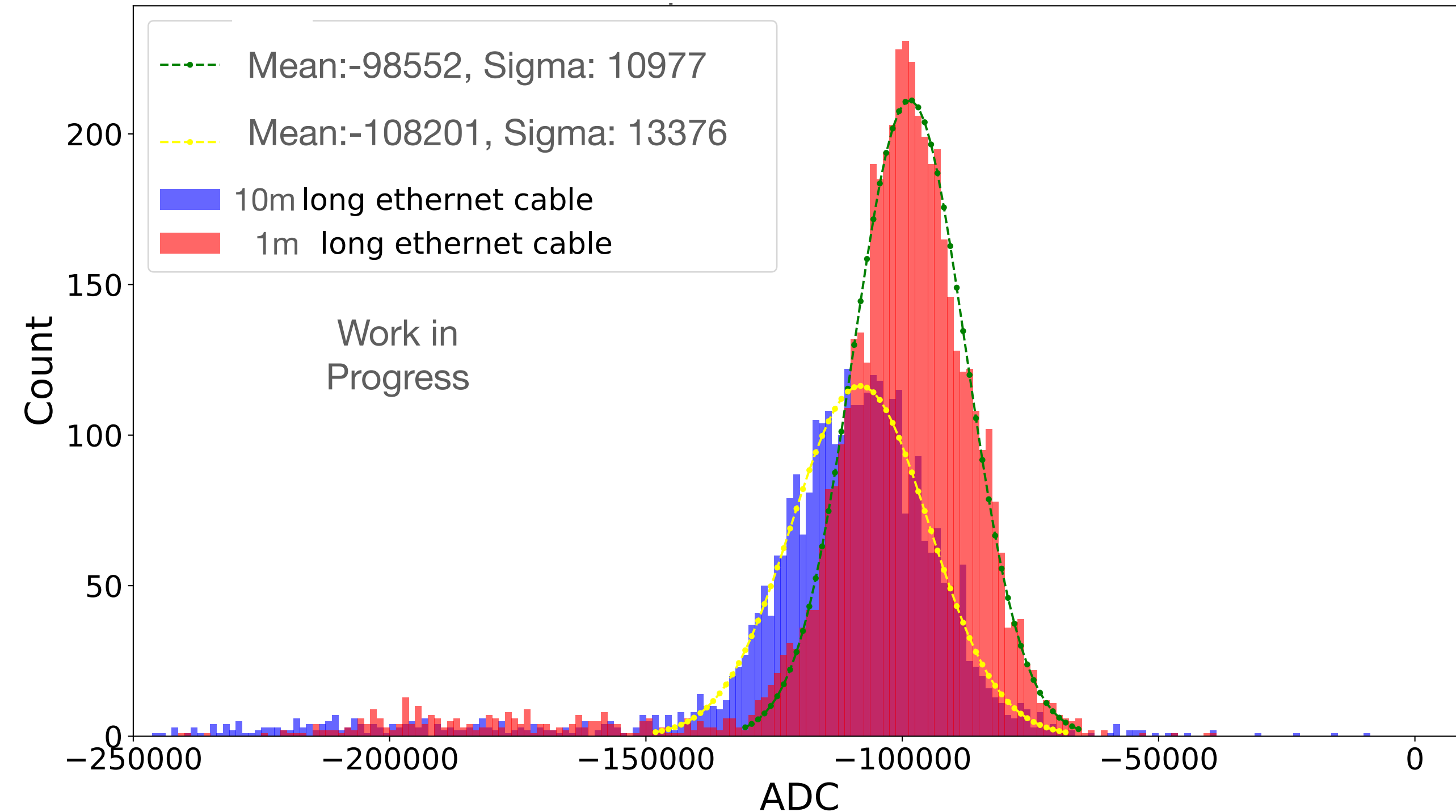
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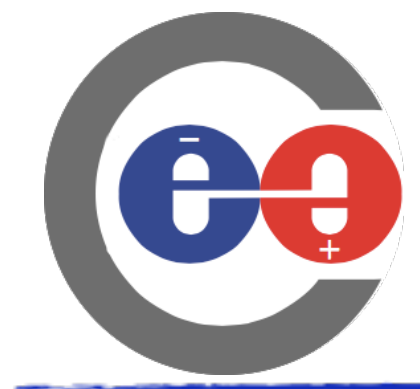
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MIP spectrum with different cable length:

- Study impact on energy measurement
- Study impact on time measurement

Integral of 1pe signal

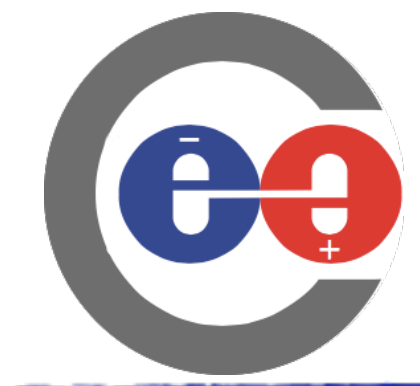




Data Taking at the DESY Testbeam

Energy spectrum of EM showers by placing tungsten absorber plates:

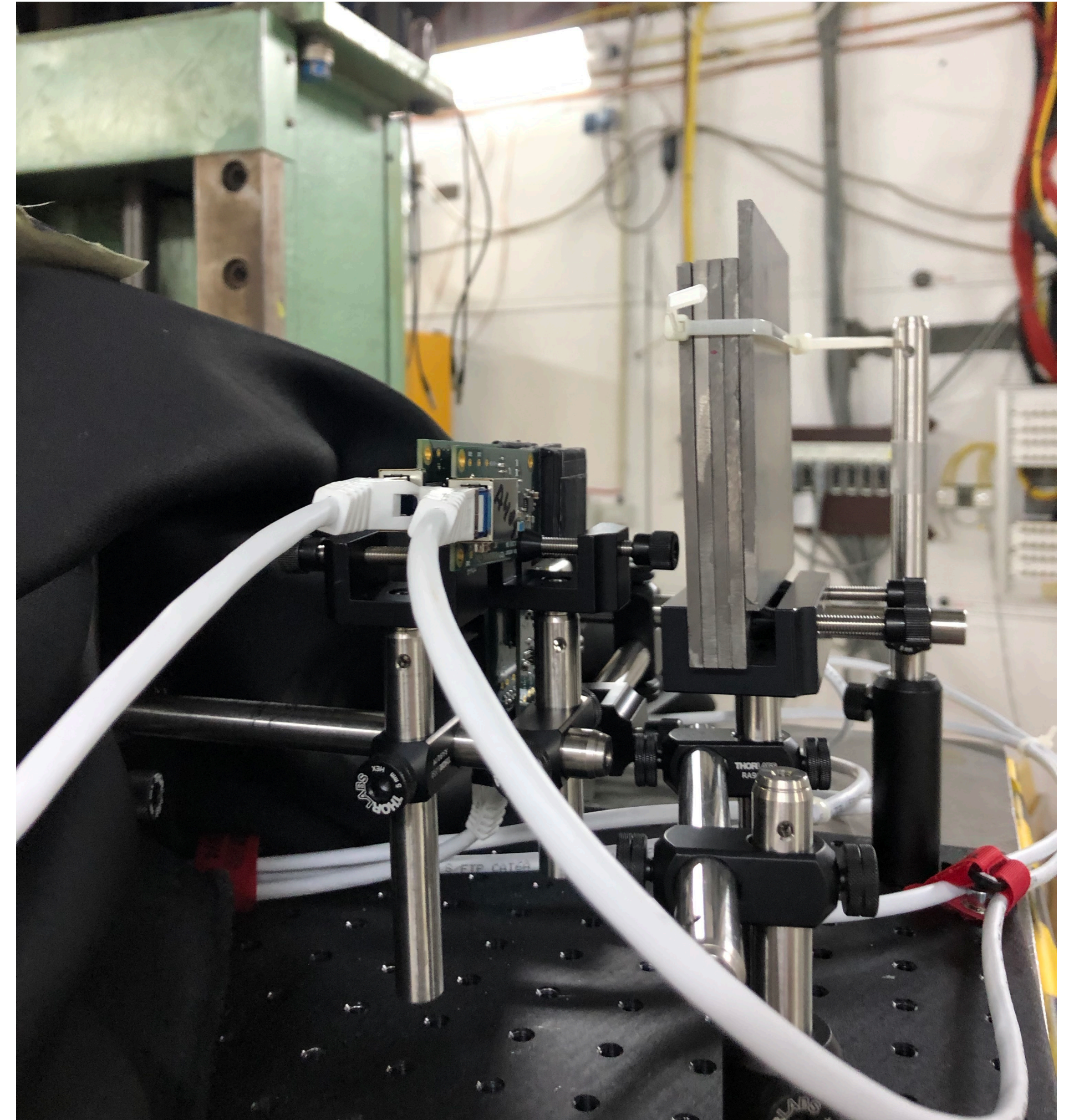
- Increase statistics of high energy hits
- Impact of shower on time resolution

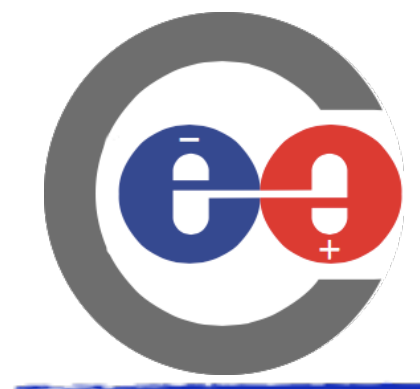


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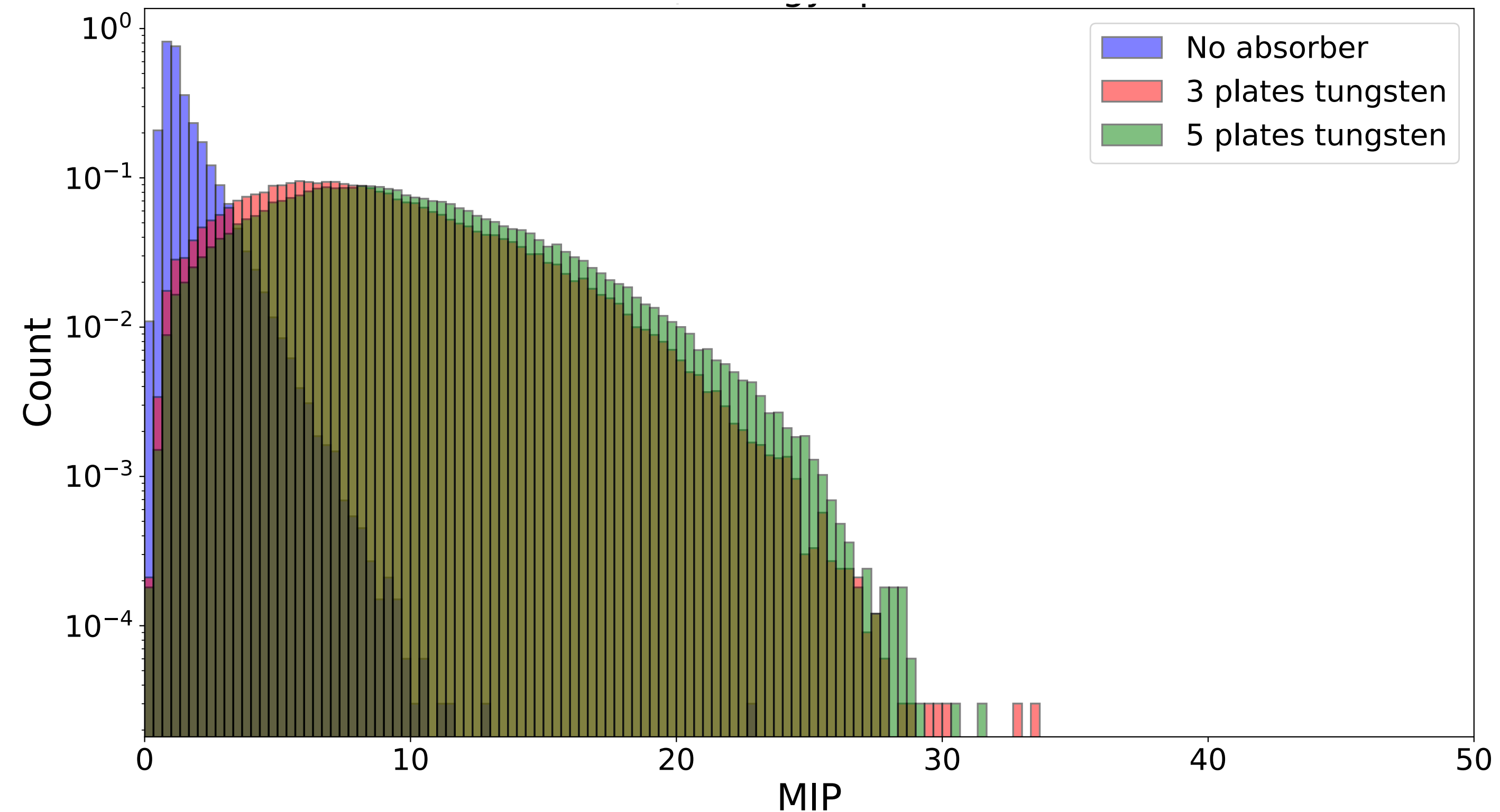


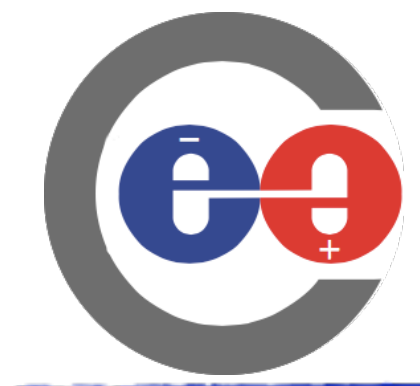


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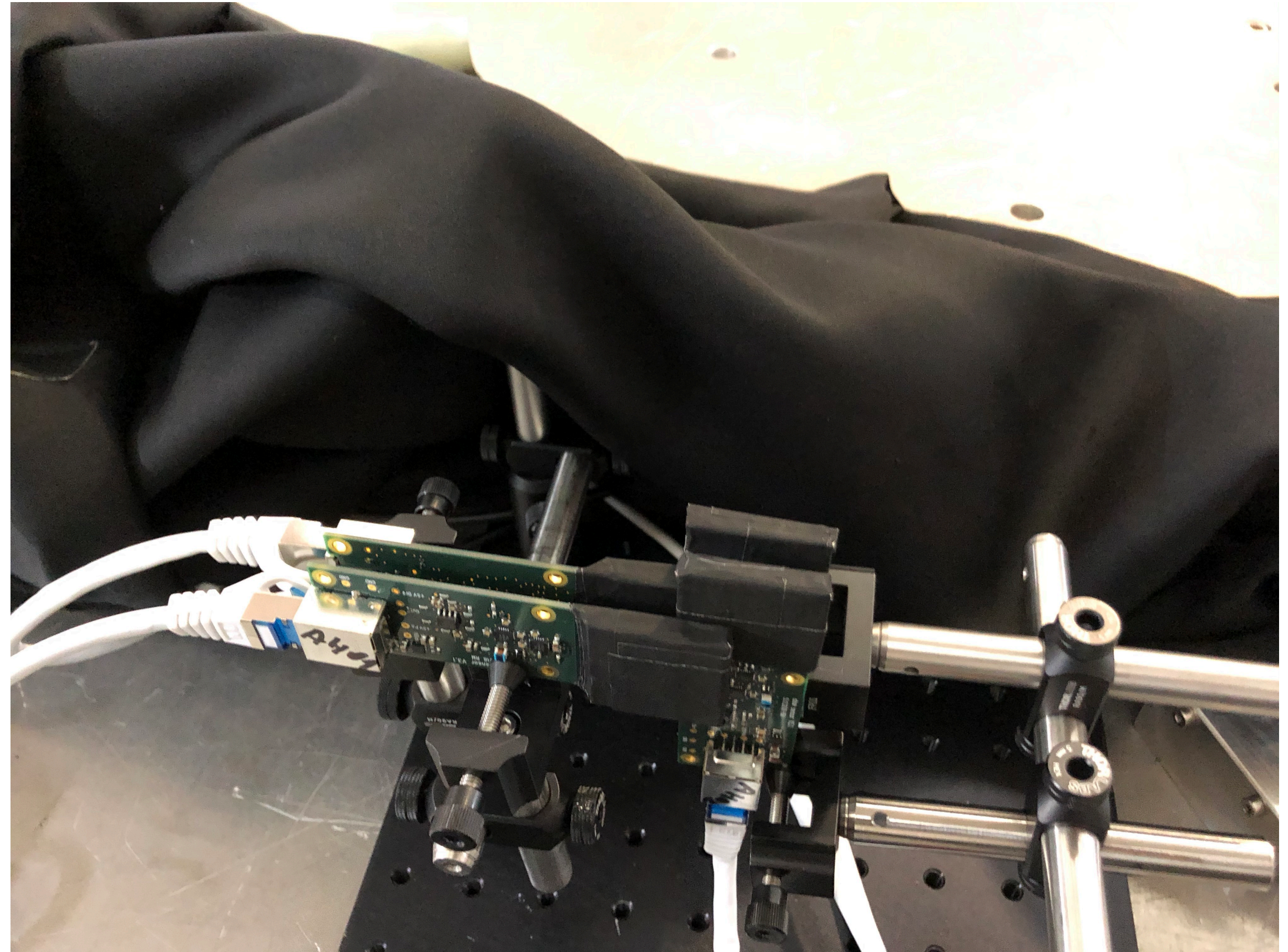
Data Taking at the DESY Testbeam

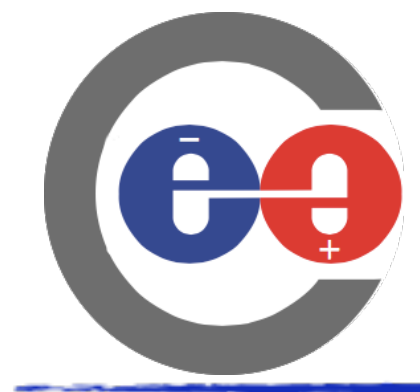
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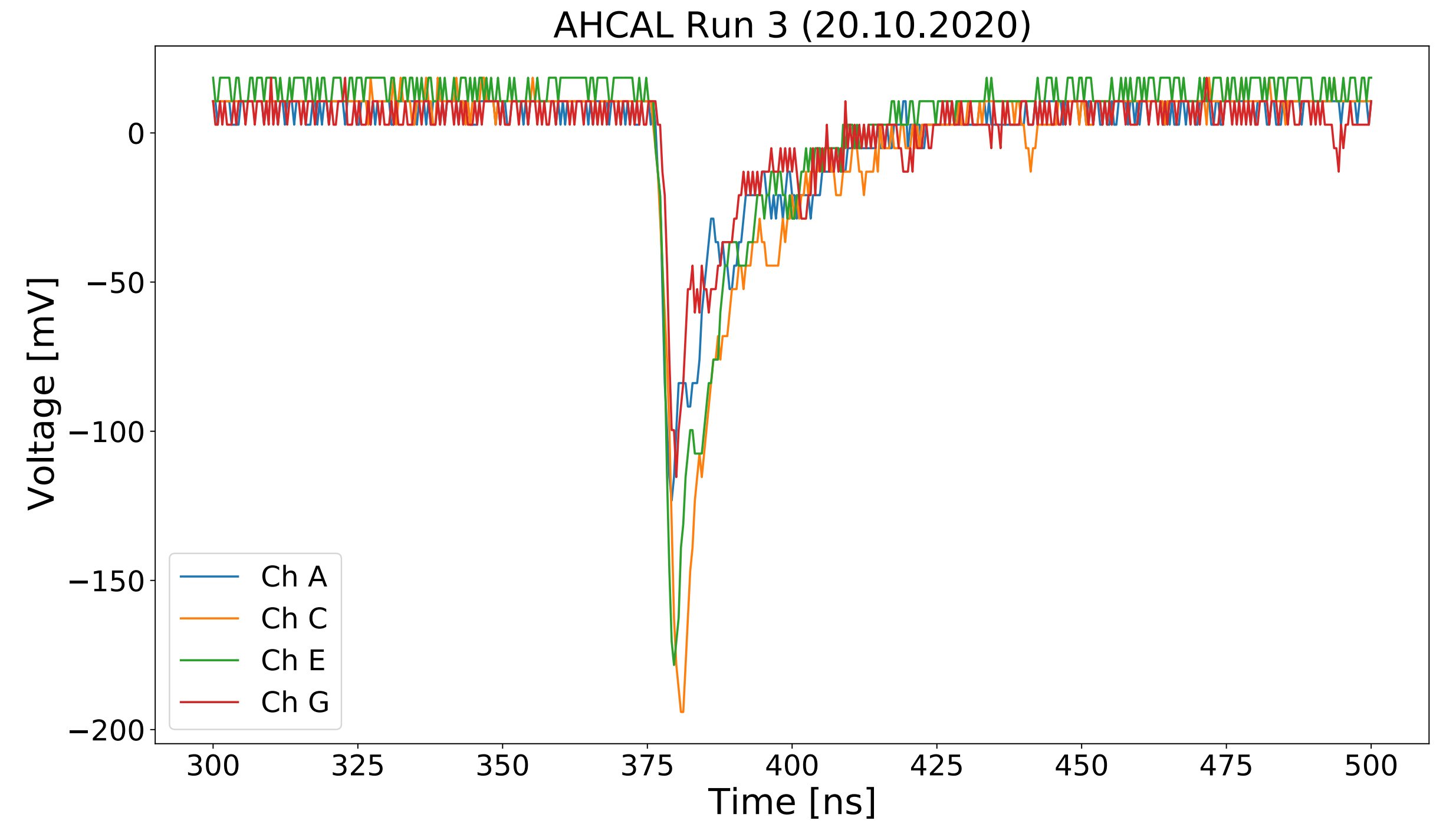
Displaced scintillators:

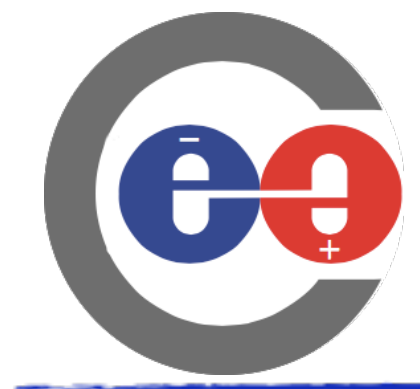
- Position dependent dataset
- Impact on energy response
- Impact on time resolution





Event Reconstruction

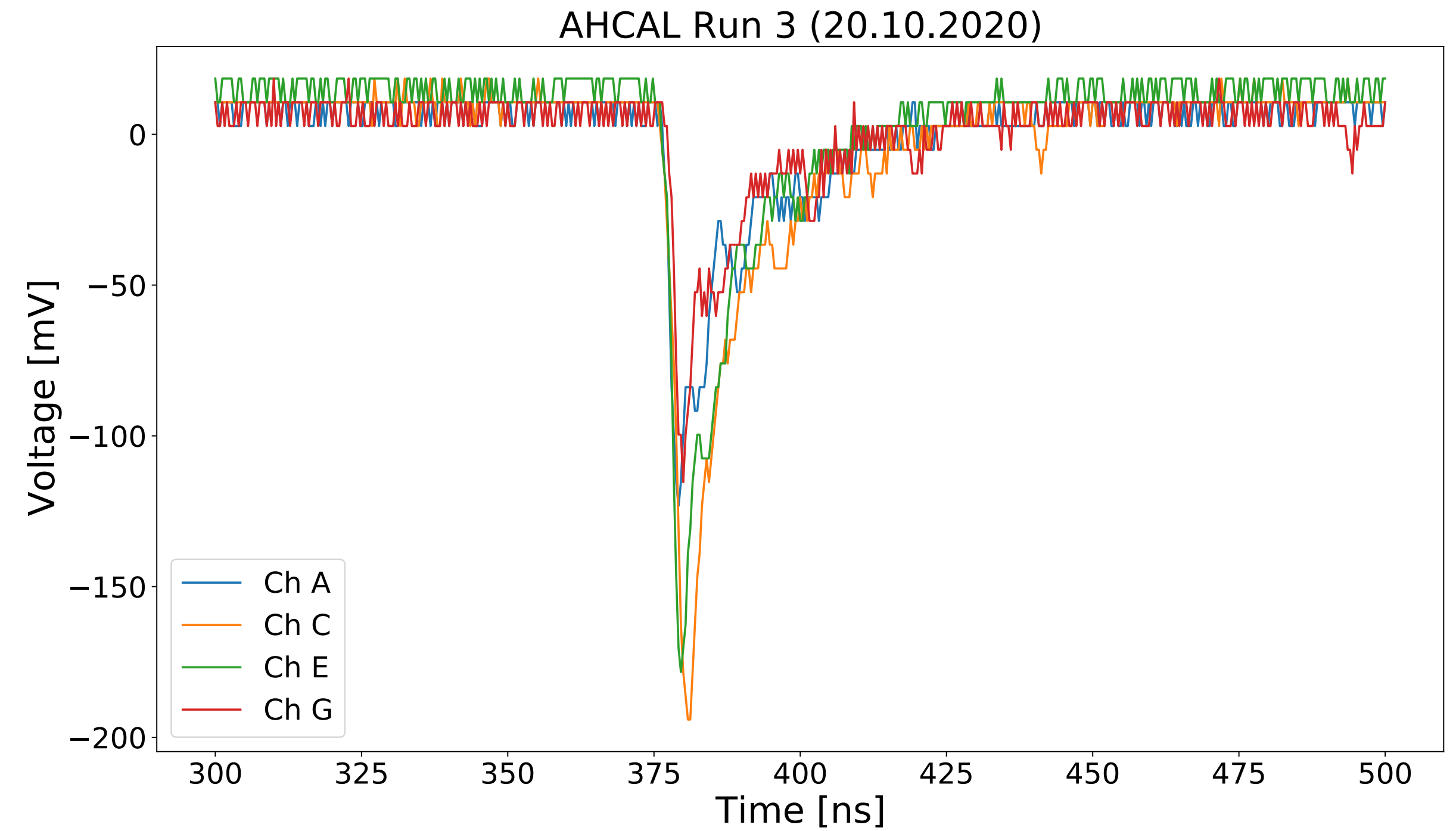




Event Reconstruction

Per event:

1. Search for maximum amplitude in each waveform

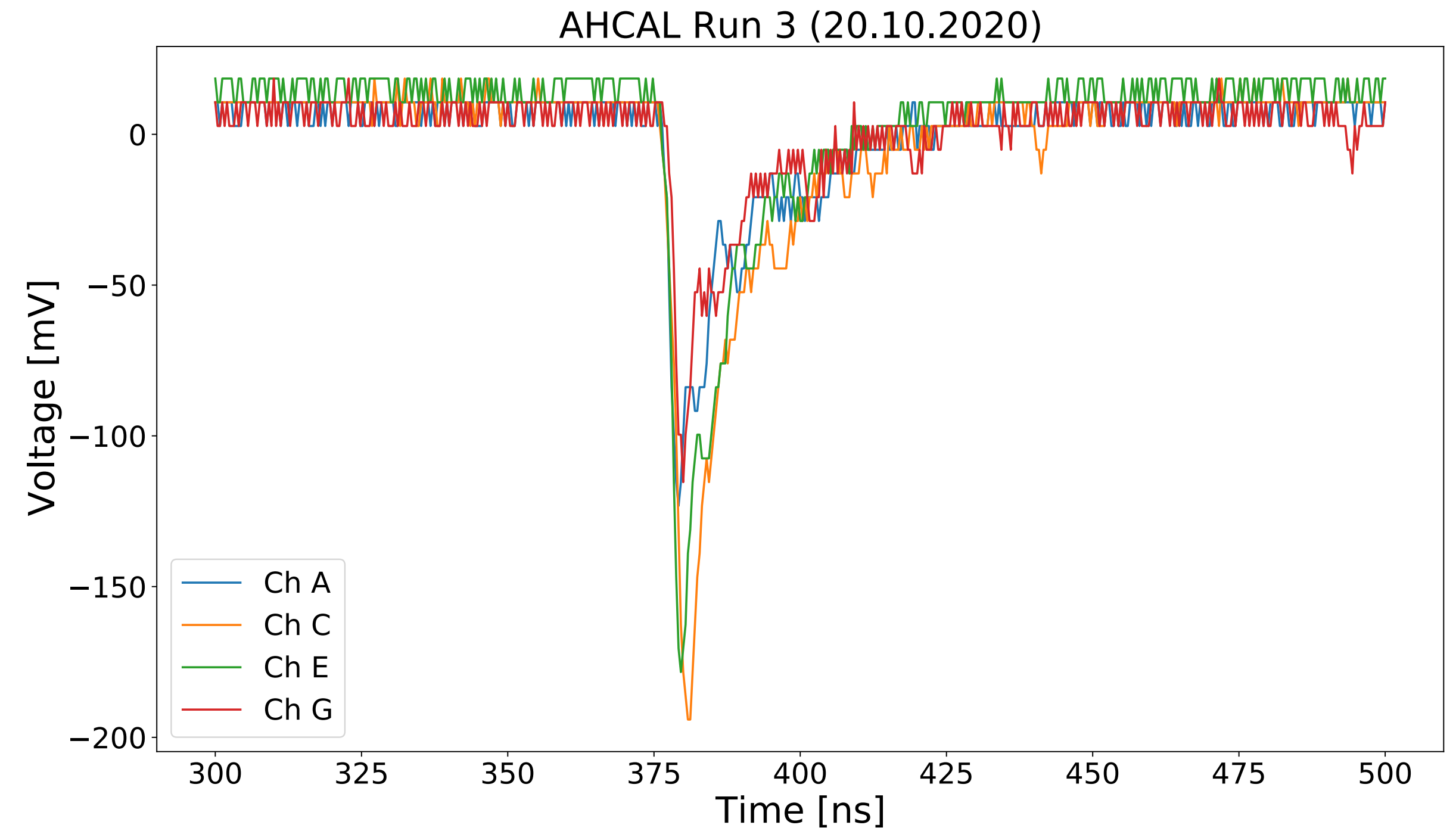


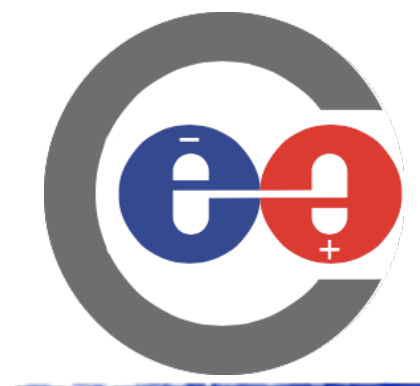


Event Reconstruction

Per event:

1. Search for maximum amplitude in each waveform
2. Per waveform, search for first time, the signal crosses 25% of maximum amplitude (constant fraction discrimination)

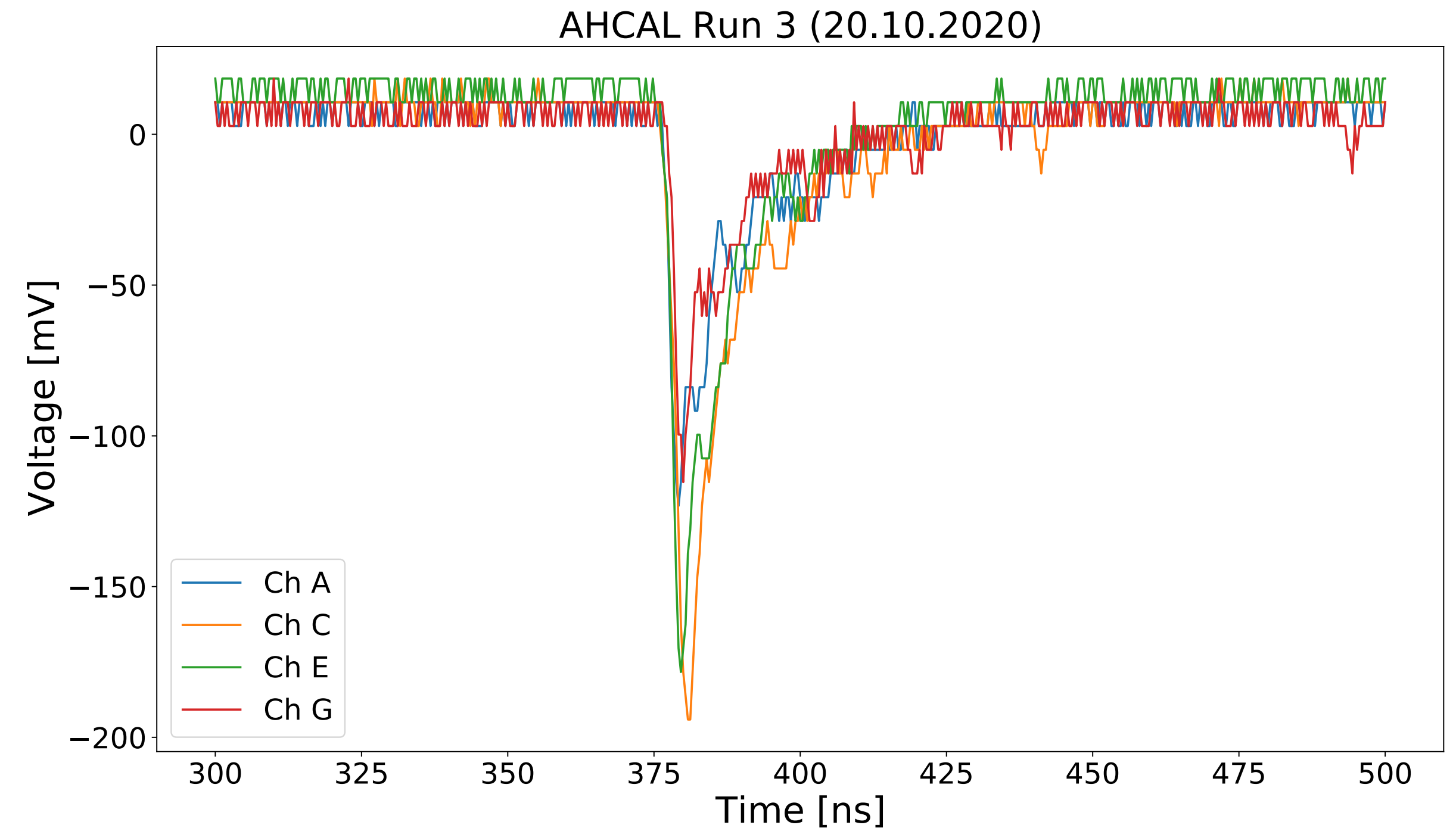


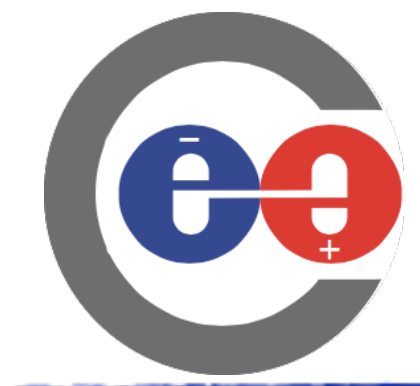


Event Reconstruction

Per event:

1. Search for maximum amplitude in each waveform
2. Per waveform, search for first time, the signal crosses 25% of maximum amplitude (constant fraction discrimination)
3. If crossing is between two time bins, interpolate linear

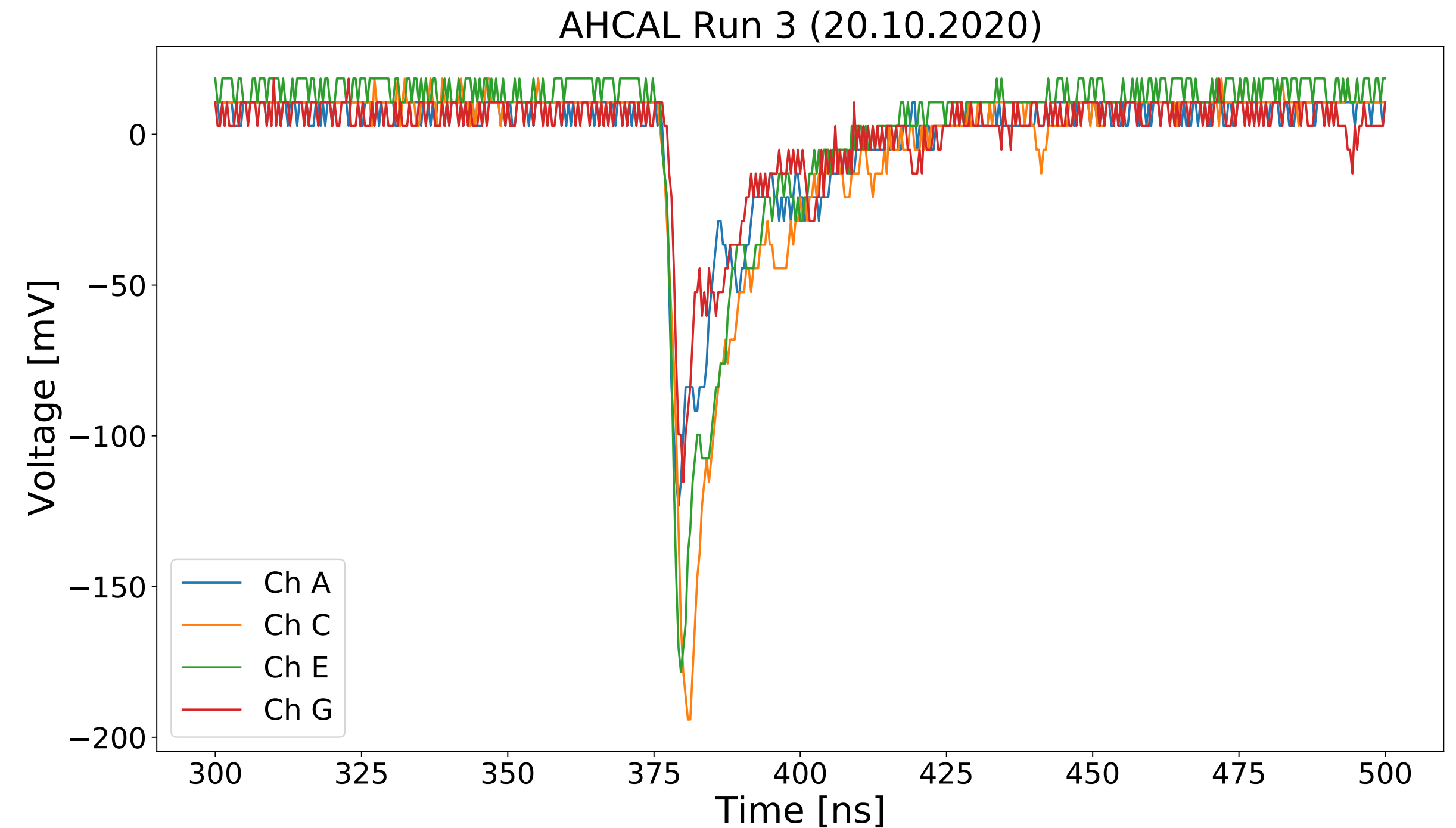


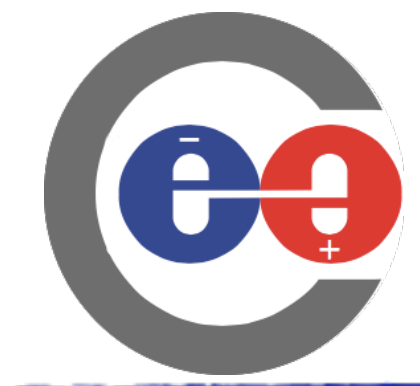


Event Reconstruction

Per event:

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2. Per waveform, search for first time, the signal crosses 25% of maximum amplitude (constant fraction discrimination)
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4. Obtain 2 trigger times (ChA, ChG) and 2 hit times (ChC, ChE)

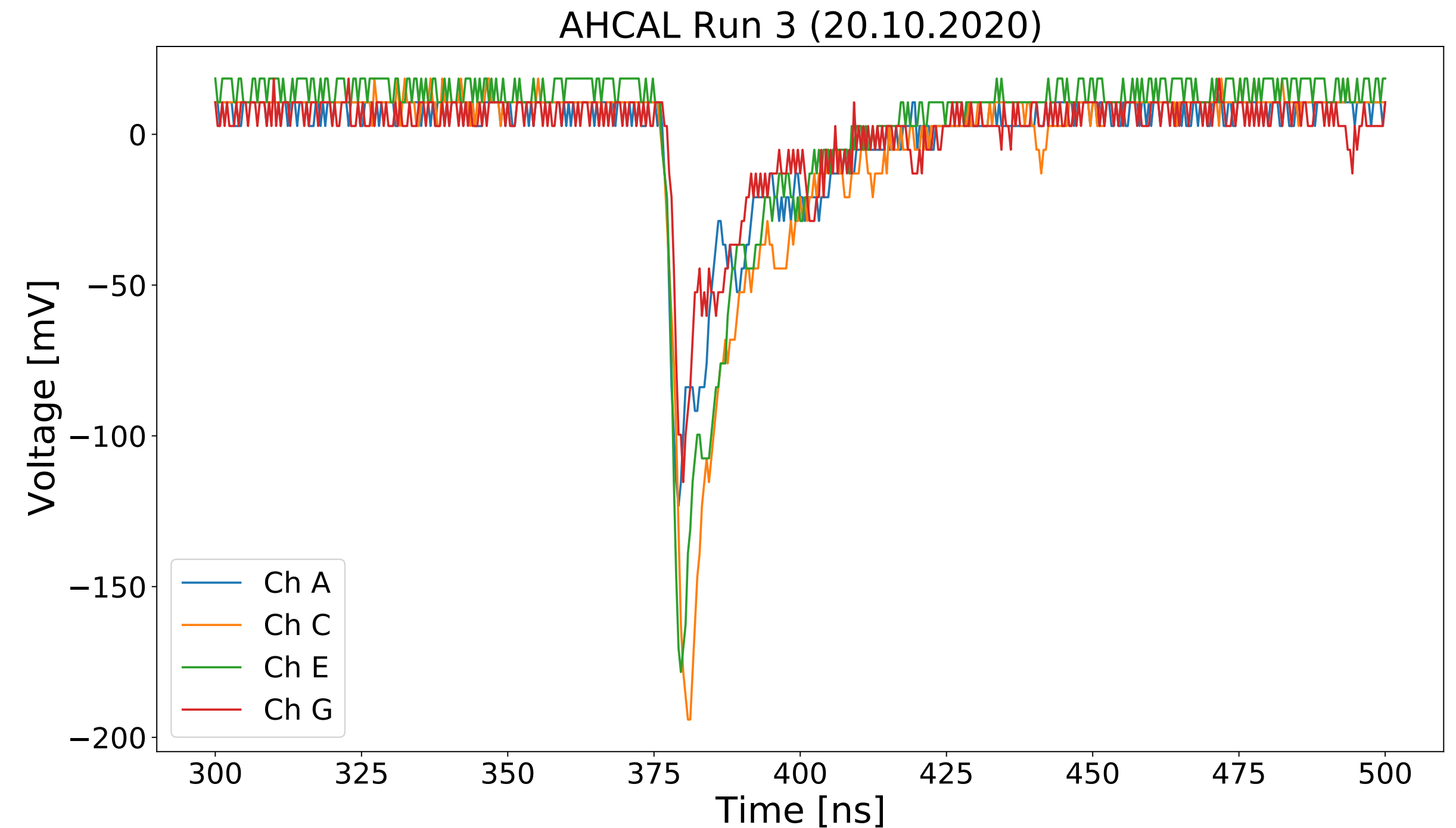




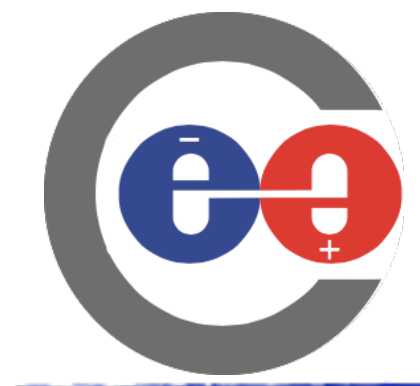
Event Reconstruction

Per event:

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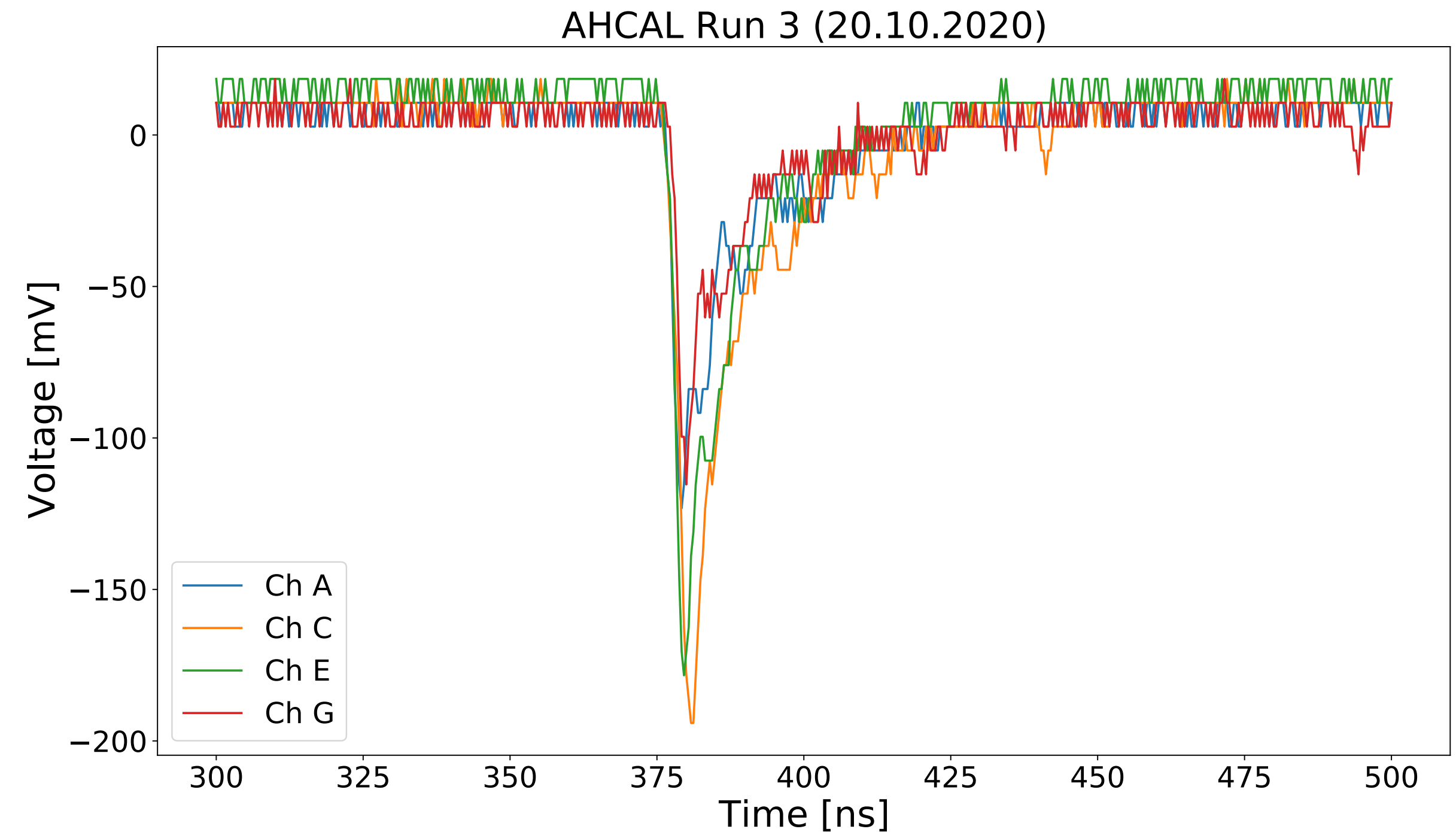
5. Calculate event time from average of trigger times

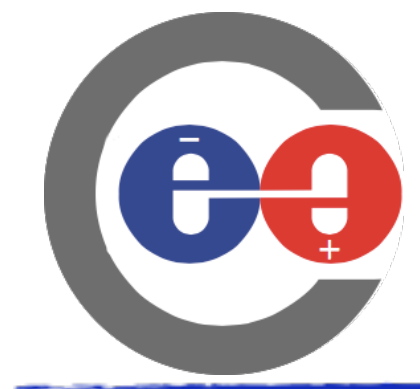


Event Reconstruction

Per event:

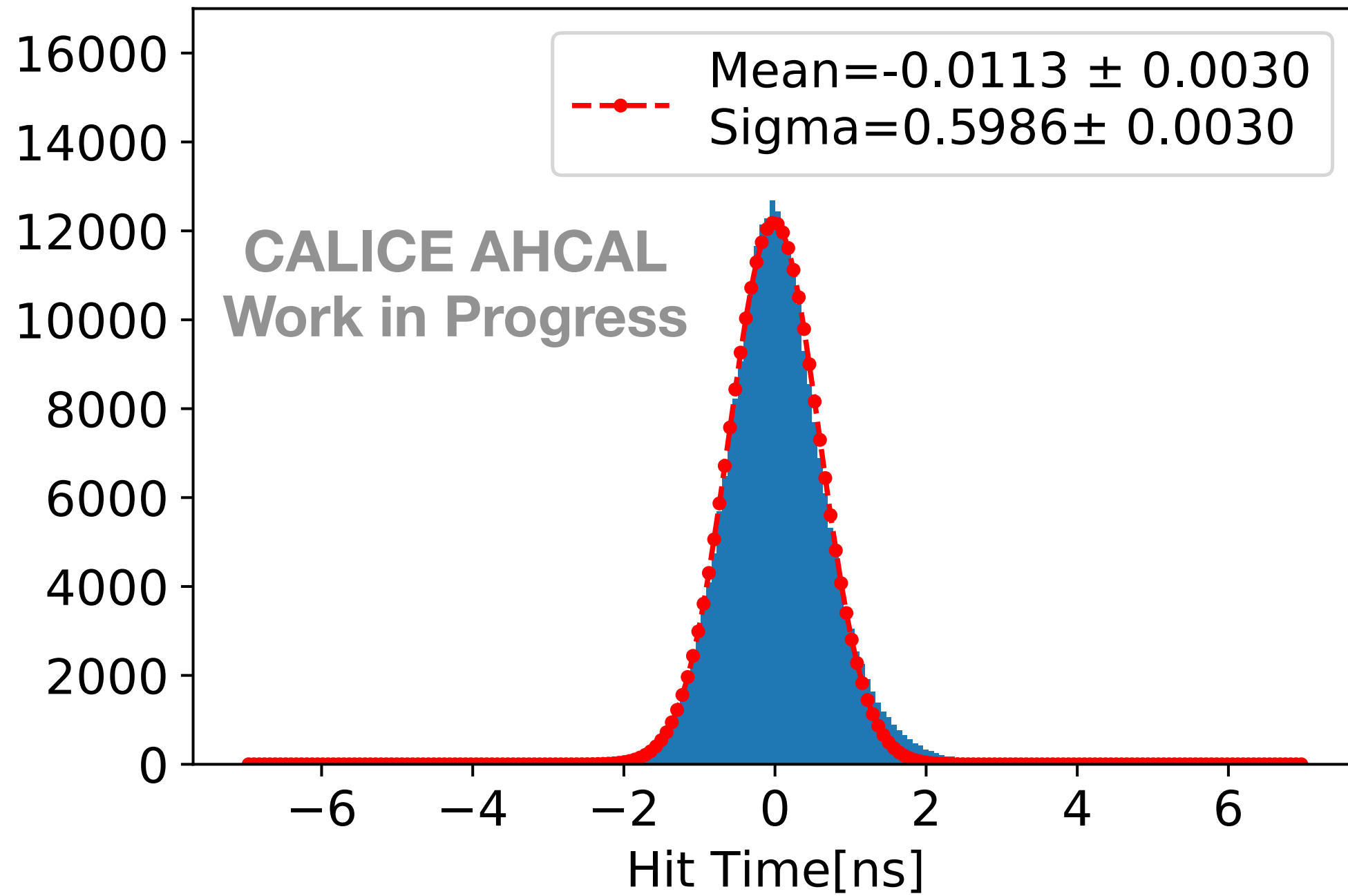
1. Search for maximum amplitude in each waveform
2. Per waveform, search for first time, the signal crosses 25% of maximum amplitude (constant fraction discrimination)
3. If crossing is between two time bins, interpolate linear
4. Obtain 2 trigger times(ChA, ChG) and 2 hit times (ChC, ChE)
5. Calculate event time from average of trigger times
6. Calculate relative hit times by: event time - hit time



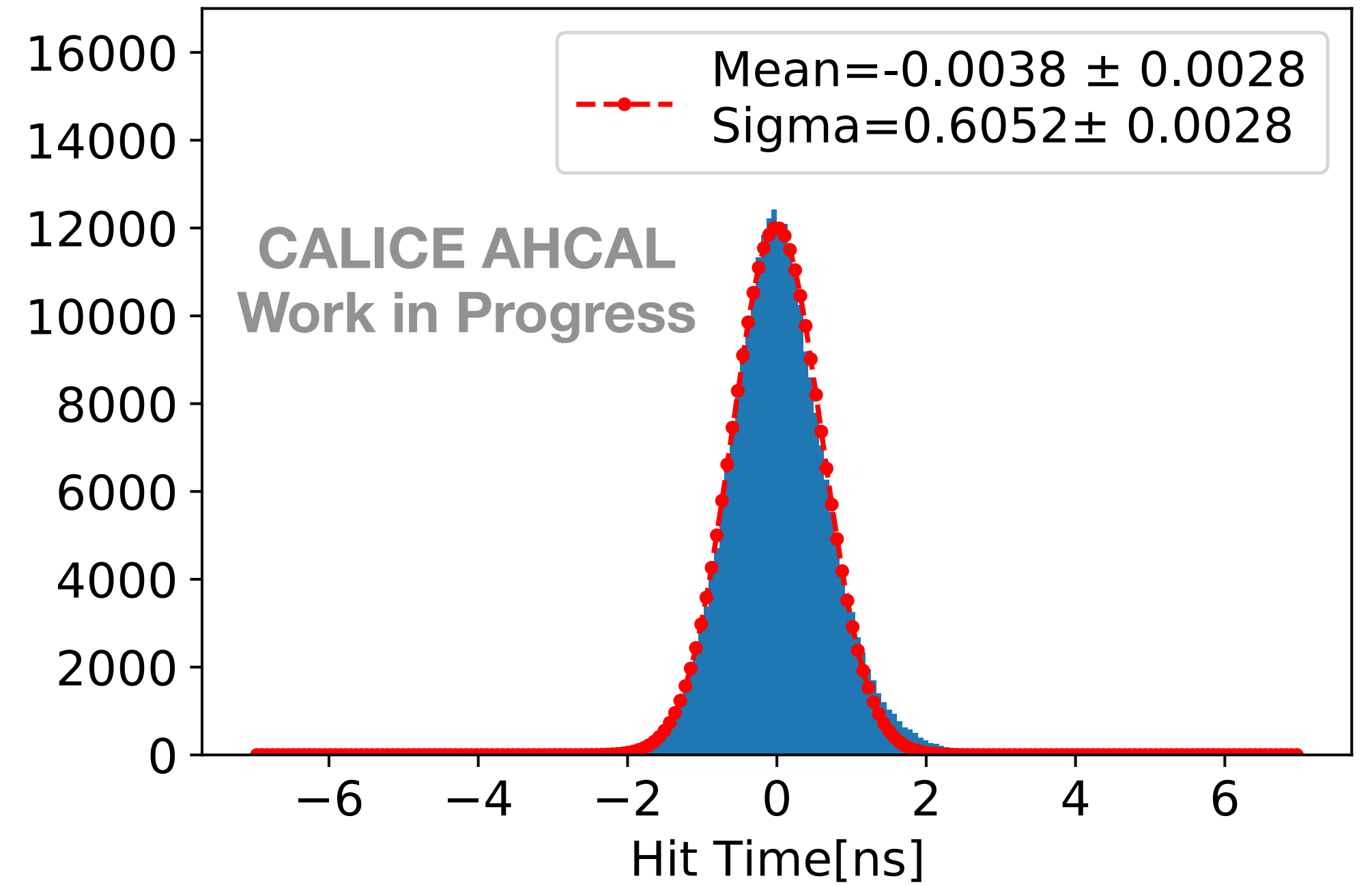


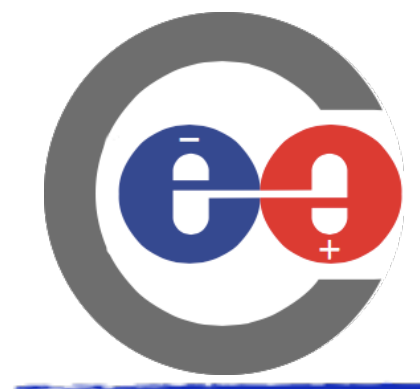
Time Resolution - Polystyrene Scintillator

Time Distribution Channel C

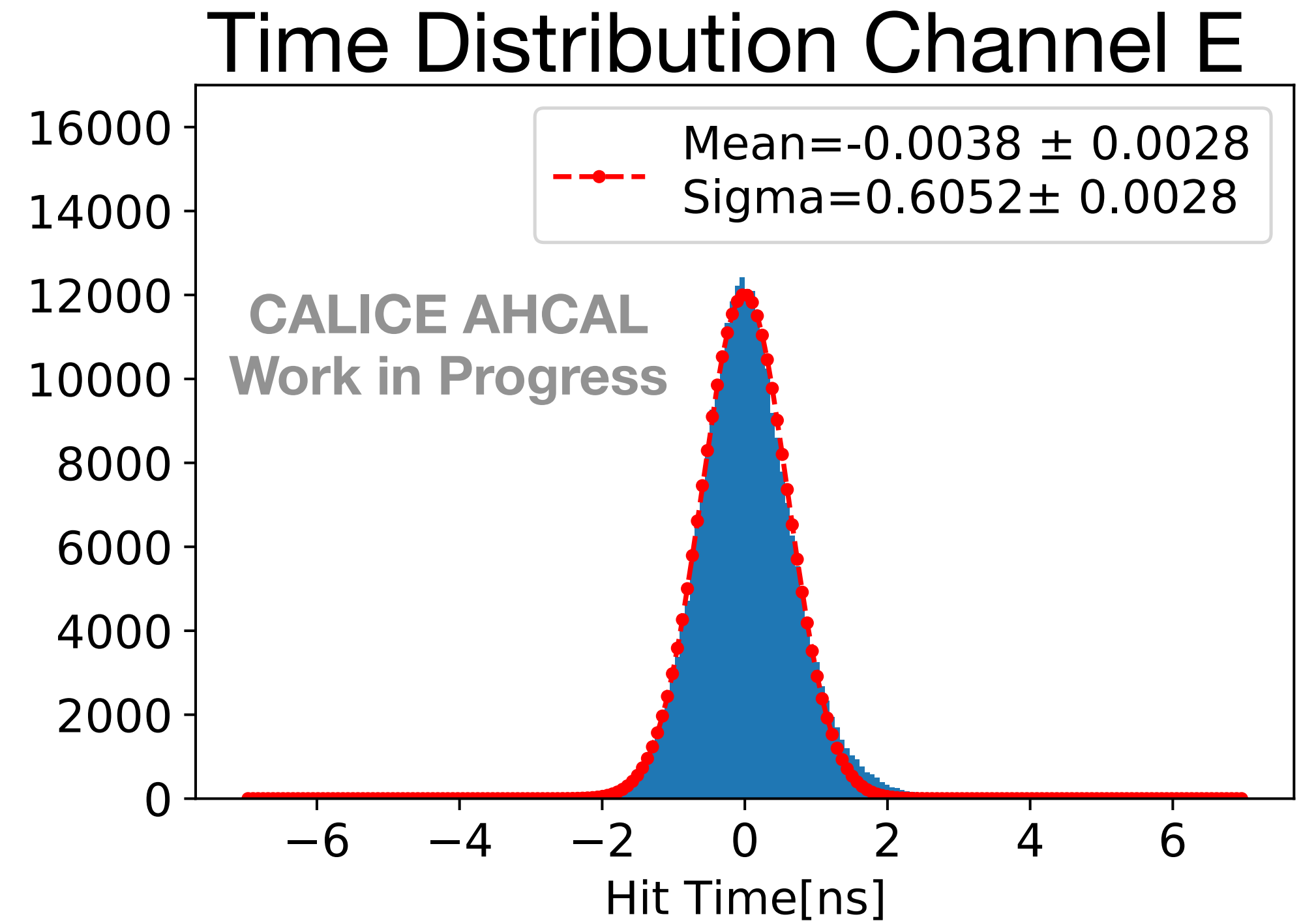
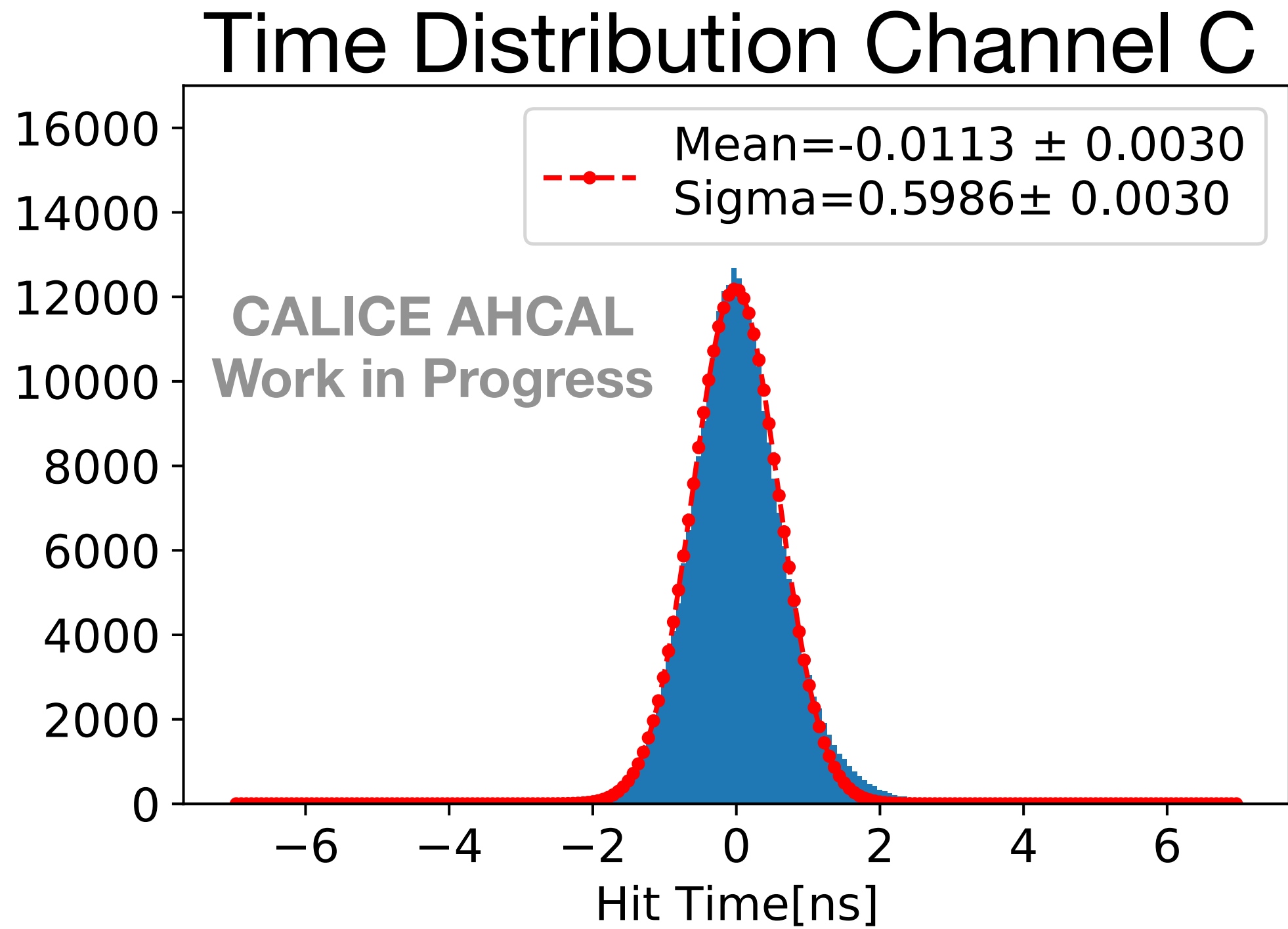


Time Distribution Channel E

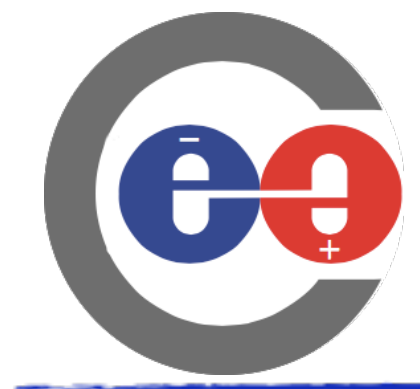




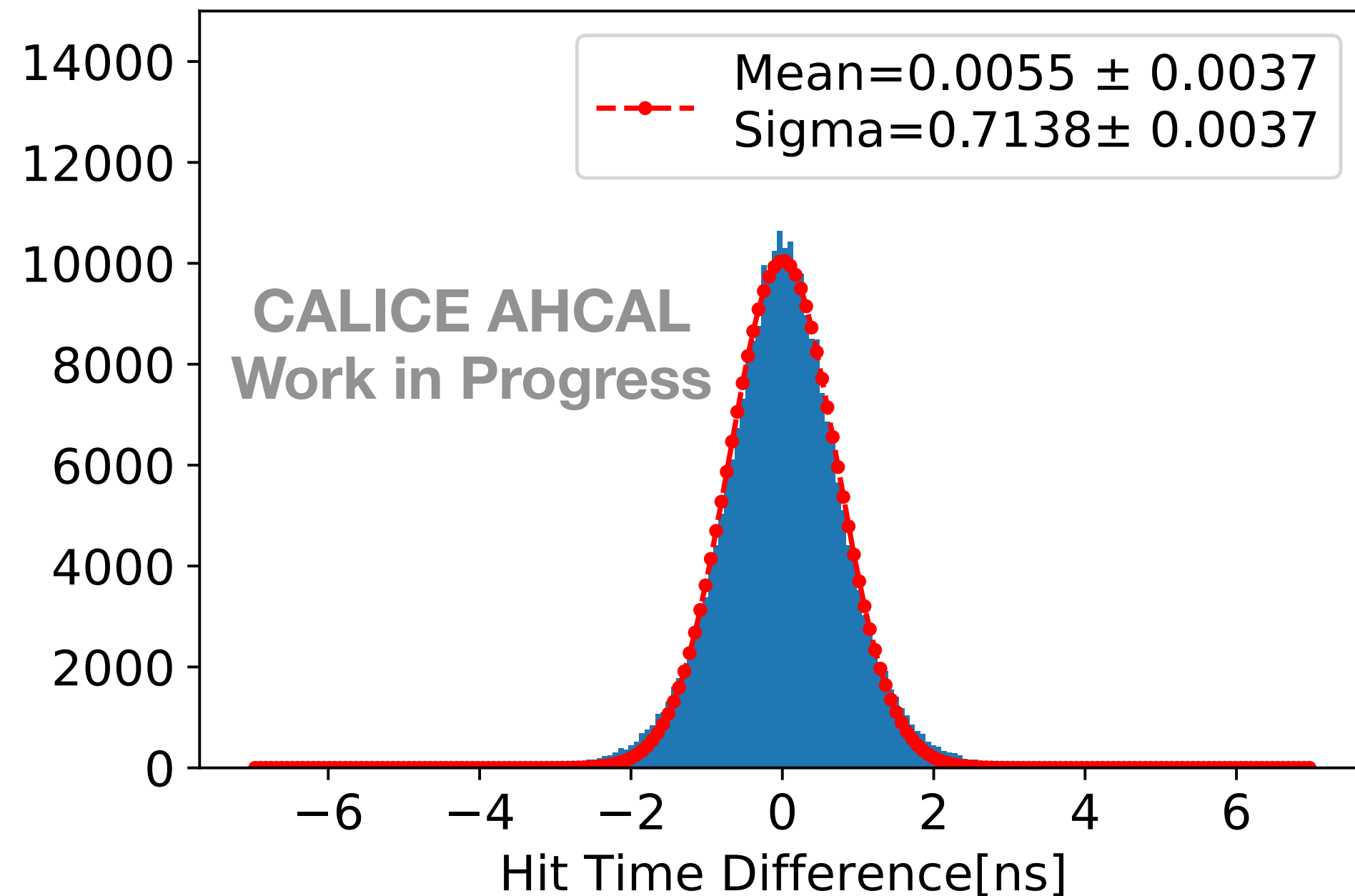
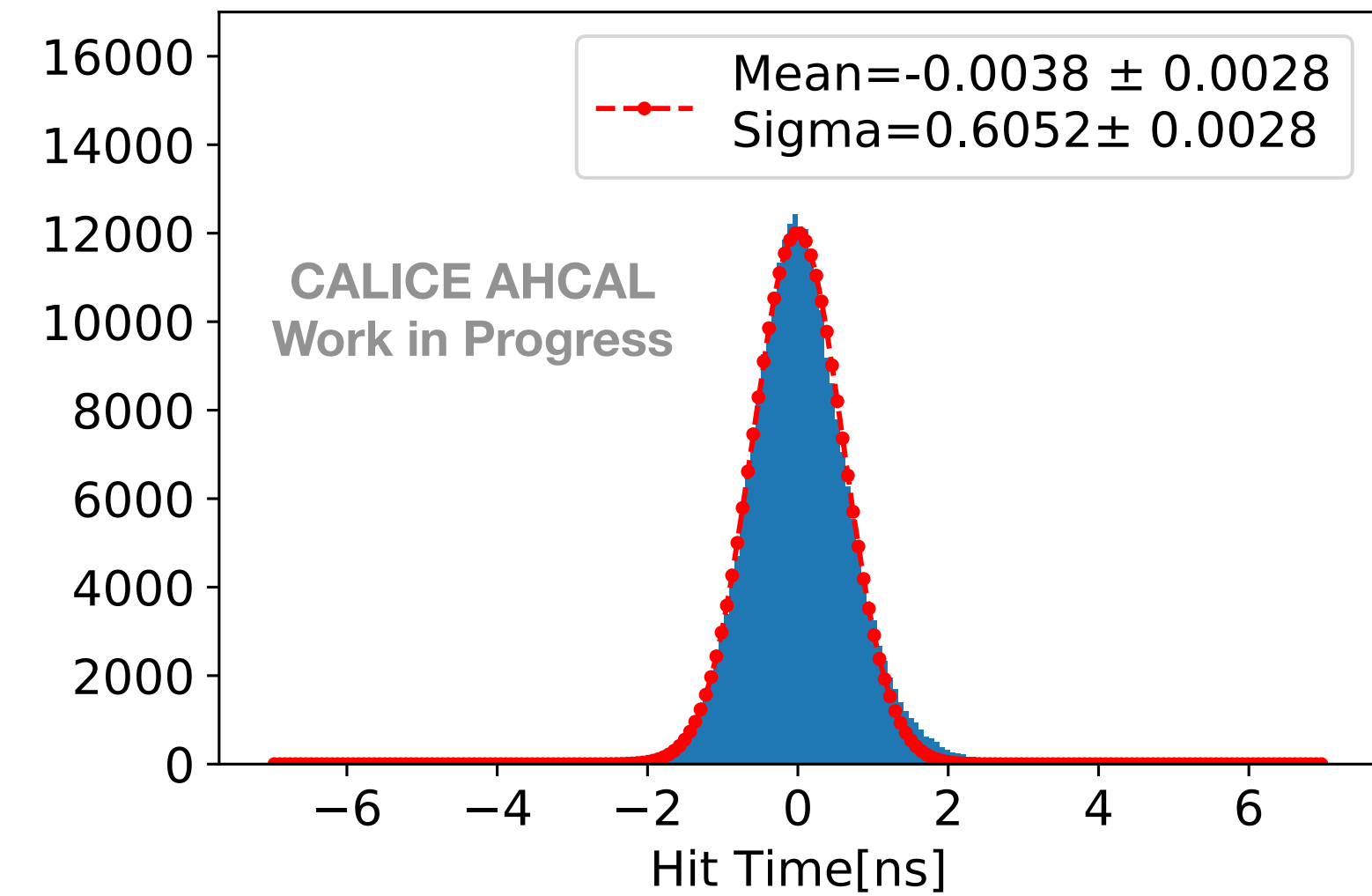
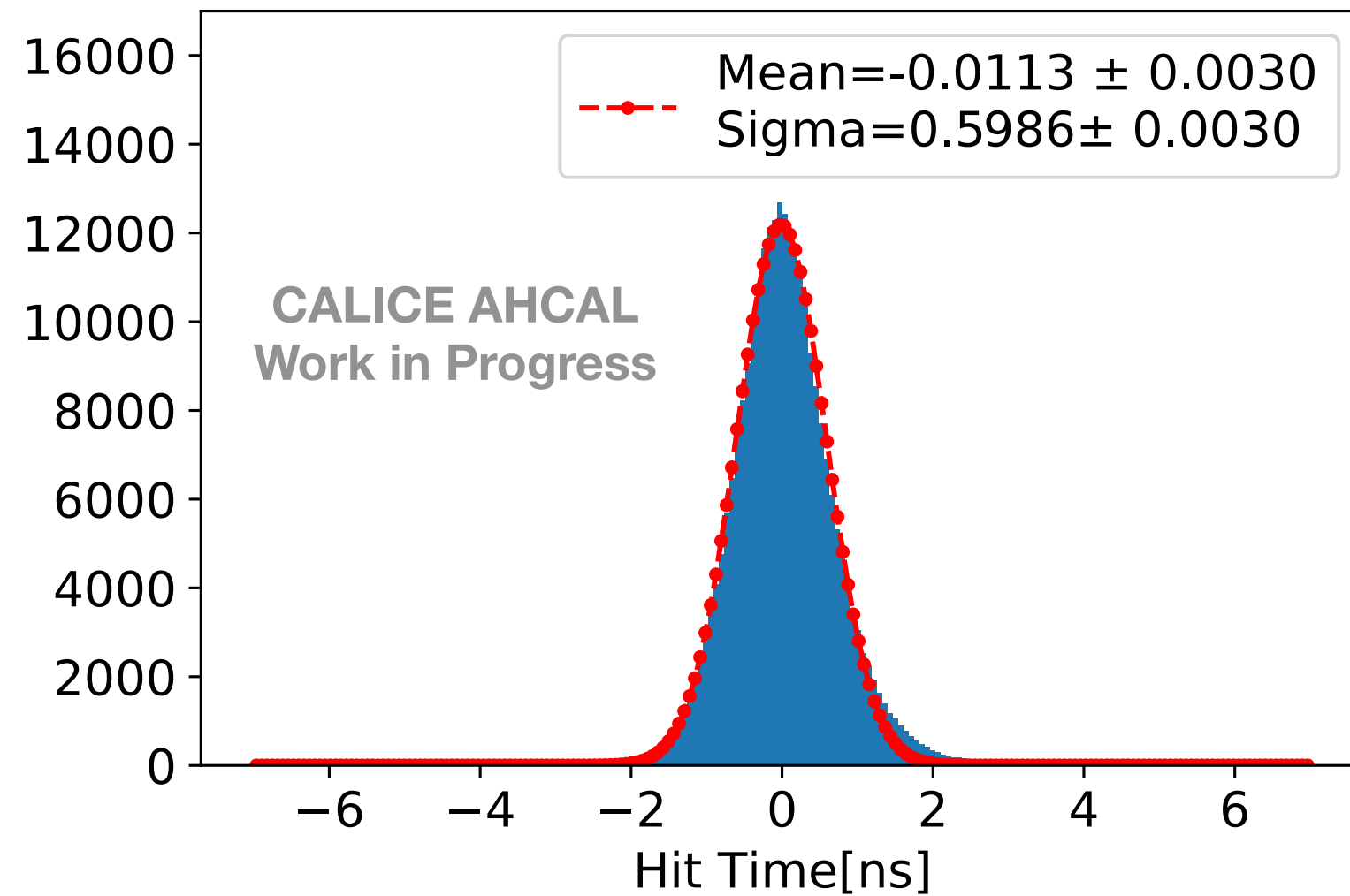
Time Resolution - Polystyrene Scintillator



Obtain time difference of hits in channel C and channel E to calculate the resolution

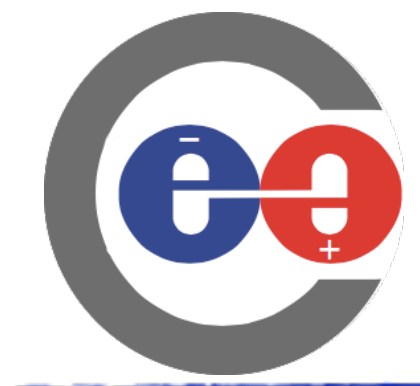


Time Resolution - Polystyrene Scintillator

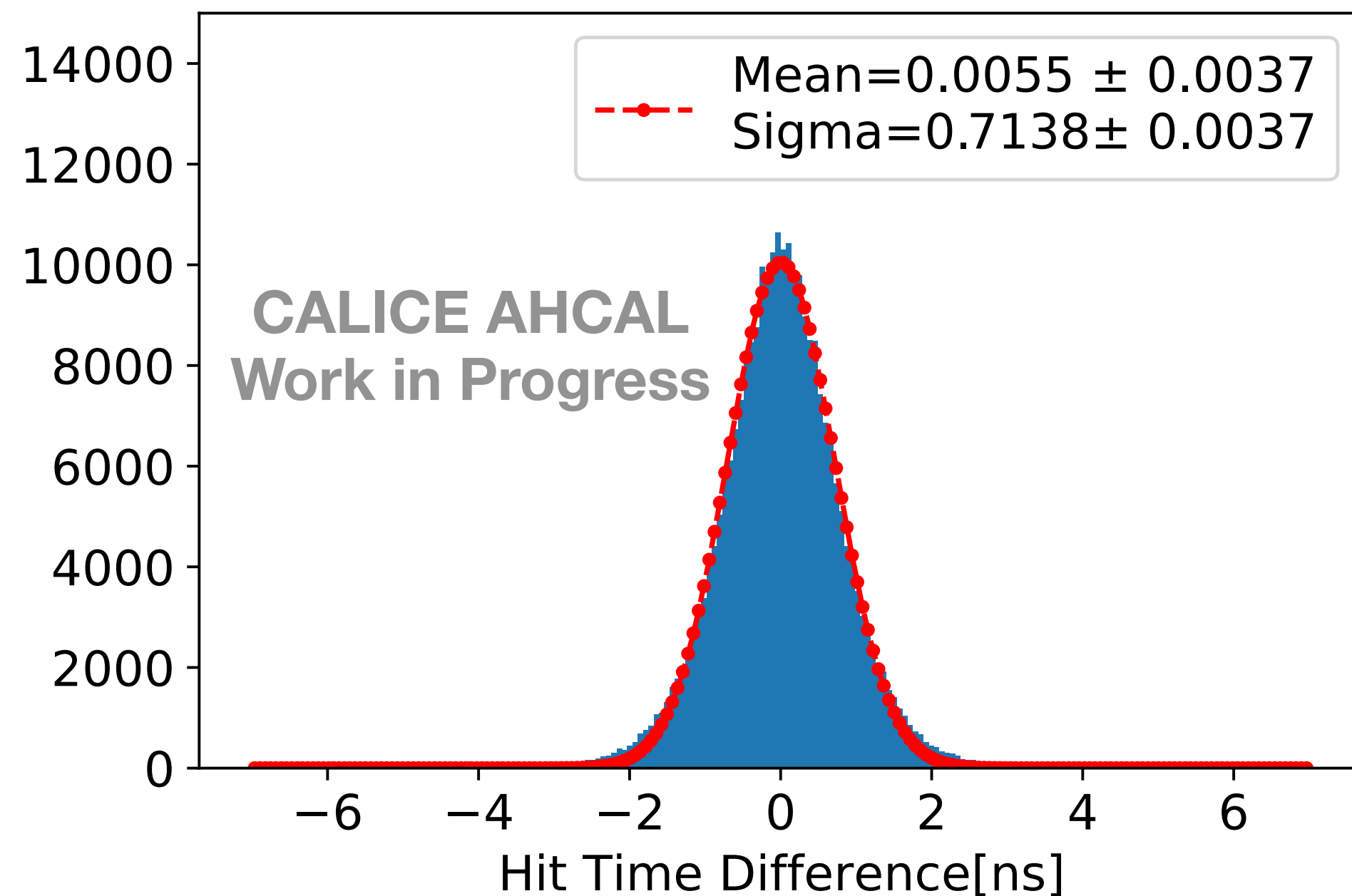
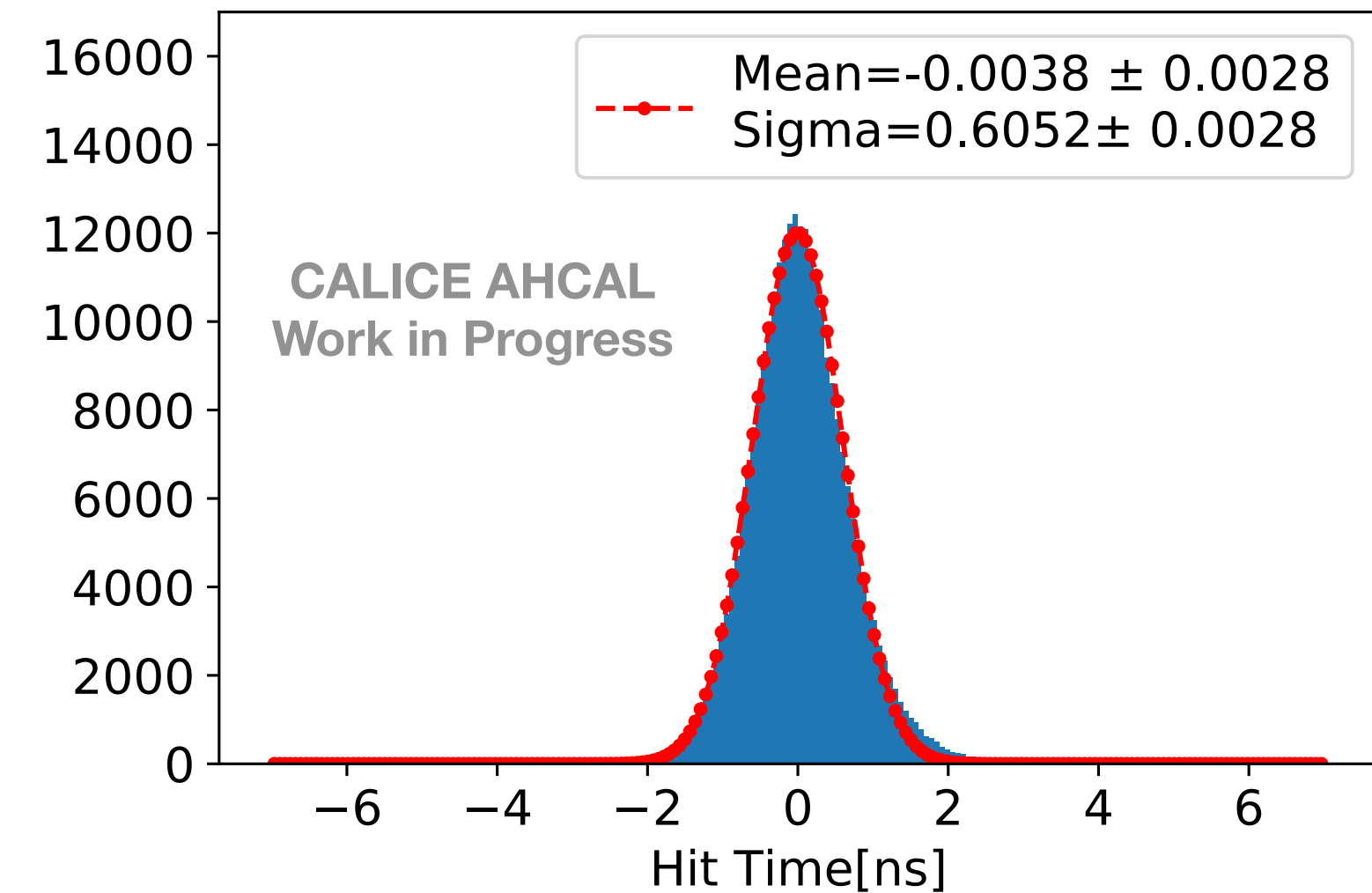
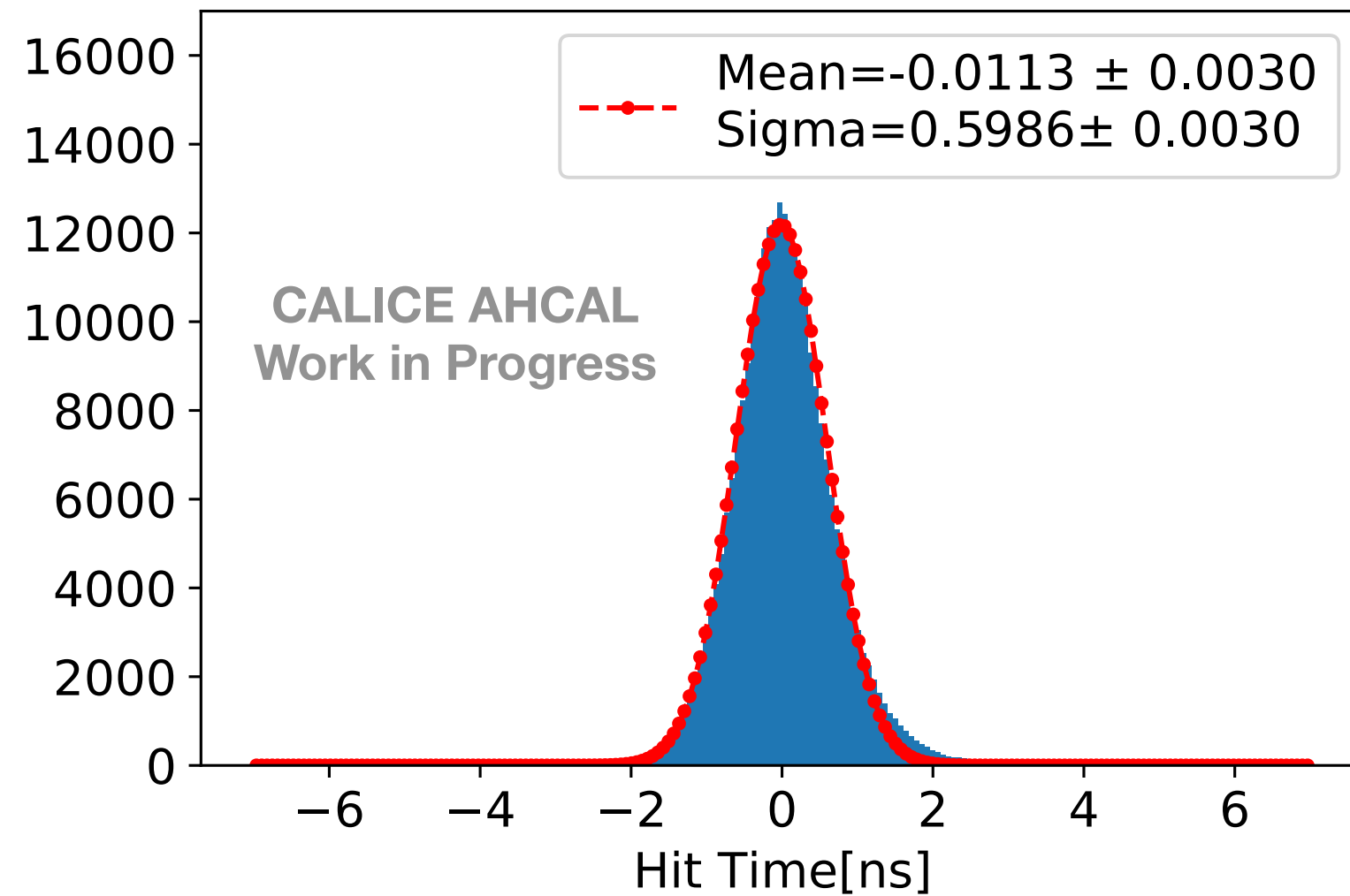


Time resolution = $0.718 / \sqrt{2} = 0.507 \text{ ns}$

- Interpret as Intrinsic time resolution of SiPM-on-Tile



Time Resolution - Polystyrene Scintillator

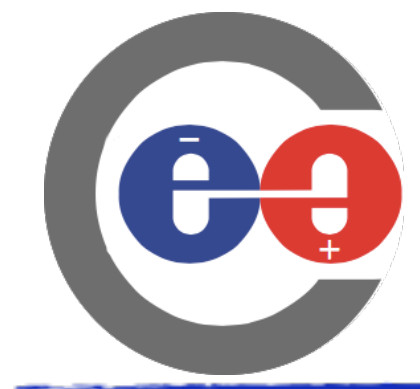


Time resolution = $0.718 / \sqrt{2} = 0.507 \text{ ns}$

- Interpret as Intrinsic time resolution of SiPM-on-Tile

Compared to 0.780 ns of the AHCAL:

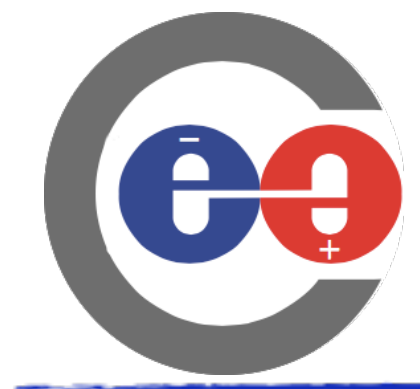
- AHCAL front-end contributes $\sim 0.6 \text{ ns}$



Conclusion and Outlook

Successful implementation of modular tile test setup:

- Tested at DESY in October 2020
- Simple setup allows for various configurations and short access times

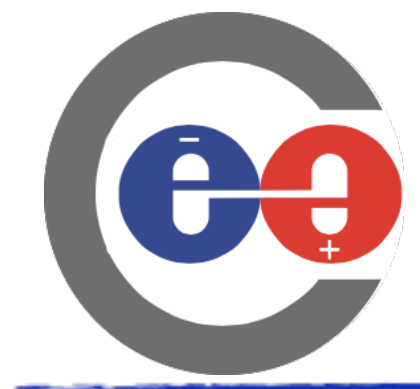


Conclusion and Outlook

Time resolution of SiPM-on-Tile configuration with polystyrene scintillator at 0.507ns

AHCAL prototype time resolution at 0.780ns:

- Front-end contributes ~0.6ns
- Improve front-end and SiPM-on-Tile configuration, to significantly improve the AHCAL time resolution



Conclusion and Outlook

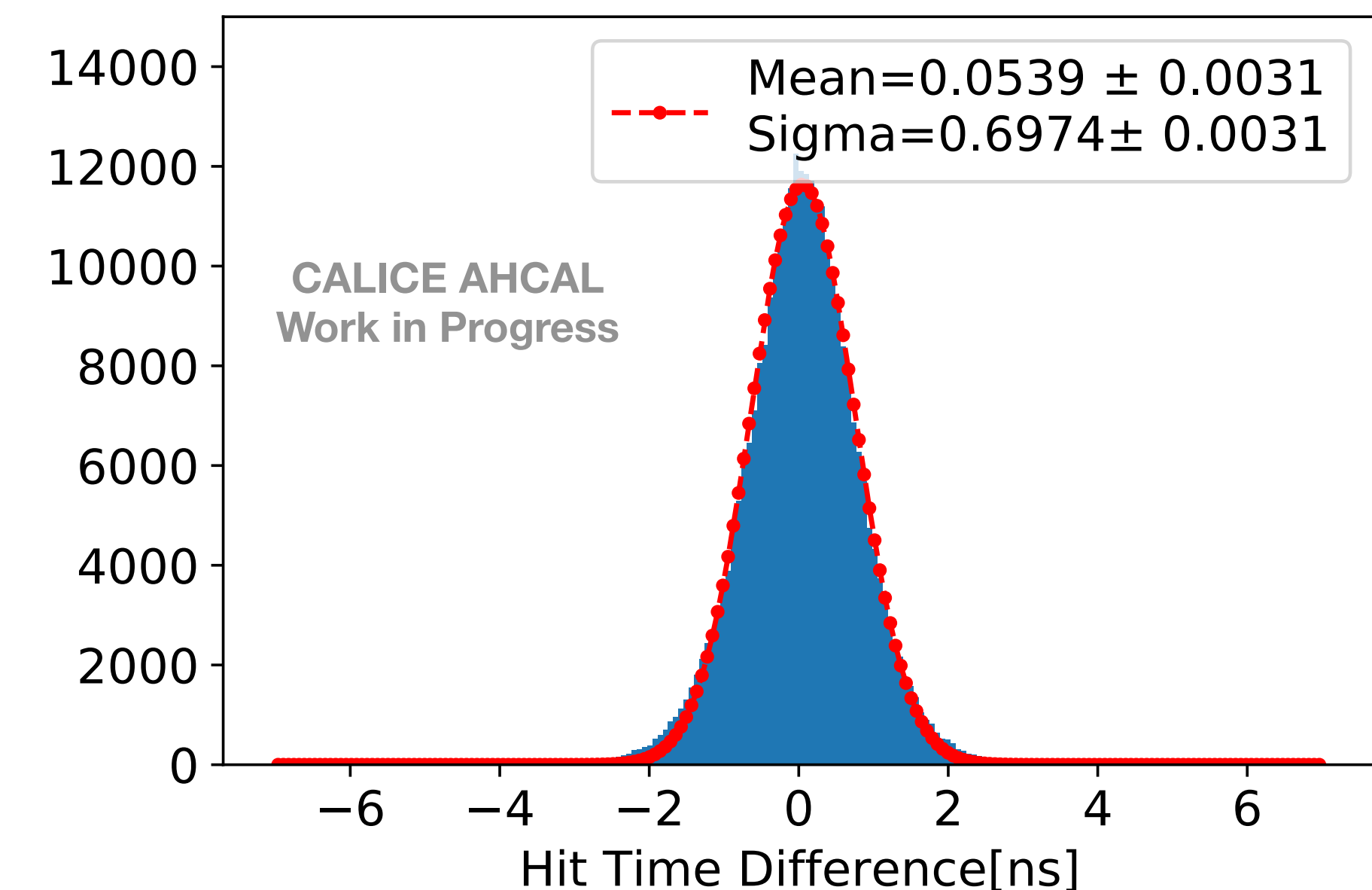
Time resolution of SiPM-on-Tile configuration with polystyrene scintillator at 0.507ns

AHCAL prototype time resolution at 0.780ns:

- Front-end contributes ~0.6ns
- Improve front-end and SiPM-on-Tile configuration, to significantly improve the AHCAL time resolution

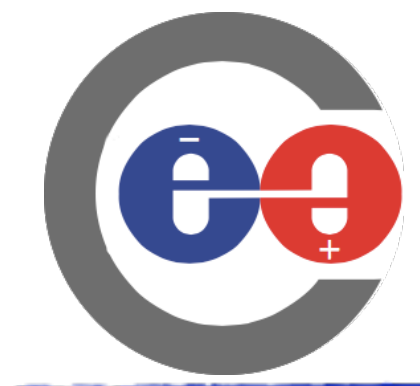
Analysis of full dataset ongoing:

- Preliminary analysis of SiPM-on-Tile configuration with BC408 scintillator yields a time resolution of $0.697\text{ns}/\sqrt{2}=0.490\text{ns}$

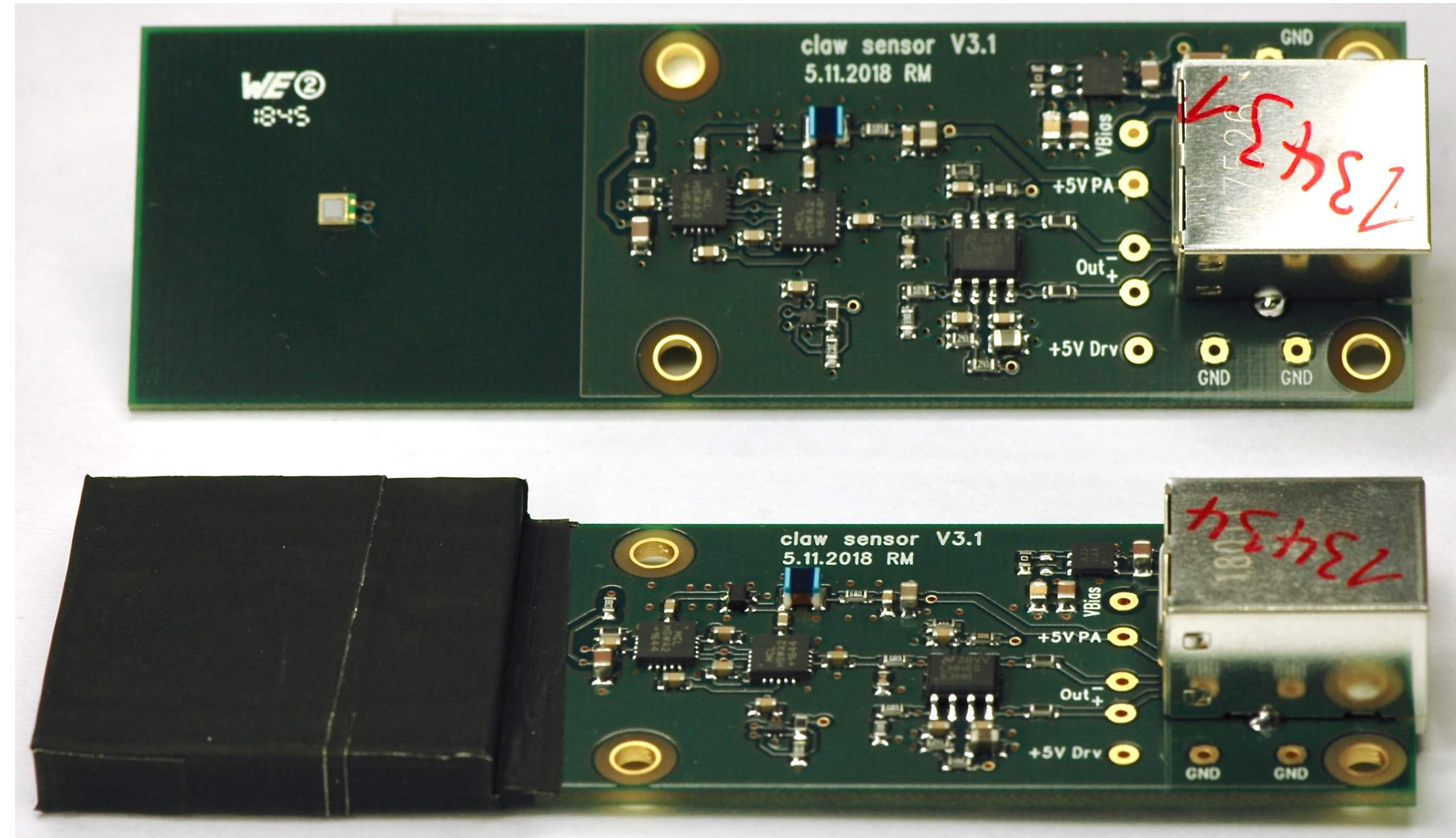
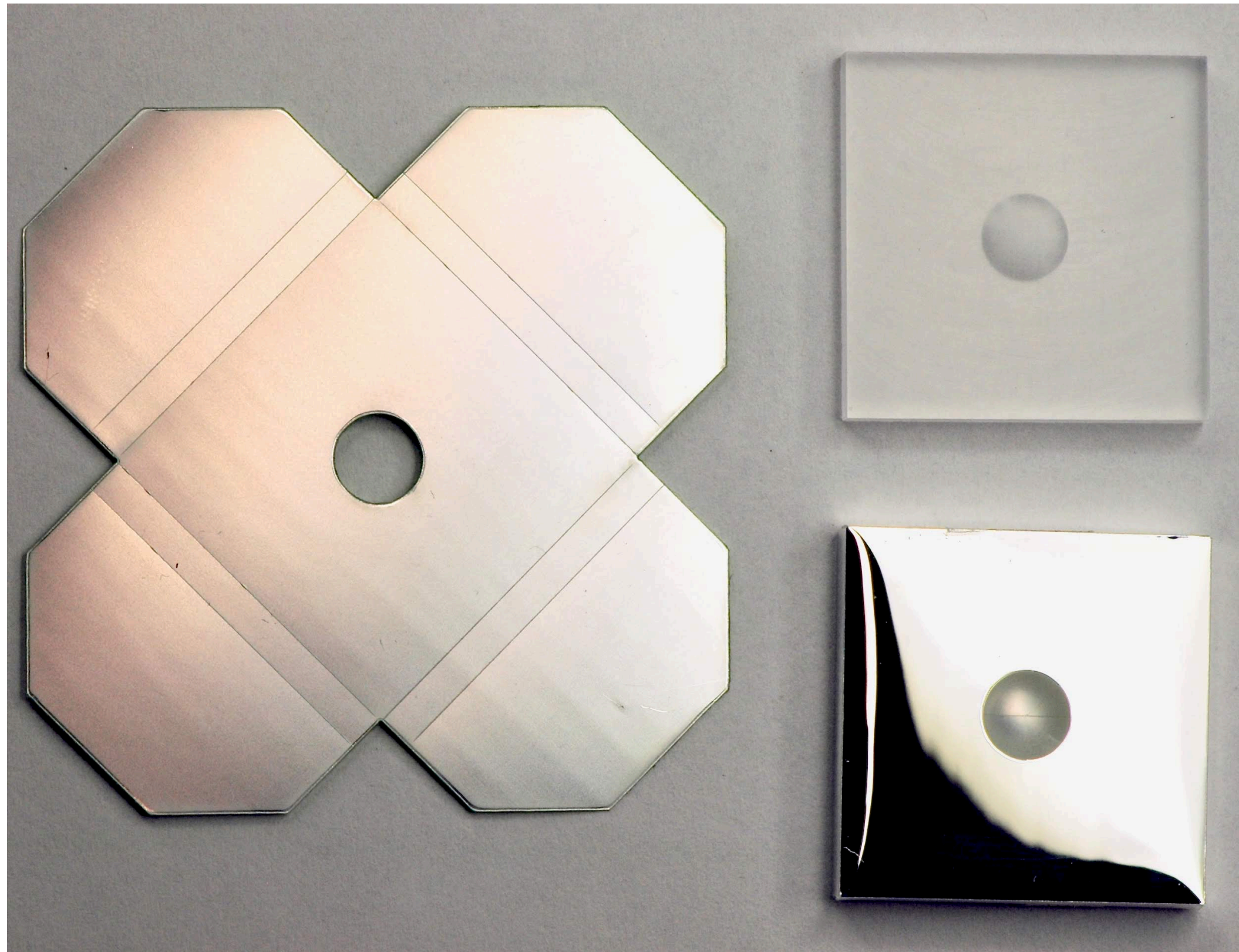


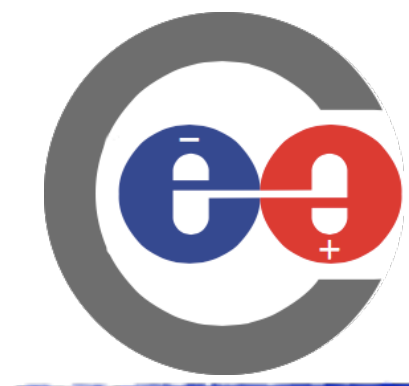


Backup



Sensors

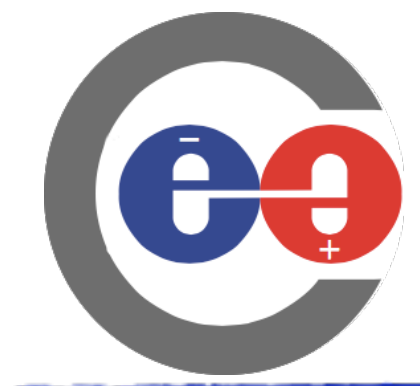




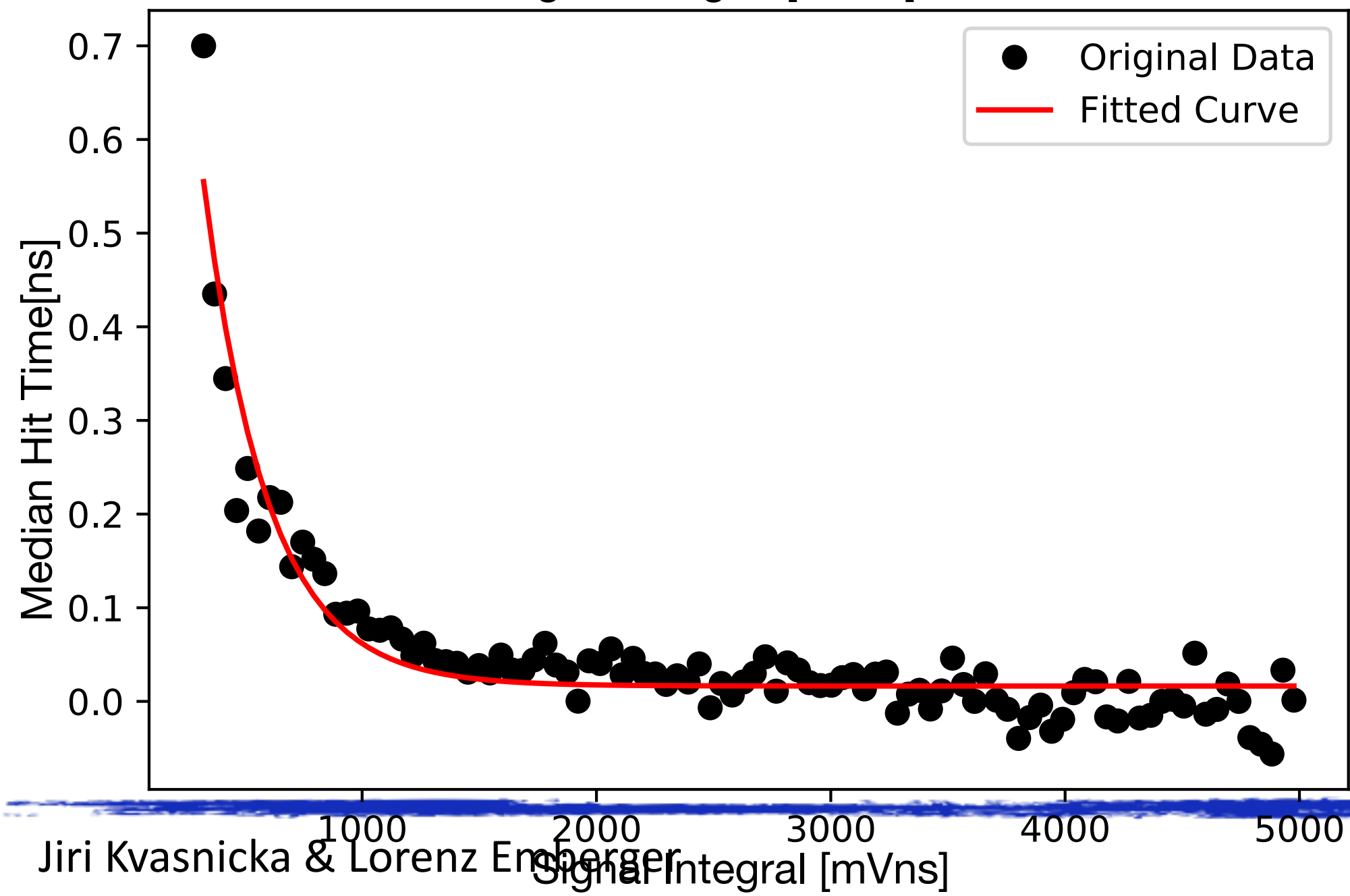
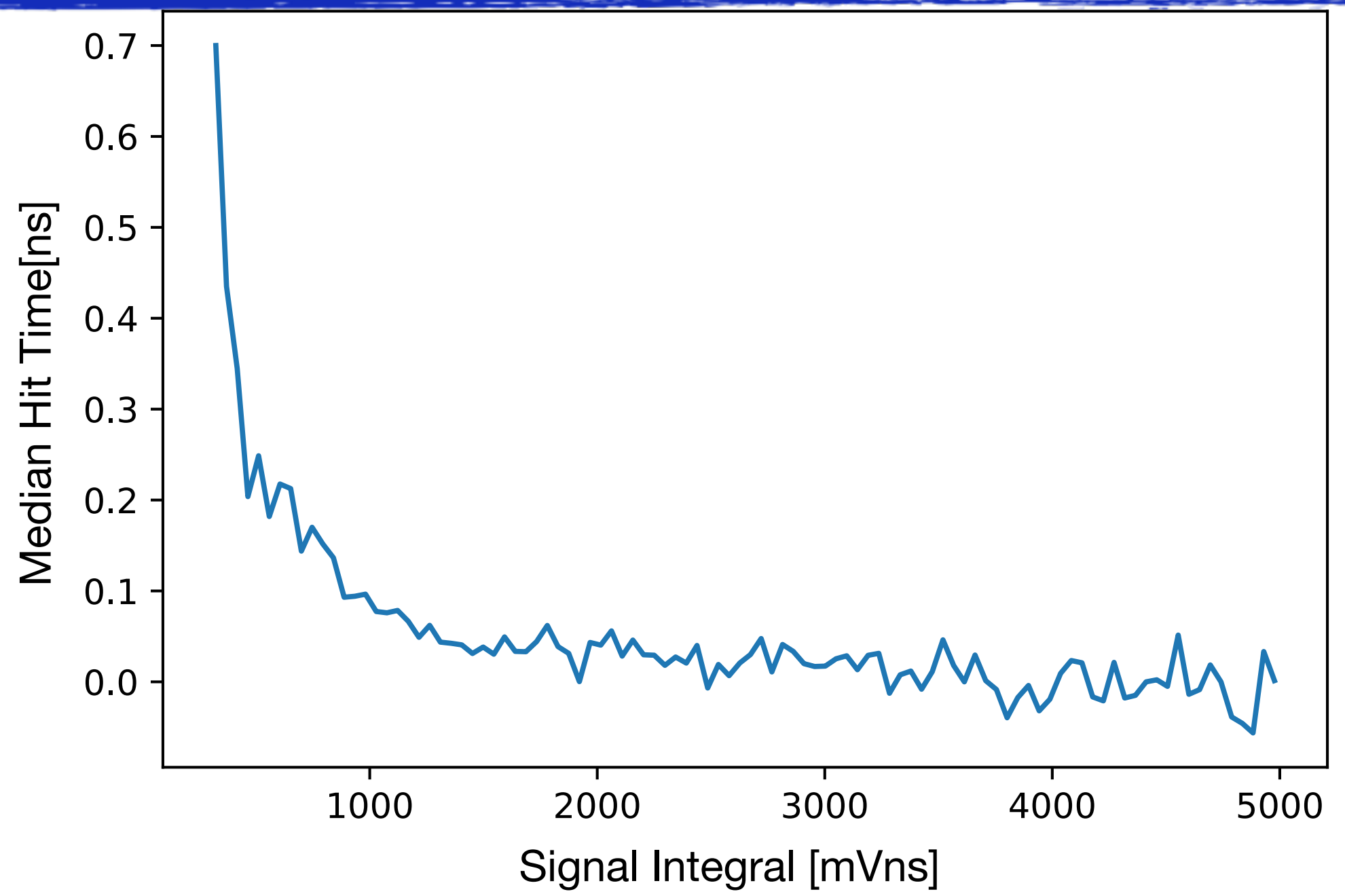
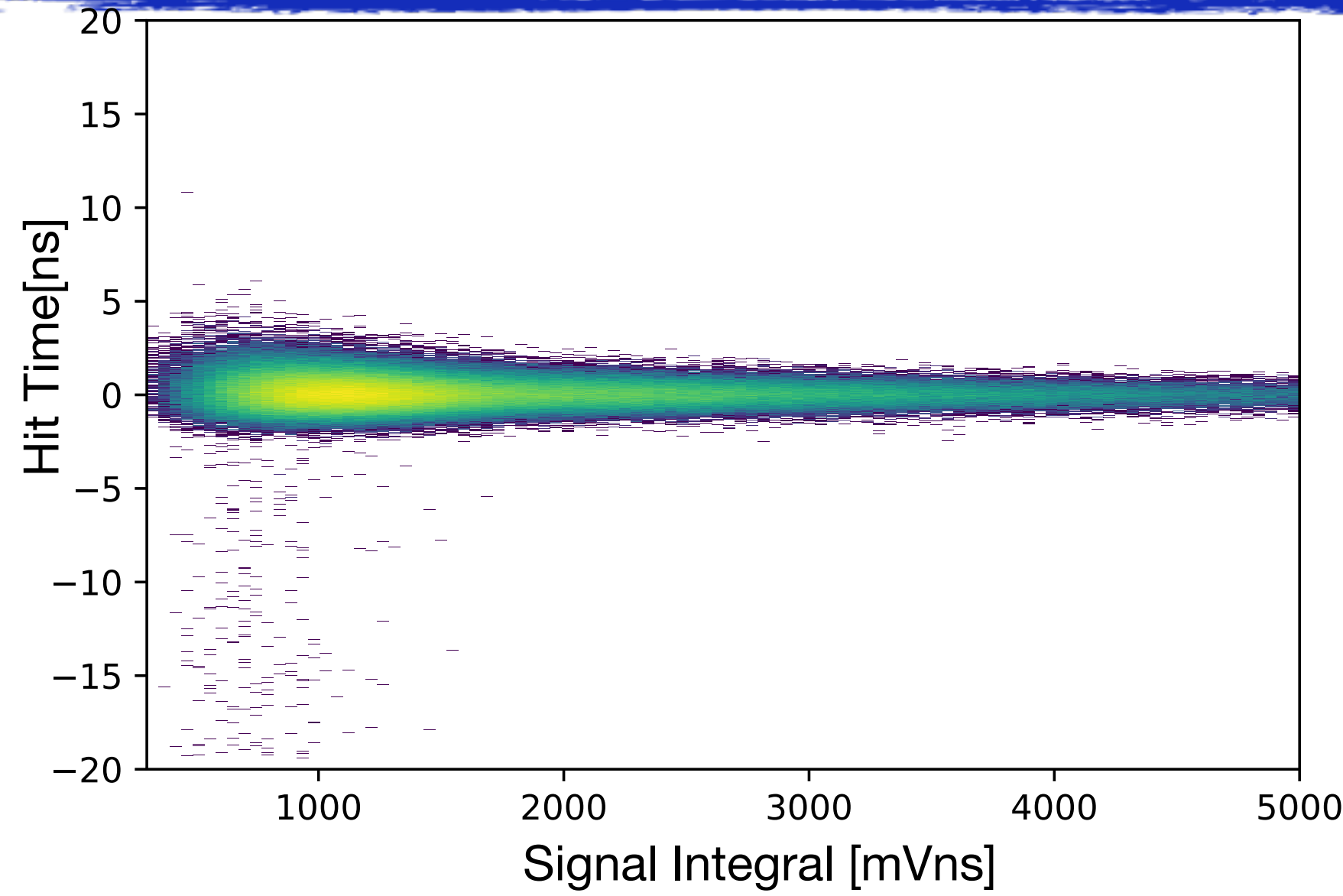
SiPM

Number of channels	1 ch
Effective photosensitive area / ch	1.3 × 1.3 mm
Number of pixels /ch	2668
Pixel size	25 μm
Spectral response range	320 to 900 nm
Gain (typ.)	7.0×10 ⁵

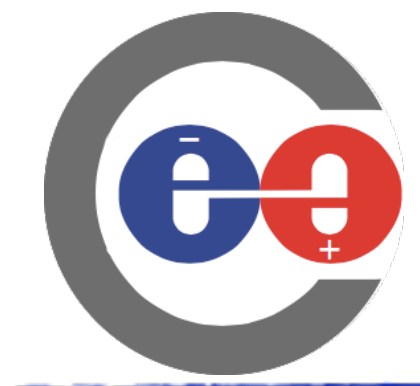
Information taken from: <https://www.hamamatsu.com/eu/en/product/type/S13360-1325PE/index.html>



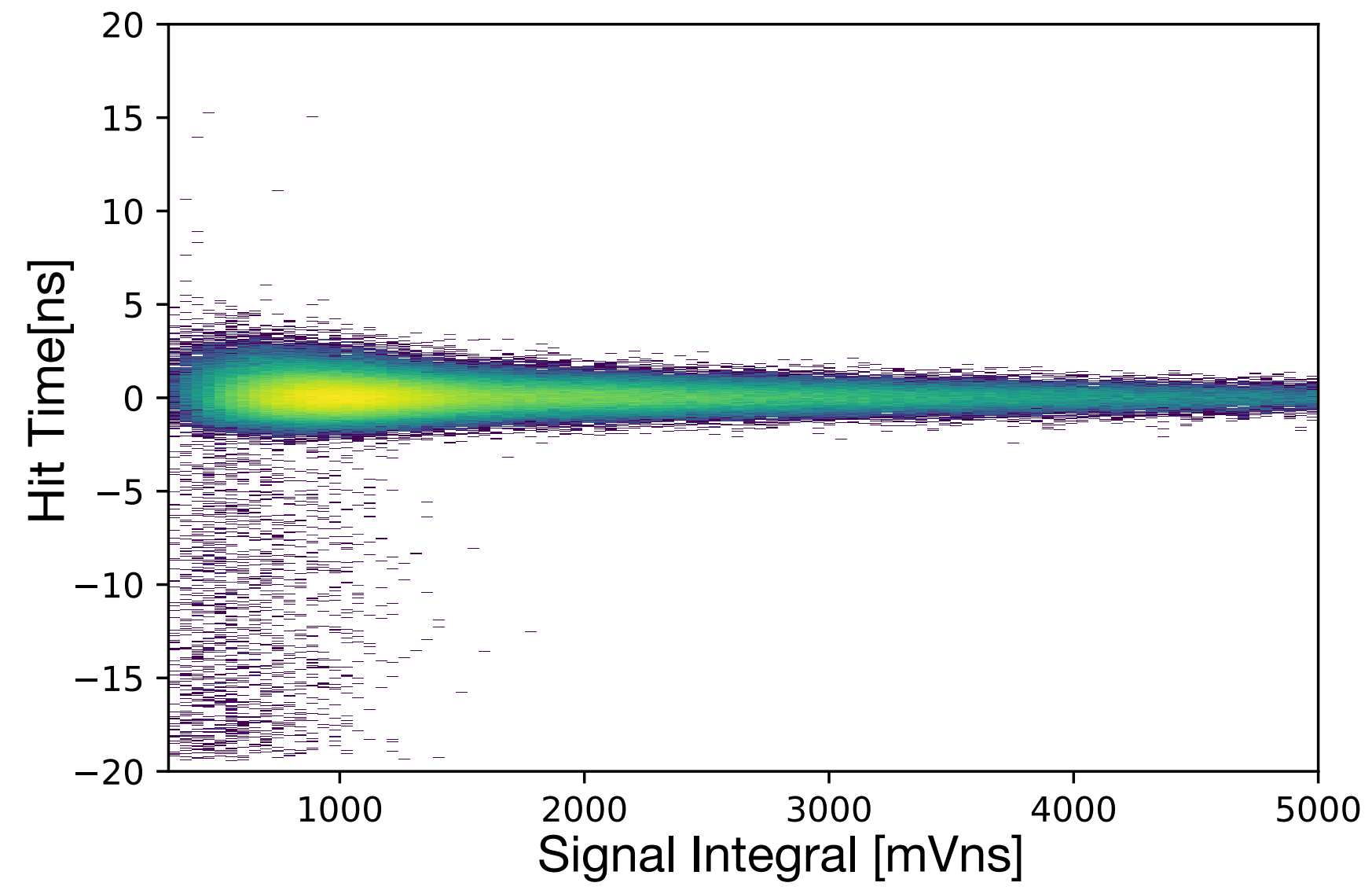
Time Walk Channel E

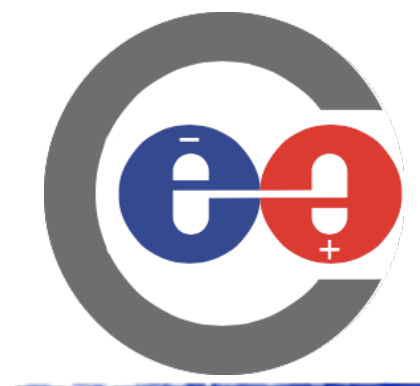


$$\text{Correction} = 1.7578 \cdot \exp(-0.0037 \cdot x) + 0.0164$$

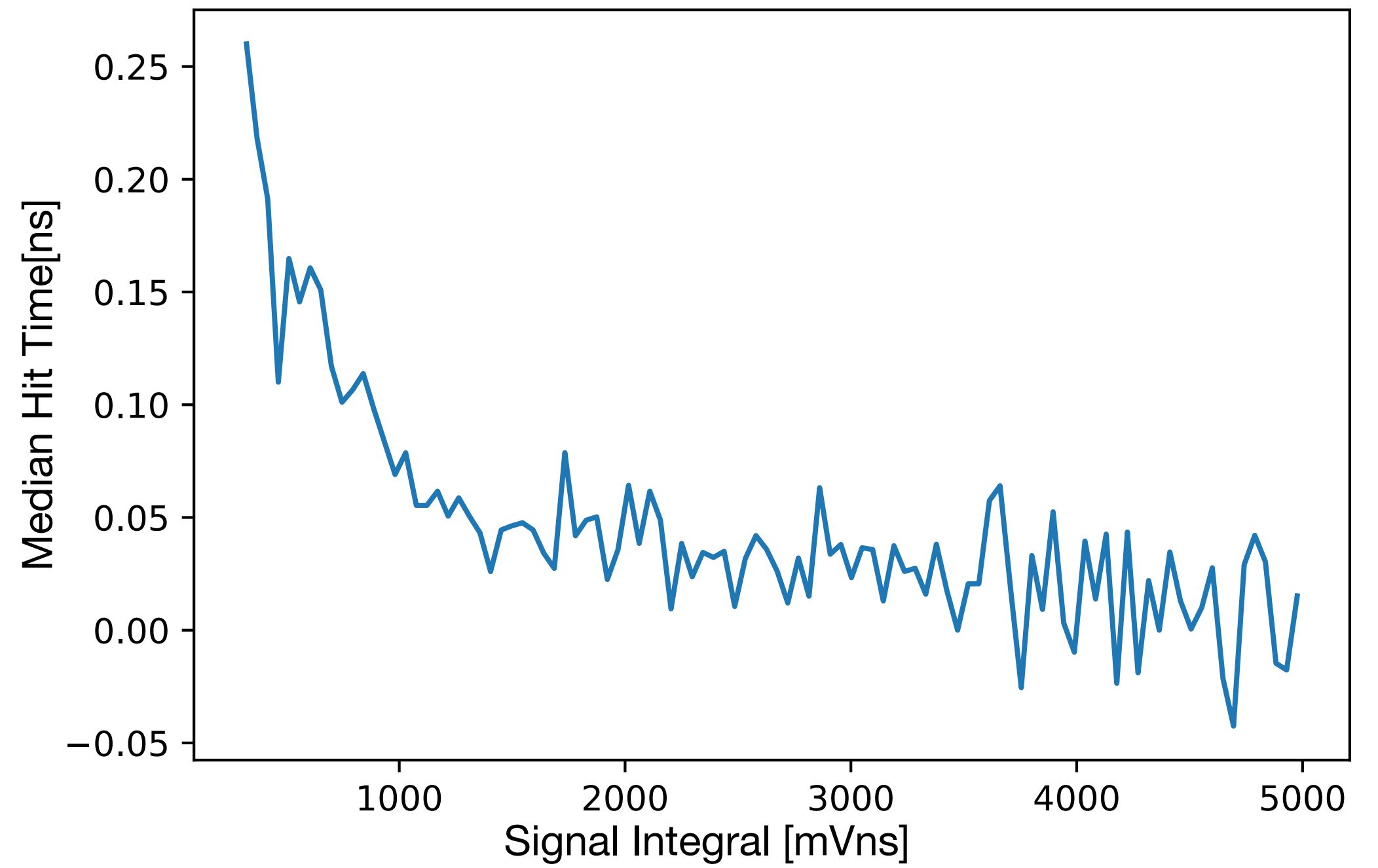
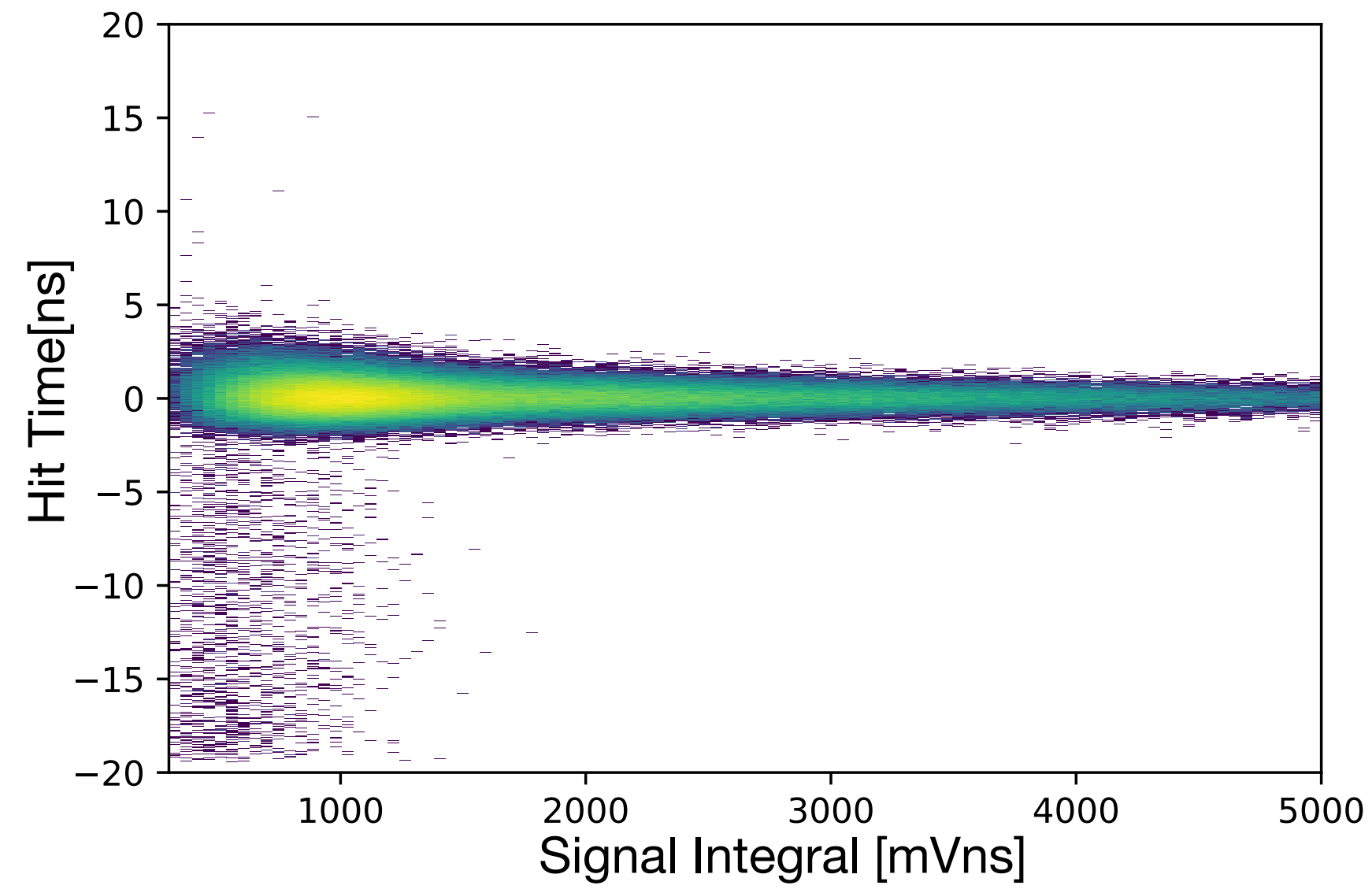


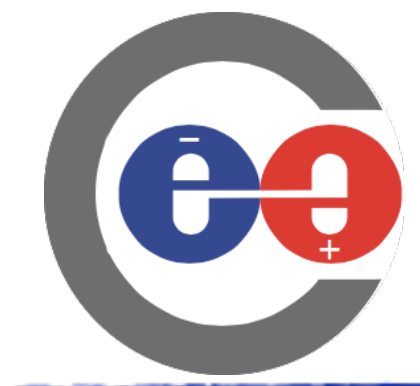
Time Walk Channel C



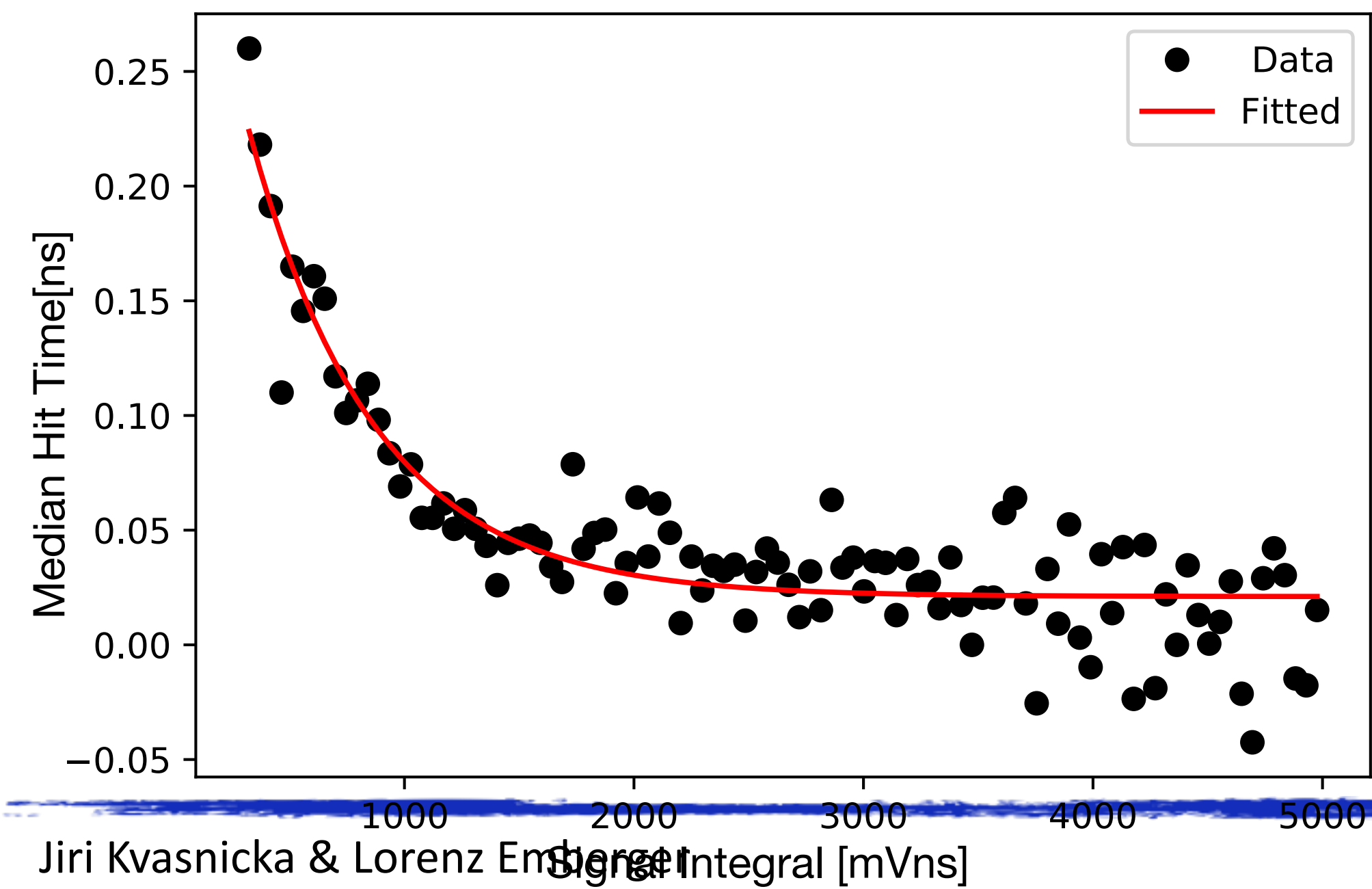
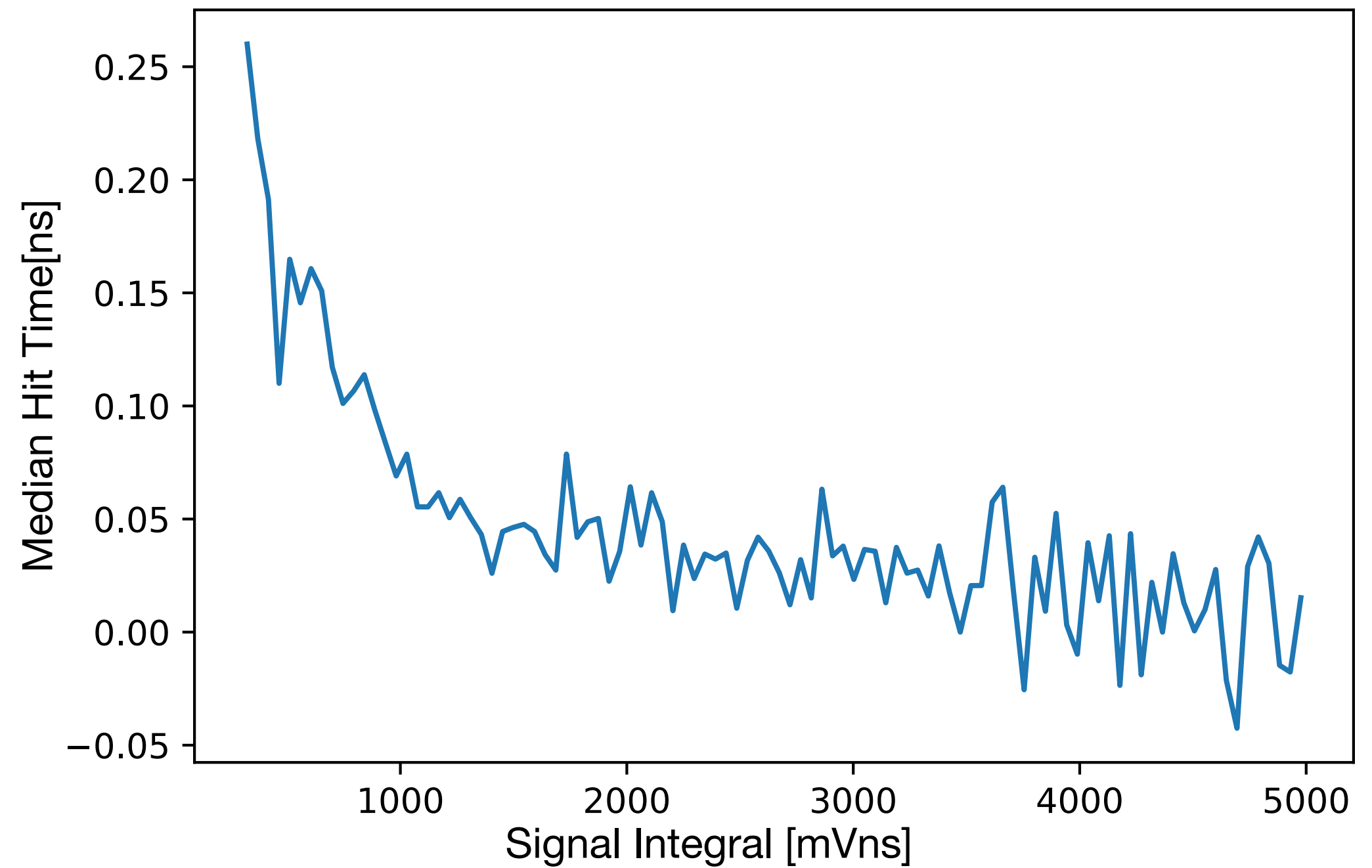
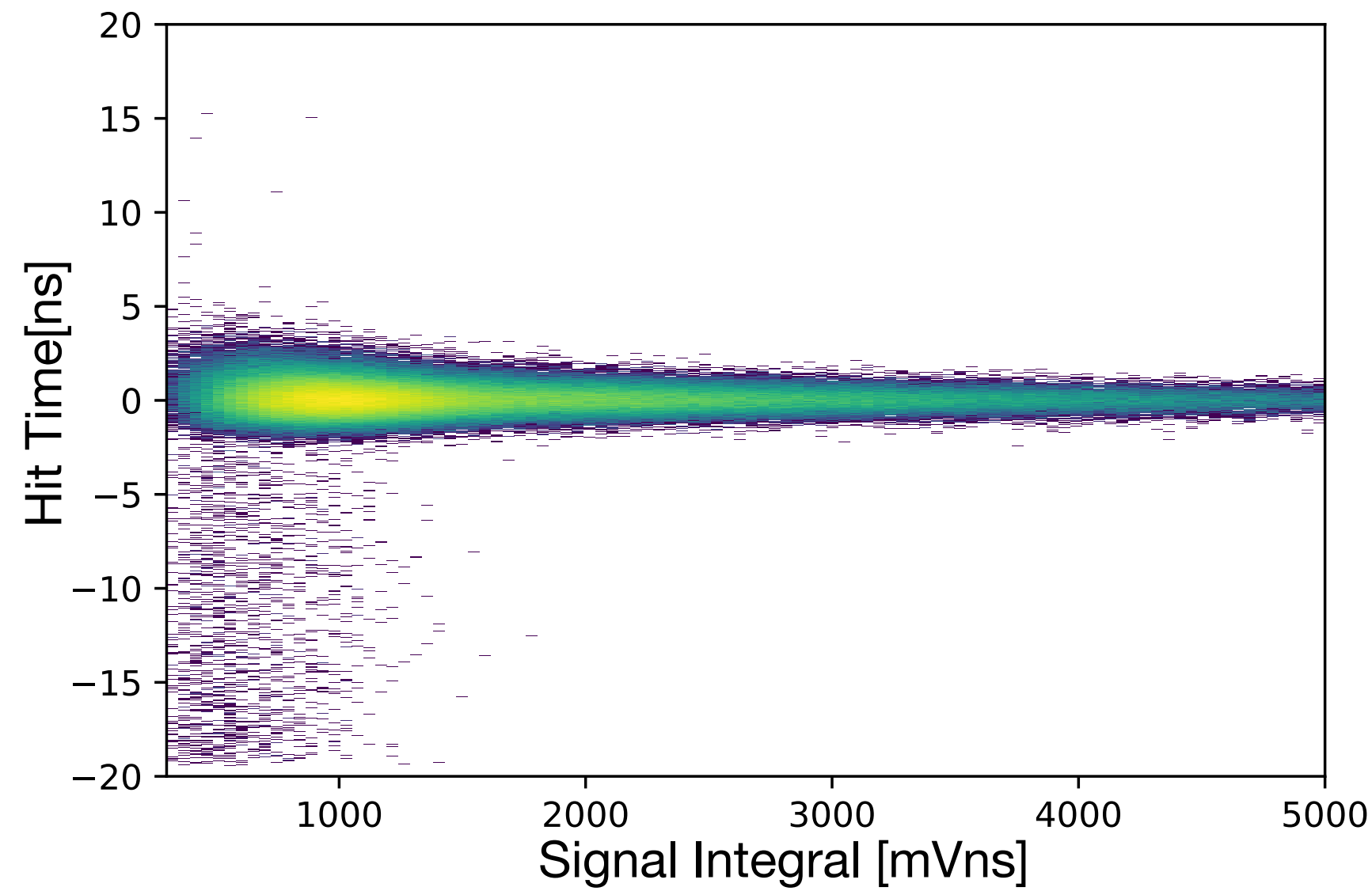


Time Walk Channel C



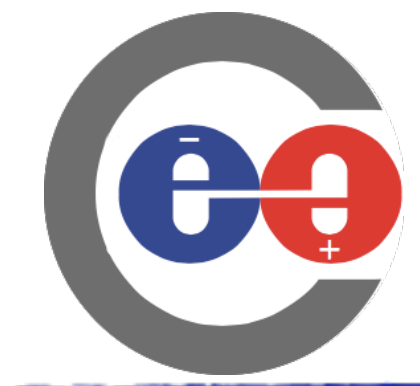


Time Walk Channel C



$$\text{Correction} = 0.3663 \cdot \exp(-0.0018 \cdot x) + 0.02103$$

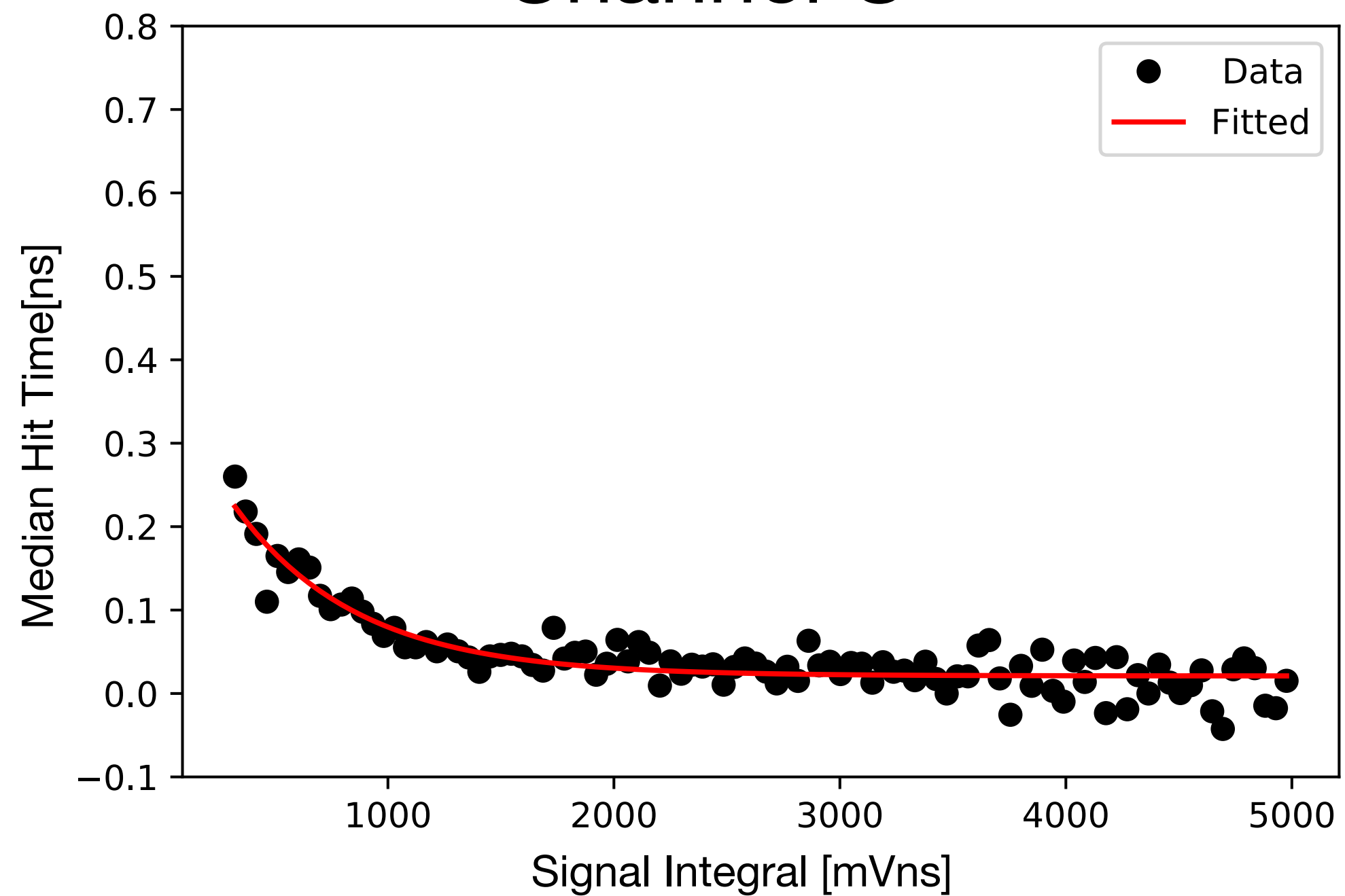
Same procedure done for channel E



Comparison of TW Behavior

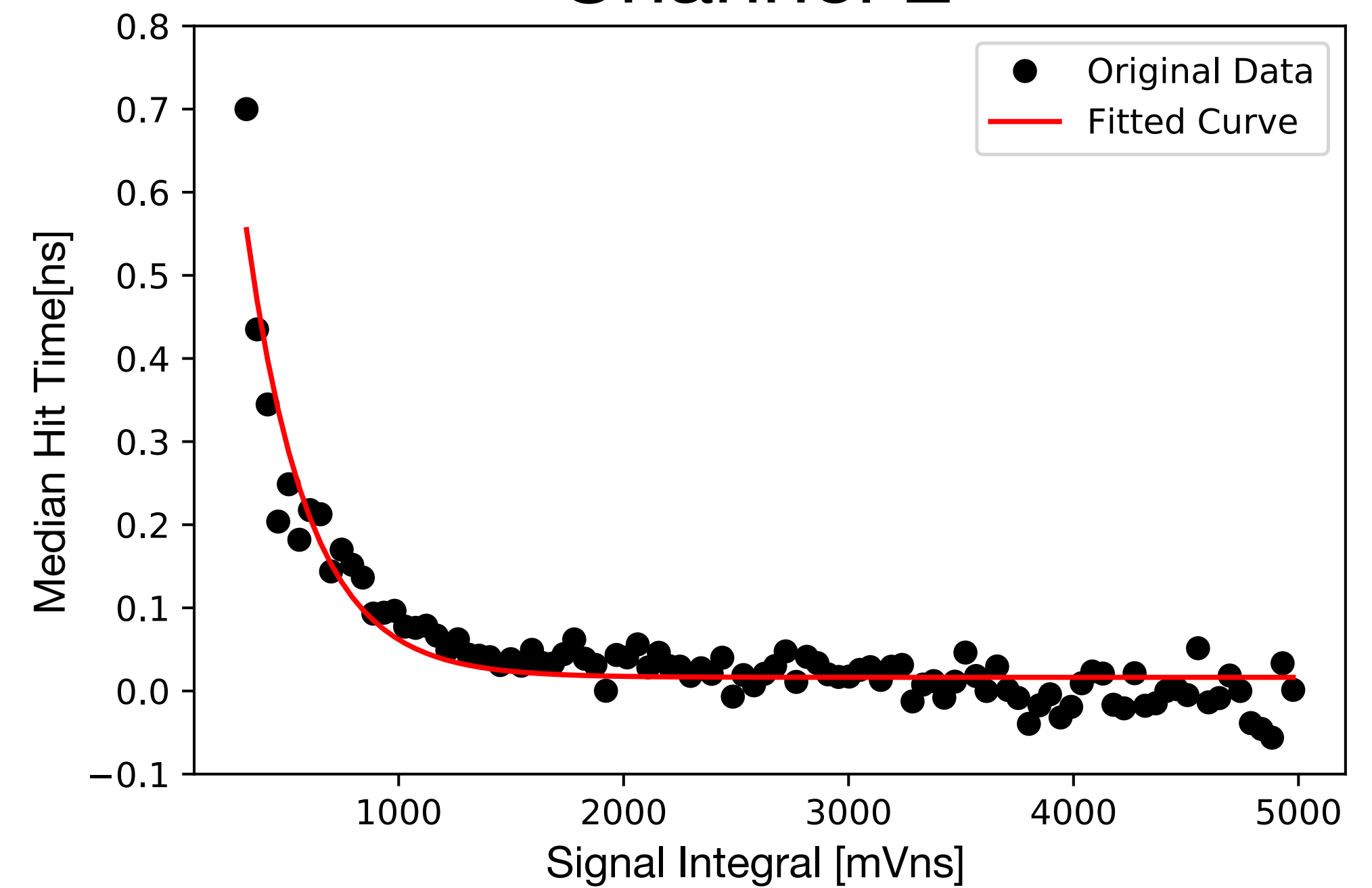
Very low statistics under ~500mVns

Channel C

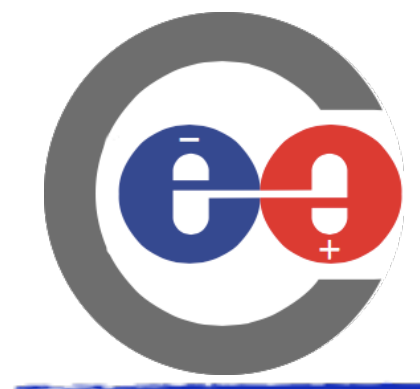


$$\text{Corr} = 0.3663 \cdot \exp(-0.0018 \cdot x) + 0.02103$$

Channel E



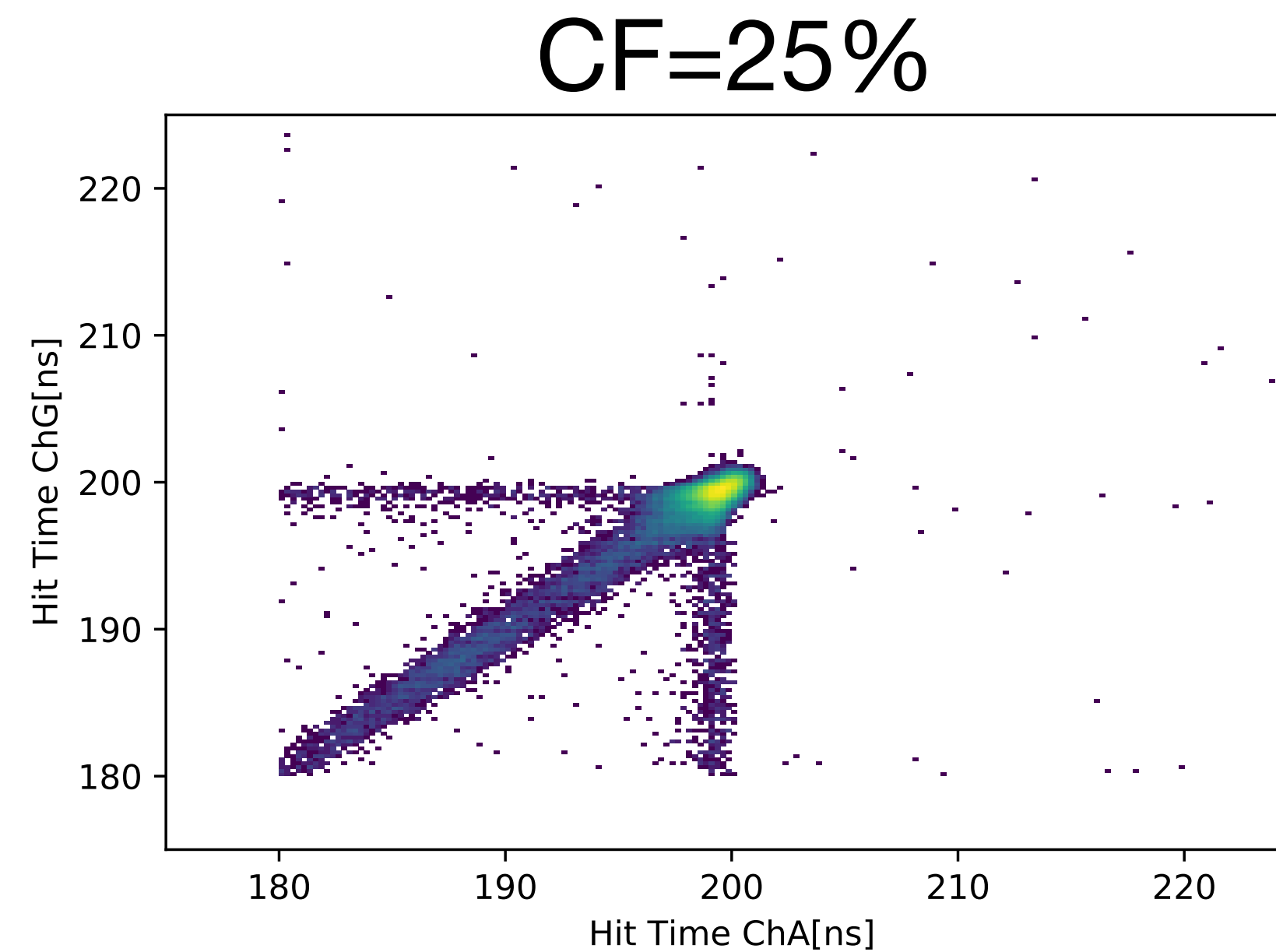
$$\text{Corr} = 1.7578 \cdot \exp(-0.0037 \cdot x) + 0.0164$$

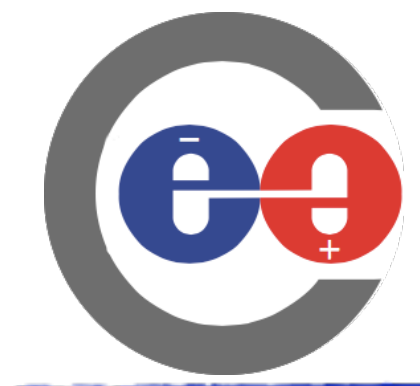


Trigger Channel A vs. Trigger Channel G

Pre-trigger time: $500\text{samples} * 0.4\text{ns/sample} = 200\text{ ns}$

Earlier trigger times from noise or additional particle arriving earlier

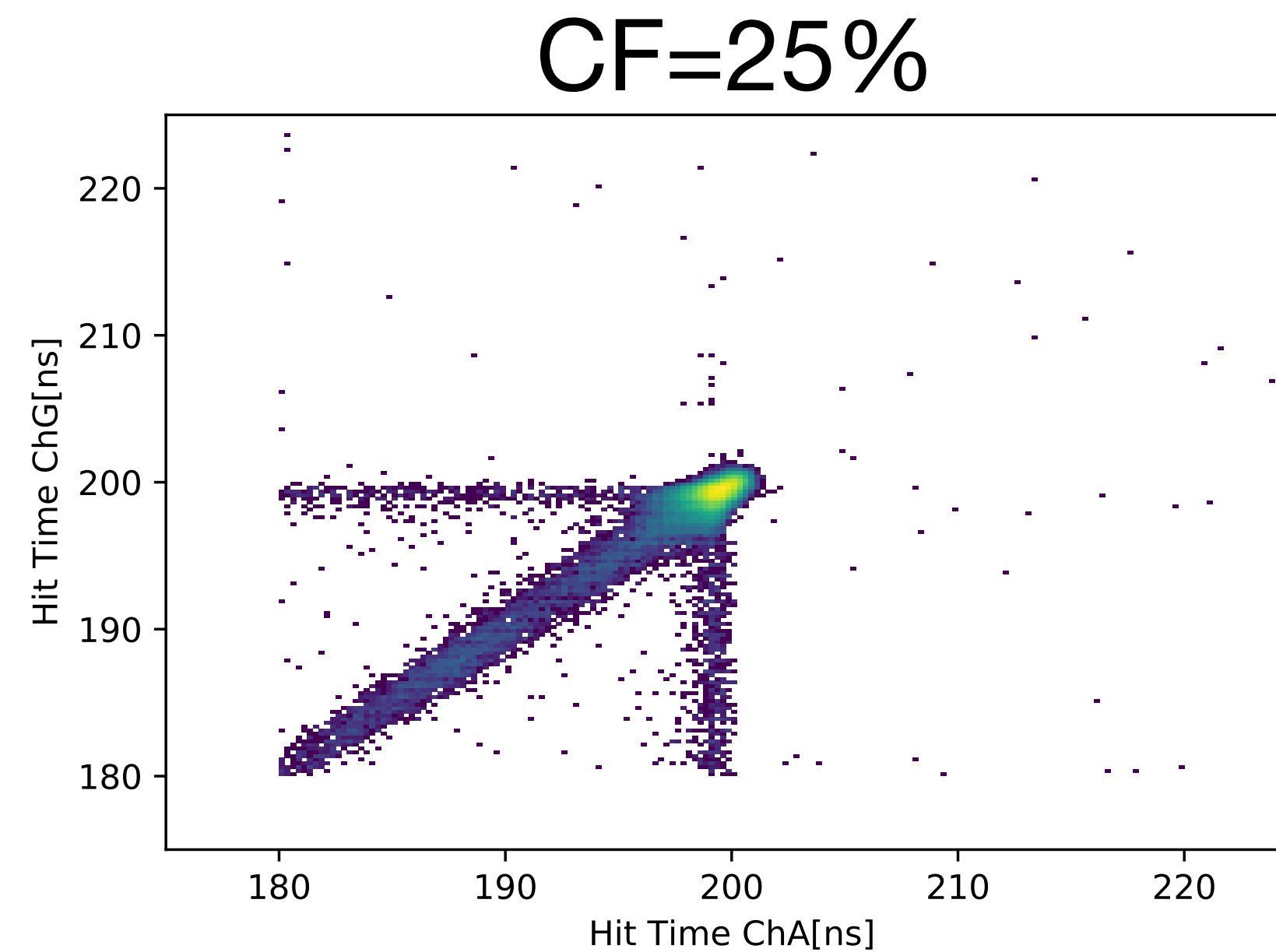




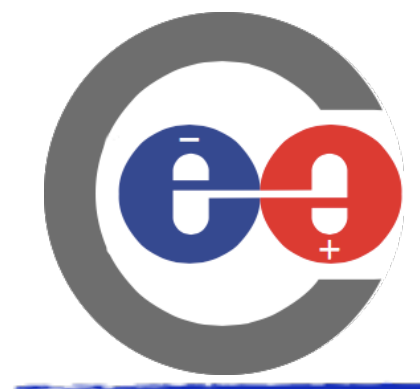
Trigger Channel A vs. Trigger Channel G

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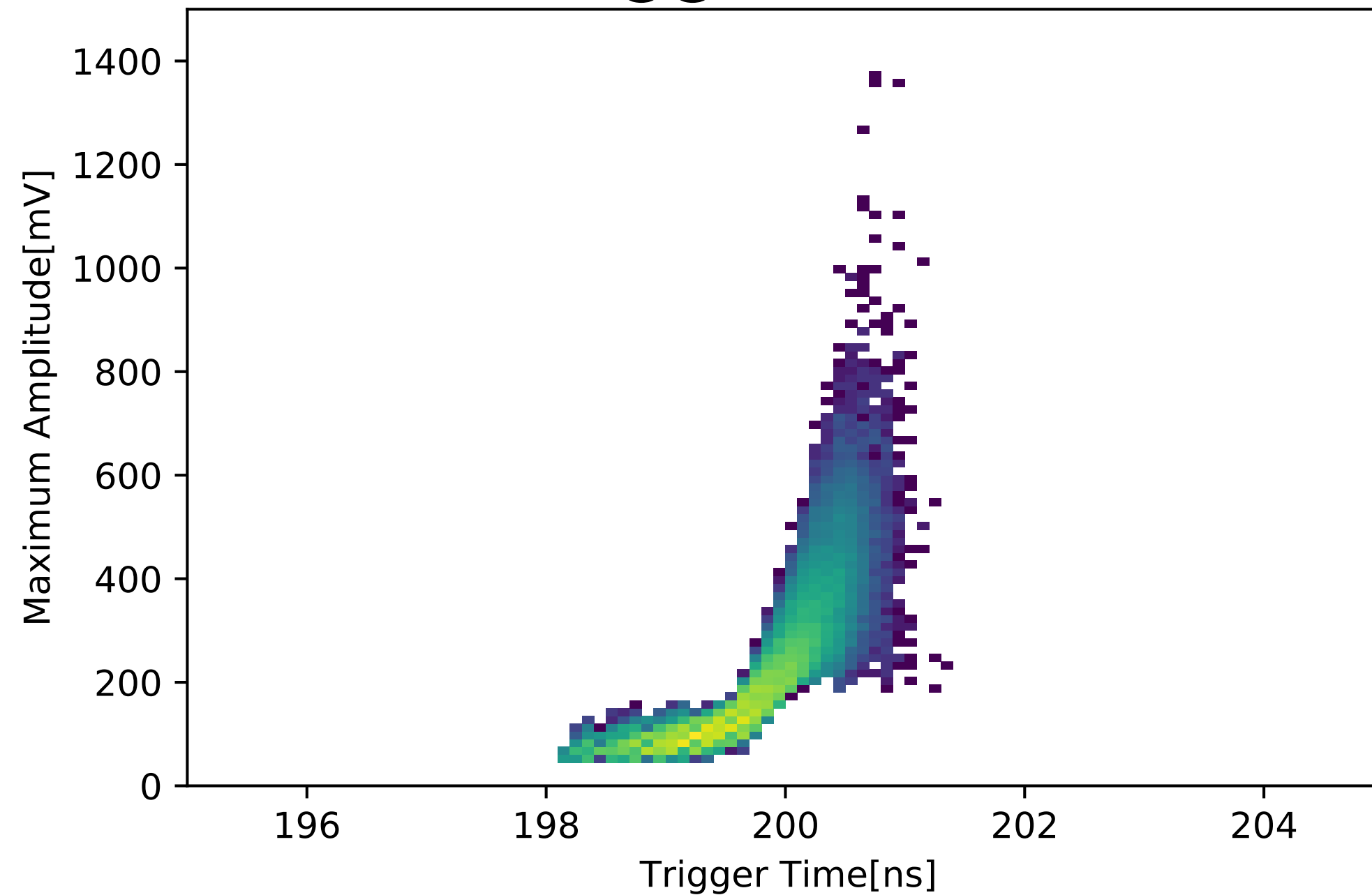


Only take triggers in between 198ns and 202ns

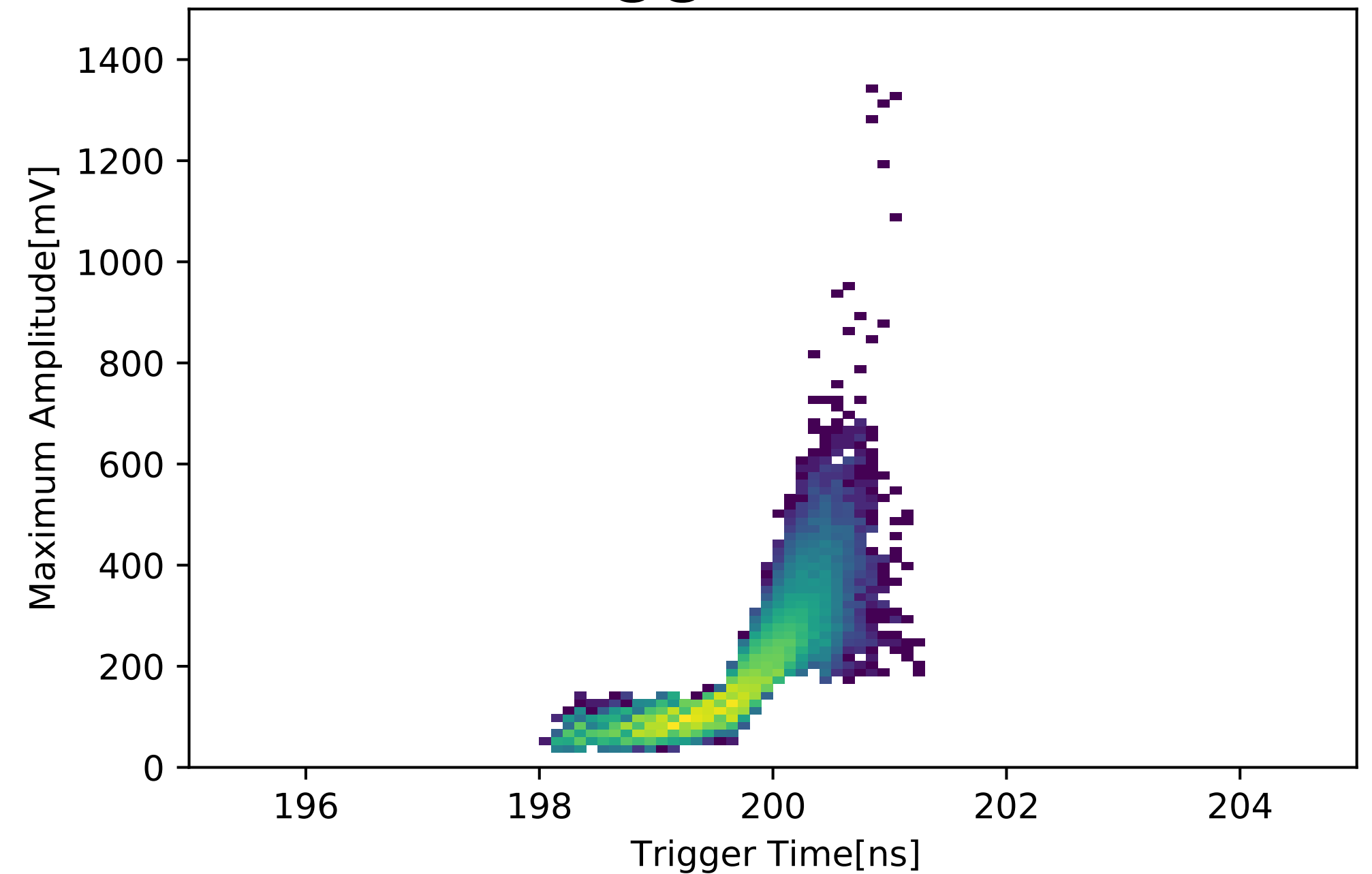


Trigger Time VS Amplitude

Trigger Channel A



Trigger Channel G



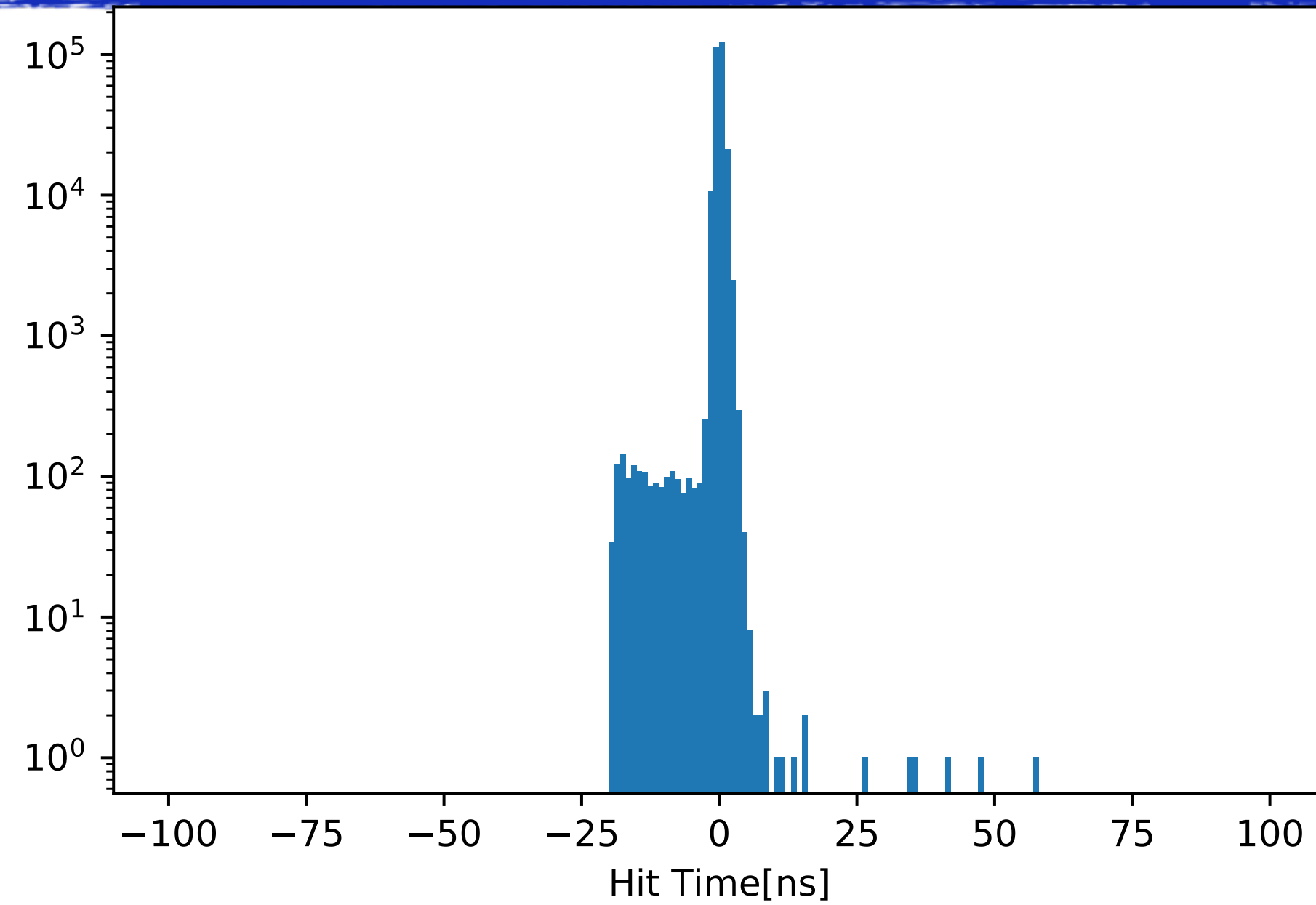
$(\text{TriggerA} + \text{TriggerG}) / 2 = \text{reference time}$

$\text{Relative hit time} = \text{hit time} - \text{reference time}$

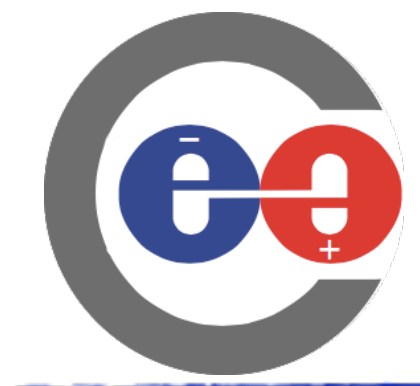


Relative Hit Time in Signal Channels

Channel C

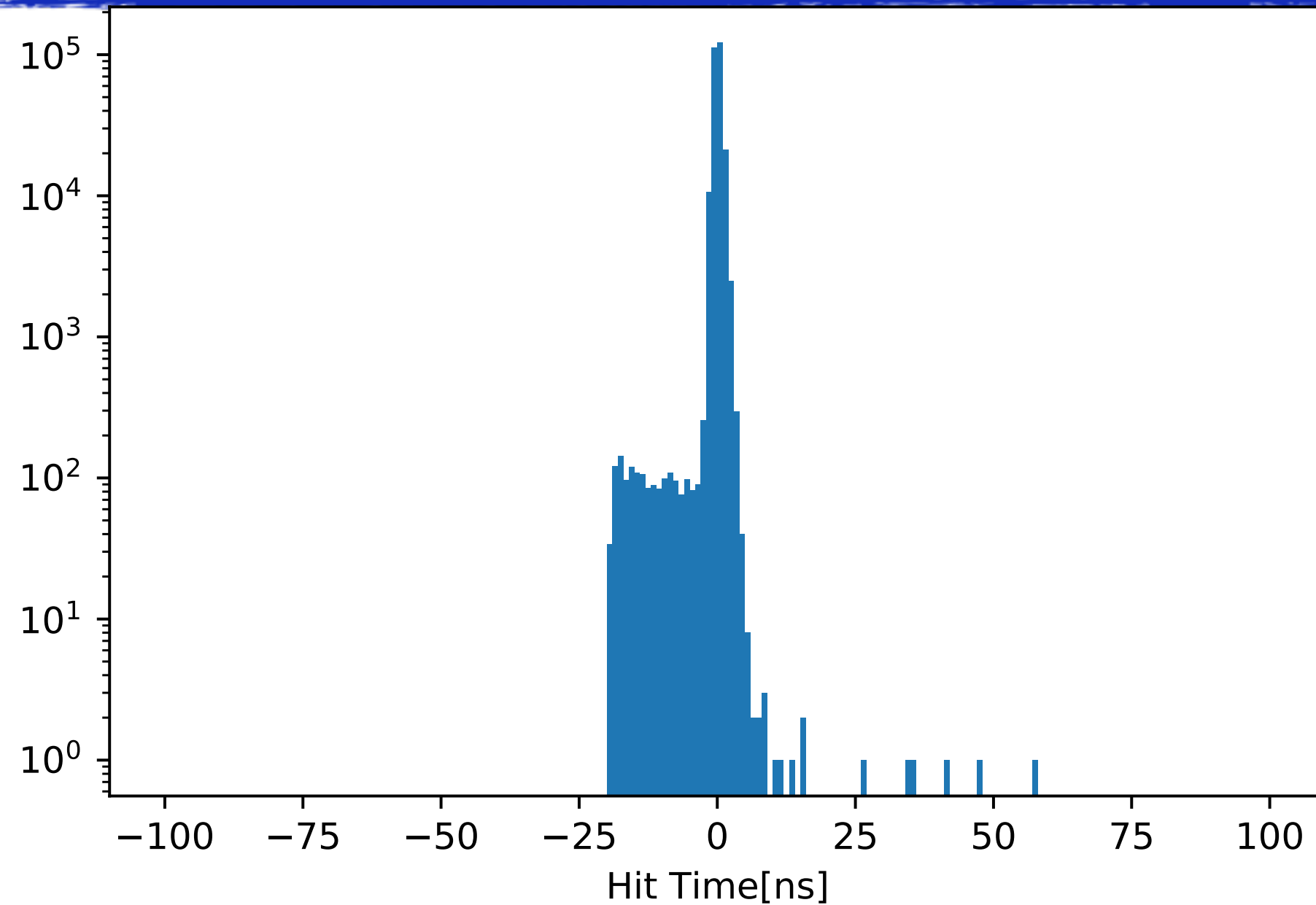


Tail to early times from noise or additional particle

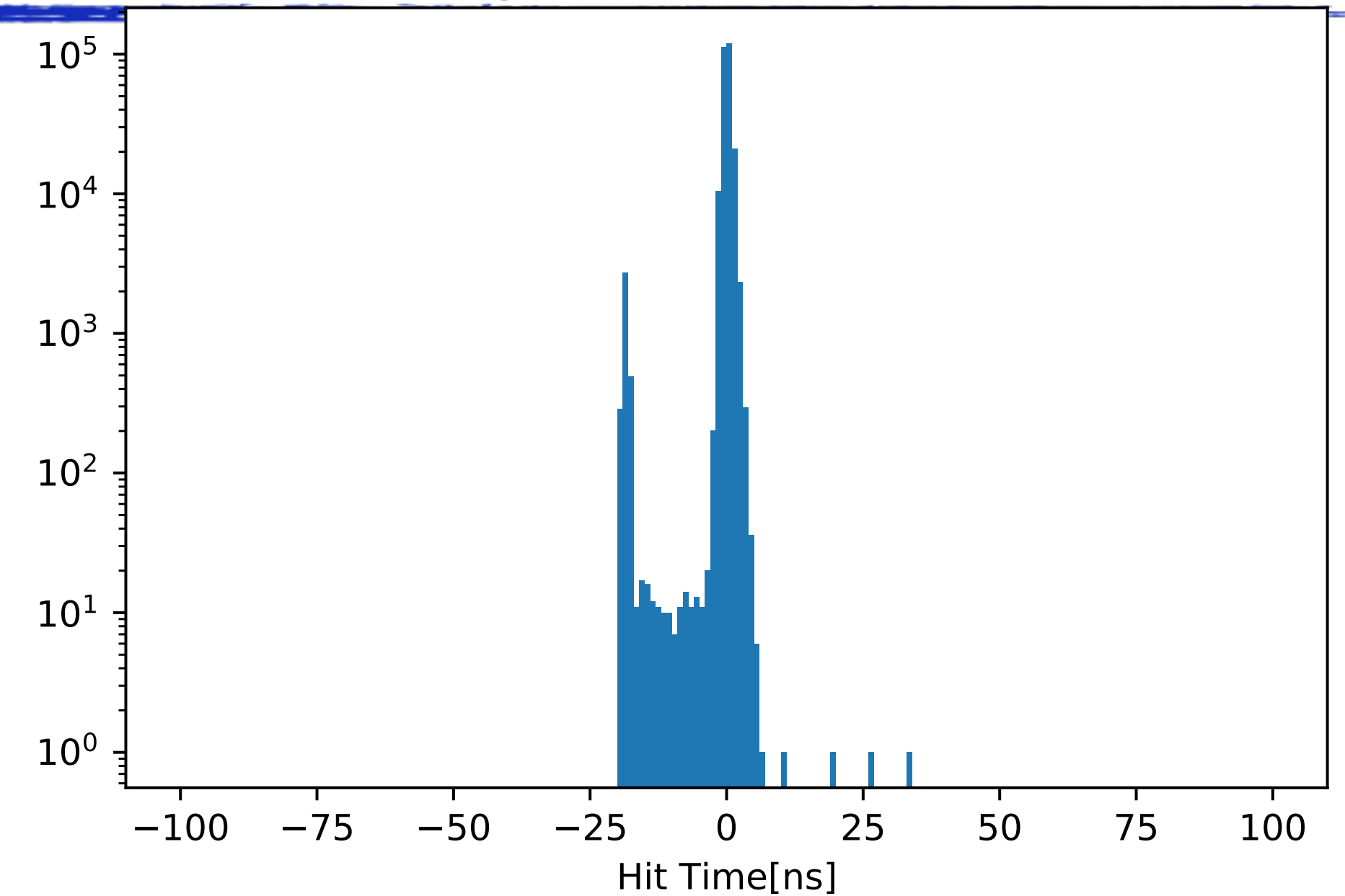


Relative Hit Time in Signal Channels

Channel C



Channel E



Tail to early times from noise or additional particle

Contribution in Channel E at 1180mV

Not yet understood, rejected

