

Abstract: We present a novel implementation of classification using boosted decision trees (BDT) on field programmable gate arrays (FPGA). Two example problems are presented, in the binary classification of electrons vs. photons and in the selection of vector boson fusion-produced Higgs bosons vs. the rejection of the multijet processes. The firmware implementation of binary classification requiring 100 training trees with a maximum depth of 4 using four input variables gives a latency value of about 10ns. Implementations of machine learning algorithms such as BDT will enable the level-1 trigger systems of the Run 4 LHC to be more sensitive to new physics. The work is described in [[2104.03408](https://arxiv.org/abs/2104.03408)].

Introduction

- ATLAS and CMS have FPGA based L1 trigger systems reduce rate: $40MHz \rightarrow 100 kHz$, latency of μ s [1,2]
- Lots of effort to evolve "simple" algorithms, e.g., di-jet mass to ML algorithms: BDT, NN [3,4]
- fwX: implement a new set of ML tools to prepare a BDT for firmware implementation
- The fwX workflow shown in Fig. 1 starts by training a BDT using software, such as TMVA

Figure 1: The work flow for training a BDT using external software, which is then passed to the nanosecond optimization component of fwX in preparation for firmware implementation

with boost weight w_β

- Fixed bit precision is used to represent input variables
- The tree is first "flattened" as shown in Fig. 2
- Bin engines are used to identify the appropriate weight
- BSBE: Bin engine by bit shift, LUBE: Bin engine by look-up table Resource utilization
- Evaluate resource cost vs: N_{var} , tree depth, bin engine, and N_{bits}
- Problem: e/γ separation, using public dataset provided in [5]
- **Latency**: as low as 10ns
- **Resource** (LUT, FF, DSP, BRAM) costs can be a few % and scale with the number of bins in the BDT as shown in Fig. $3 \& 4$

Unit norm.

VBF Higgs trigger: a sample physics case

Dataset & training

- Unit norm. • Goal: VBF Higgs vs. multi-jet bkg
- Signal: VBF H→inv. (POWHEG)
- Background: Multijet (Pythia)
- Test: VBF H→aa→4b
- Detector smearing & pileup done using DELPHES [6], with $\mu = 50$.

Fig 5: input variables for BDT

Fig 6: BDT distribution for signal and background. Test sample with hadronic b-jets shows that the training is robust for a variety of Higgs decay modes

Results

1 10 10^{2} 10^{3} $10⁴$ 10^{5} 10^{6} FF usage

cα,ii

10% of total 1% 0.1% 0.01%

 $O_{\alpha 00}$

cα,i

 $O_{\alpha 10}$

cβ,ii

Oα01 Oα11

 x_b x_b

Flattened tree τα with boost weight wα

• Table 1: after optimization of BDT, few % cost and latency

Bin Engine type LUBE BSBE Notes Benchmark pt Use: xcvu9p flga2104-2L-e

[₩]Х.

cβ,i

 x_a x_a

 $O_{\beta 10}$

Oβ11

Oβ00

Oβ01

• BDT can improve cut based performance by 2x (Fig. 7)

 10^2 10^3 10^4 10^5 10^6 10^7 10^8

Fig 3

• Equivalent performance for firmware vs. software BDT (Fig. 7)

 N_{bin}

BC

0.1% of total

cα,ii

cβ,ii

xb

Merged tree ταβ ⁼ Flattened tree τβ

 10^2 10^3 10^4 10^5 10^6 10^7 10^8

cα,i

Fig 4

cβ,i

w_α O_{α00} + w_β O_{β00}

wα Οα01 + w_β O_{β00} w_α O_{α01+} w_β O_{β01}

 N_{bin}

Bin Engine type LUBE BSBE Notes Benchmark pt Use: xcvu9p flga2104-2L-e

'Y.

w_α O_{α01} + w_β O_{β11} w_α O_{α11} + w_β O_{β11} w_α O_{α01 +} w_β O_{β00} w_α O_{α11} + w_β O_{β10} w_α O_{α00} + w_β O_{β10} w_α O_{α10} + w_β O_{β10}

Fig 2

xa

References

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