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VMM3 - Progress in the development

Monday 8 February 2021 11:30 (30 minutes)

This presentation will outline the latest developments and findings with VMM3a. It reports on several activities carried out within the RD51 collaboration using the general Scalable Readout System. After the ASIC has been integrated within the last years, the first commercial production of joint orders by many teams was just completed. In several projects, the system was improved, a QA system was developed and properties of the chip like noise, time and energy resolution were studied.

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