

Measurement results for the AARDVARC: A High Performance Waveform Digitizer System-on-Chip

Thursday, May 27, 2021 5:12 AM (18 minutes)

Data acquisition systems for state of the art imaging based particle identification detectors are expected to handle large numbers of channels, high accuracy timing, and operate under limited spatial and power constraints. In many applications, full waveform digitization is considered necessary to guarantee the required timing resolution and avoiding the undesirable degradation due to time walk, pile-up and other sources of noise. Such acquisition systems are even more problematic in that the data volume and the computational requirements push the power, cost and space limits even further.

Based on such requirements a new multi-channel Waveform digitizer the AARDVARC has been designed and fabricated. This 4 channel front-end chip operates at 13GSa/s and has 32k of sampling depth per channel. In this summary, measurements of analog and digital performance of the asic will be reported. The device has been fabricated in 130 nm process.

TIPP2020 abstract resubmission?

No, this is an entirely new submission.

Funding information

U.S. DOE SBIR DE-SC0017833 grant

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Session Classification: Posters: Front-end electronics

Track Classification: Readout and Data Processing: Readout: Front-end electronics