

Session Program

May 24 - 28, 2021

TIPP 2021

Readout: Trigger and DAQ

Tue, May 25

7:30 AM

Readout: Trigger and DAQ: Tuesday Middle

Session | **Location:** | **Conveners:** Carlo A. Gottardo, Carlo Alberto Gottardo, Pierre-Andre Amaudruz

07:30 - 07:48

Concept of KamLAND2 DAQ system

Speaker

Sei Ieki

07:48 - 08:06

Trigger-DAQ and slow control systems in the Mu2e experiment

Speaker

Antonio Gioiosa

08:06 - 08:24

Real-Time Signal Processing for Large-Volume Cyclotron Radiation Emission Spectroscopy

Speaker

Noah Oblath

08:24 - 08:42

Electronics for Multi-PMTs for the IWCD at Hyper-Kamiokande

Speaker

Marcin Ziembicki

08:42 - 09:00

Development of a dedicated DAQ system for Crystal Eye pathfinder

Speaker

Luigi Ferrentino

9:00 AM

Wed, May 26

7:30 AM

Readout: Trigger and DAQ: Wednesday Middle

Session | **Location:** | **Conveners:** Nico Giangiacomi, Nikolina Ilic, Nico Giangiacomi

07:30 - 07:48

System Design and Prototyping for the CMS Level-1 Trigger at the High-Luminosity LHC

Speaker

Dr Alexandre Zabi

07:48 - 08:06

The LHCb Upstream Tracker data flow architecture

Speaker

Carlos Abellan Beteta

08:06 - 08:24

Performance of the readout system of the ALICE ZDC calorimeters in LHC Run 3

Speaker

Pietro Cortese

08:24 - 08:42

Triggering on charged particles in collider experiments

Speaker

Ashutosh Kotwal

08:42 - 09:00

Recent experience with Streaming Readout for CLAS12-Forward Tagger

Speaker

Mariangela Bondi

9:00 AM

Thu, May 27

9:30 AM

Readout: Trigger and DAQ: Thursday Late

Session | **Location:** | **Conveners:** Mark Stringer, David Abbott

09:30 - 09:48 **INO's RPC-DAQ module: Performance review and upgrade plans**

Speaker

Mandar Saraf

09:48 - 10:06 **Hog (HDL on git): a collaborative HDL management tool**

Speaker

Francesco Gonnella

10:06 - 10:24

Design of a Robust Fiber Optic Communications System for Future Giga-scale Neutrino Detectors

Speaker

Robert Halliday

10:24 - 10:42 **Muon trigger with fast Neural Networks on FPGA, a demonstrator**

Speaker

Matteo Migliorini

10:42 - 11:00

Readout system and testbeam results of the RD50 MPW2 HV-CMOS pixel chip

Speaker

Patrick Sieberer

11:00 AM