

TIPP 2021

Thursday, May 27, 2021

Posters: Trigger and DAQ: Thursday Early (5:00 AM - 5:30 AM)

[id] title	presenter	board
[689] mPSD data monitoring at mCBM experiment	KARPUSHKIN, Nikolay	
[283] ATLAS LAr Calorimeter Commissioning for LHC Run-3	FORTIN, Etienne Marie	
[303] Development of new high speed data acquisition system prototype for SOI pixel detector using 10 Gb Ethernet SiTCP	Dr NISHIMURA, Ryutaro	
[323] Machine Learning for Real-Time Processing of ATLAS Liquid Argon Calorimeter Signals with FPGAs	FRITZSCHE, Nick	
[332] FELIX: commissioning the new detector interface for the ATLAS trigger and DAQ system	XU, Hao	
[335] Design of a resilient, high-throughput, persistent storage system for the ATLAS Phase-II DAQ system	ABED ABUD, Adam	
[339] Upgrade of the ATLAS Level-1 Endcap Muon Trigger System for LHC Run-3	TSUJIKAWA, Yoshiaki	
[340] Upgrade of the ATLAS Level-0 Endcap Muon Trigger for HL-LHC	KOBAYASHI, Ren	
[363] Performance and Integration results of a high resolution Time to Digital Converter designed for INO ICAL Experiment	Mr ELANGO VAN, Yuvaraj	
[387] Firmware development for trigger-less mPSD readout at mCBM experiment at GSI	FINOGEEV, Dmitry	
[404] The Event Timing Finder for the Central Drift Chamber Level-1 Trigger at the Belle II experiment	SUE, Yuki	
[419] The ATLAS Level-1 Topological Processor: experience and upgrade plans	SONAY, Anil	
[434] The Particle Flow Algorithm in the Phase II Upgrade of the CMS Level-1 Trigger.	PARK, Sang Eon	
[437] Level 1 muon triggers algorithms for the CMS upgrade at the HL-LHC	ERICE CID, Carlos Francisco	
[447] CMS Hadron Calorimeter Pulse Filter for Pileup Mitigation at the Level-1 Trigger	HILTBRAND, Joshua	
[463] Application and further improvement of the RD51 general Scalable Readout System with the VMM chip	LUPBERGER, Michael	
[464] ATLAS toward the High Luminosity era: challenges on electronic systems	GONSKI, Julia Lynne	
[470] System-level performance study and commissioning of TGC frontend electronics for Phase-2 upgrade of LHC-ATLAS	TANAKA, Aoto	
[472] The upgraded electronics of the Pierre Auger surface detector	Dr CONVENGA, Fabio	
[477] Electronics Performance of the ATLAS New Small Wheel Micromegas wedges at CERN	PERGANTI, Maria	
[494] A muon tracking algorithm for the Level 1 trigger in the CMS barrel muon chambers during HL-LHC	CELA RUIZ, Jose Manuel	

[505] Development of FPGA based 128-Channel TDC for Time Projection Chambers	Mr JOHN, Jim	
[520] The HL-LHC Upgrade of the ATLAS Tile Hadronic Calorimeter	SILVERSTEIN, Samuel	
[522] A new data transfer scheme for HL-LHC upgrade of the ATLAS Tile Hadronic Calorimeter	CERVELLO DUATO, Antonio	
[547] The DAQ and control system for Jadedpix3	DONG, Sheng	
[558] The Monitoring of Pixel System (MOPS) chip for the Detector Control System of the ATLAS ITk Pixel Detector	AHMAD, Rizwan	
[561] The Opto-electrical conversion system for the data transmission chain of the ATLAS ITk Pixel detector upgrade for the HL-LHC	FRANCONI, Laura	
[567] Development of the Level-1 trigger system in BelleII experiment	KOGA, Taichiro	
[578] Monitoring System of the ATLAS ITk Laboratory	SYKORA, Martin	
[590] CALICE SiW ECAL Development and performance of a highly compact digital readout system	Mrs MAALMI, Jihane	
[619] CMS RPC Link System upgrade	BOGHRATI, Behzad	
[632] PMT Base with Integrated Waveform Capture for IceCube-Gen2	WENDT, CHRISTOPHER	
[678] ECAL trigger performance in Run 2 and improvements for Run 3	TISHELMAN CHARNY, Abraham	