**16<sup>th</sup> (Virtual) "Trento" Workshop** Tuesday, 16<sup>th</sup> February 2021

# E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures

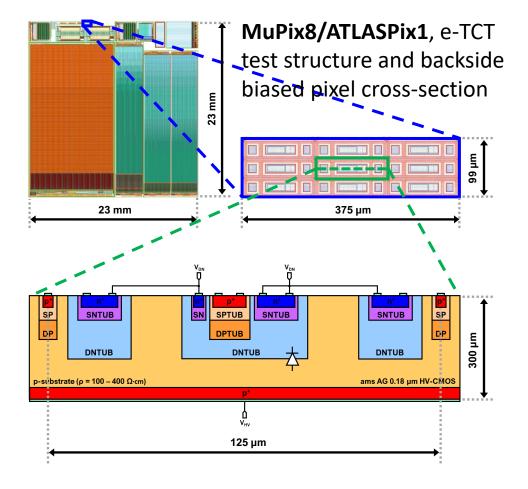
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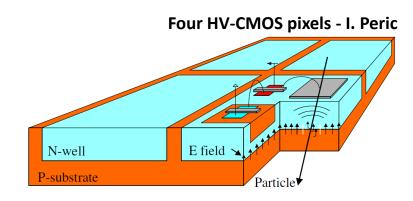
Introduction	E-TCT	W1 (50–100 Ω·cm)	W2 (100–400 Ω·cm)	Summary
Outline				

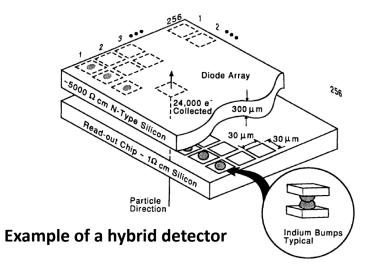
- Introduction
  - MuPix8/ATLASPix1 chip
  - Two sample types
  - I-V measurements
  - E-TCT setup and technique
- Wafer 1 (topside biased)
  - E-TCT measurements
- Wafer 2 (backside biased)
  - E-TCT measurements
- Comparison of effective doping conc. with fluence
- Summary





Introduction	E-TCT	W1 (50–100 Ω·cm)	W2 (100–400 Ω·cm)	Summary
Introduction				





Why use HV-CMOS in particle physics experiments?

- Need for low-cost, large area detectors
  - Produced in commercially available CMOS technologies
- Need for lower material budget
  - Read-out electronics embedded in sensing chip
  - No bump-bonding required (hybrids)
- Need for high radiation tolerance
  - HV-CMOS can be biased to high voltage for fast charge collection via drift



E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures

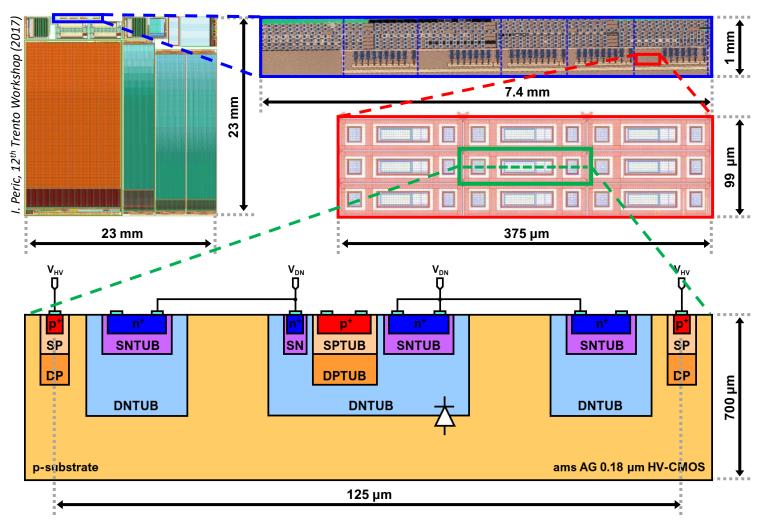
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E-TCT

W1 (50–100 Ω·cm)

#### Summary

#### MuPix8/ATLASPix1



- General design features
  - Engineering run in ams AG 180 nm HV-CMOS technology
  - Fabricated in multiple substrate resistivities 20 Ω·cm, 50–100 Ω·cm, 100–400 Ω·cm
  - Shared submission between Mu3e and ATLAS experiments
- E-TCT test structure
  - □ 3 × 3 matrix
  - Passive pixels
  - $\Box$  Pixels are 125  $\mu$ m  $\times$  33  $\mu$ m

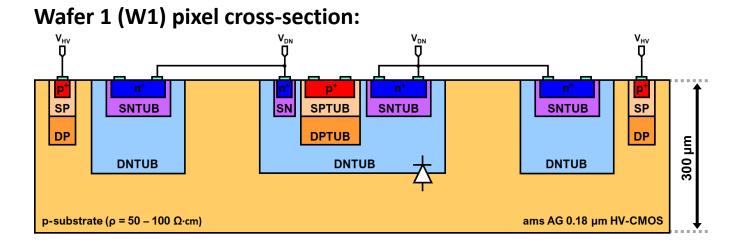
(top-left) MuPix8/ATLASPix1 shared submission (top-right) Liverpool contribution (middle-right) 3×3 test structure (bottom) Cross-section diagram of central pixel (not to scale)

E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures



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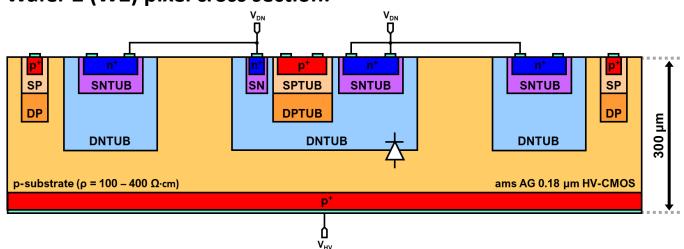
#### MuPix8/ATLASPix1 two sample types



#### 50–100 Ω·cm substrate resistivity

- Topside biased
- Thinned to 300 μm
- Measured fluences  $1e14 < \Phi_{eq} < 1e16 n_{eq} \cdot cm^{-2}$

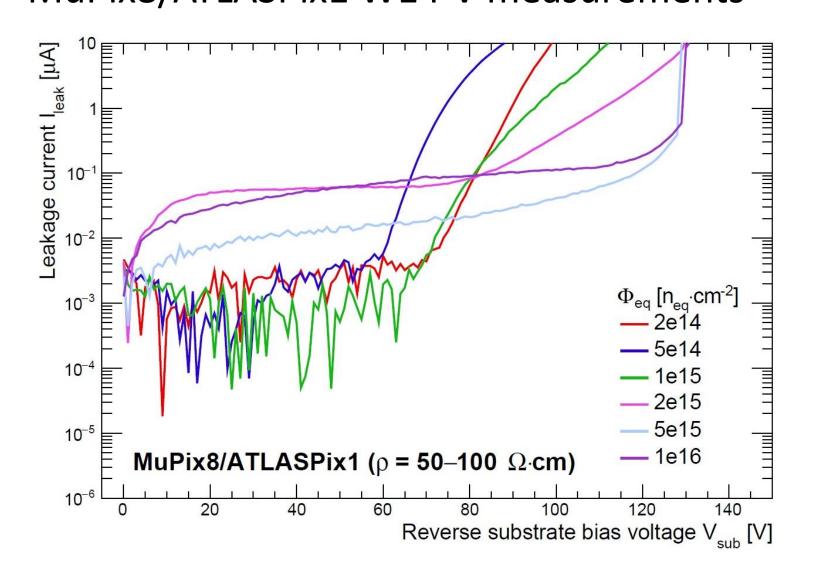
#### Wafer 2 (W2) pixel cross section:



- 100–400 Ω·cm substrate resistivity
- Wafer thinning at OPTIM (300 μm)
- Backside processing at IBS
  - p+ implantation
  - backside metalisation
- Measured fluences  $0 < \Phi_{eq} < 1e16 n_{eq} \cdot cm^{-2}$



Introduction	E-TCT	W1 (50–100 Ω·cm)	W2 (100–400 Ω·cm)	Summary
MuPix8/ATL	ASPix1 W1 I-V	measurements		



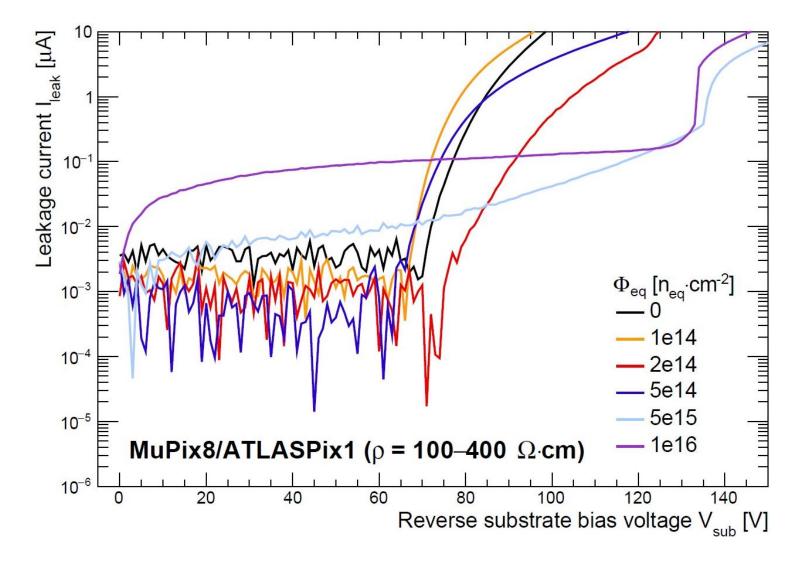
- Samples irradiated with neutrons at TRIGA reactor (Jožef Stefan Institute)
  □ 1.10<sup>14</sup> → 1.10<sup>16</sup> n<sub>eq</sub>·cm<sup>-2</sup>
- Leakage current increases with fluence (some variation)
- Breakdown voltage increased for higher fluence samples

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#### MuPix8/ATLASPix1 W2 I-V measurements

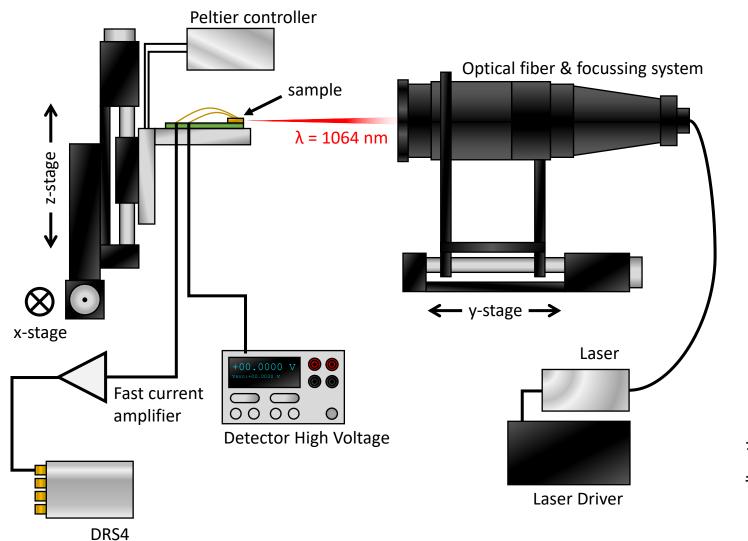


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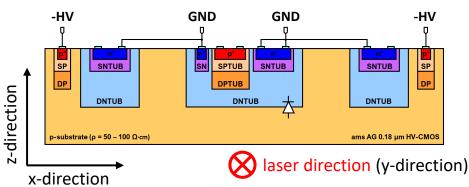




#### E-TCT experimental setup



- Particulars Scanning TCT system
- Measurement parameters
  - Collimated, pulsed, infrared laser
  - Beam diameter  $\approx 10 \ \mu m$
- Sample connection scheme
  - Sensing DNTUB = GND
  - P-type substrate = -HV



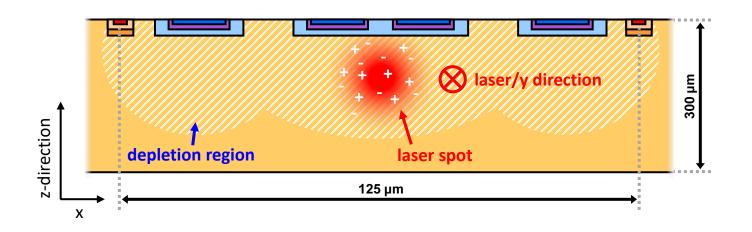


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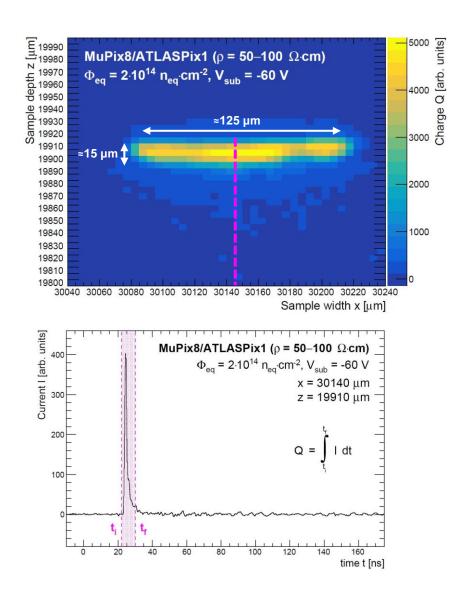
Introduction E-TCT W1 (50–100 Ω·cm)

W2 (100–400 Ω·cm)

#### Edge-TCT measurements x focus finding



- Fixed parameters
  - Substrate bias voltage V<sub>sub</sub>
  - Optics to sample distance y
- Step size 5 μm in x and z
- Waveform recorded and integrated at each position
- Centre of the pixel  $x_0 \approx 30145 \ \mu m$

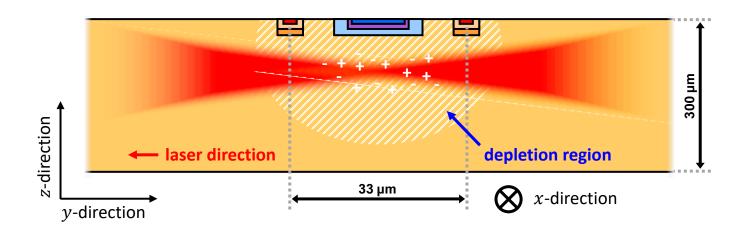




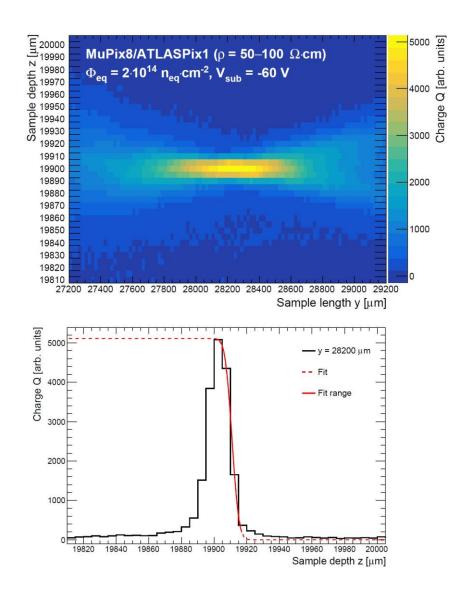
Introduction

E-TCT

#### Knife-edge measurement - y focus finding

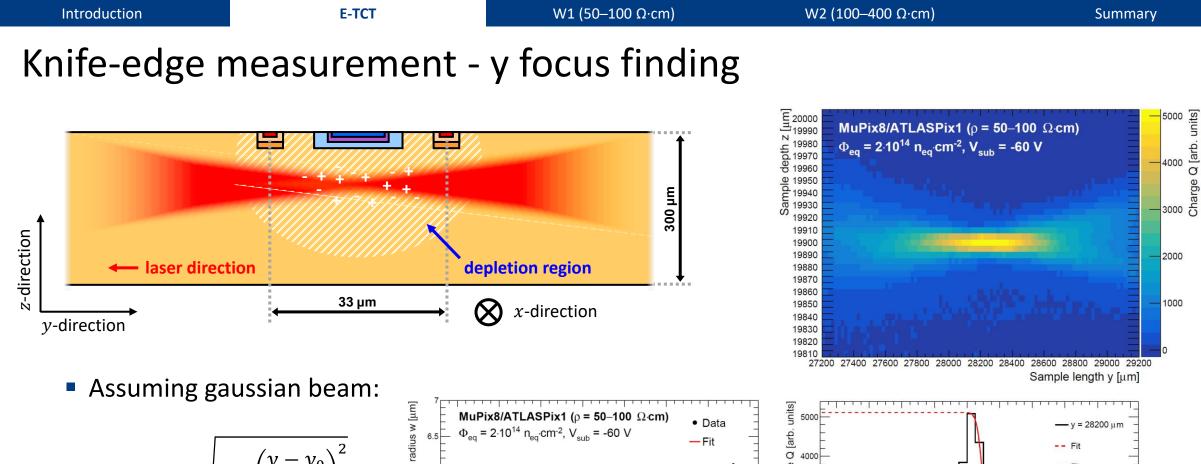


- Fixed parameters
  - Substrate bias voltage V<sub>sub</sub>
  - Sample width x
- Step size 5 μm in z, 20 μm in y
- Waveform recorded and integrated at each position
- Error function fit to each y

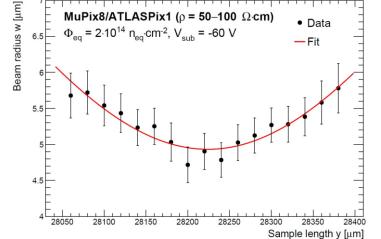


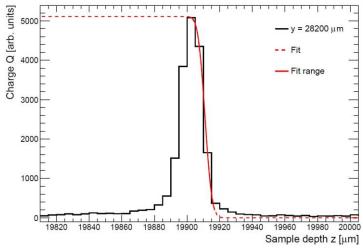


E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures



- $w(y) = w_0 \sqrt{1 + \left(\frac{y y_0}{y_R}\right)^2}$
- $y_0 \approx 28220 \ \mu m \ extracted$ from fit

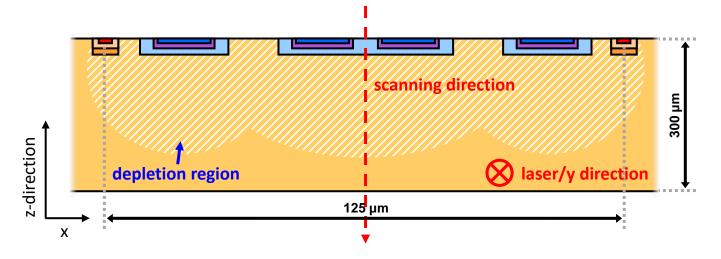




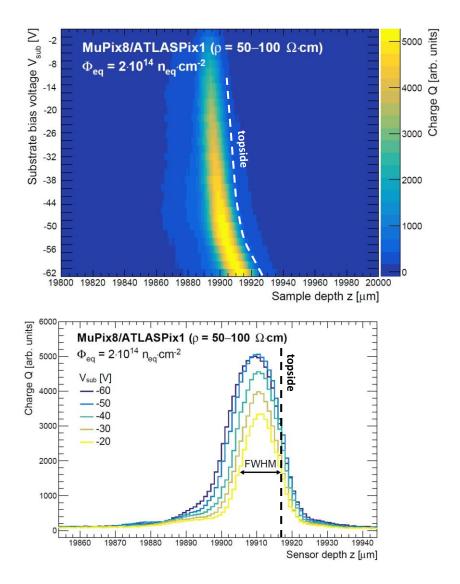


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E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures



- Fixed parameters
  - Sample width *x*
  - Optics to sample distance y
- Step size 1  $\mu$ m in *z*, 2 V in *V*<sub>sub</sub>
- Waveform recorded and integrated at each position
- Depletion depth  $W_D$  estimated from FWHM of charge collection profiles





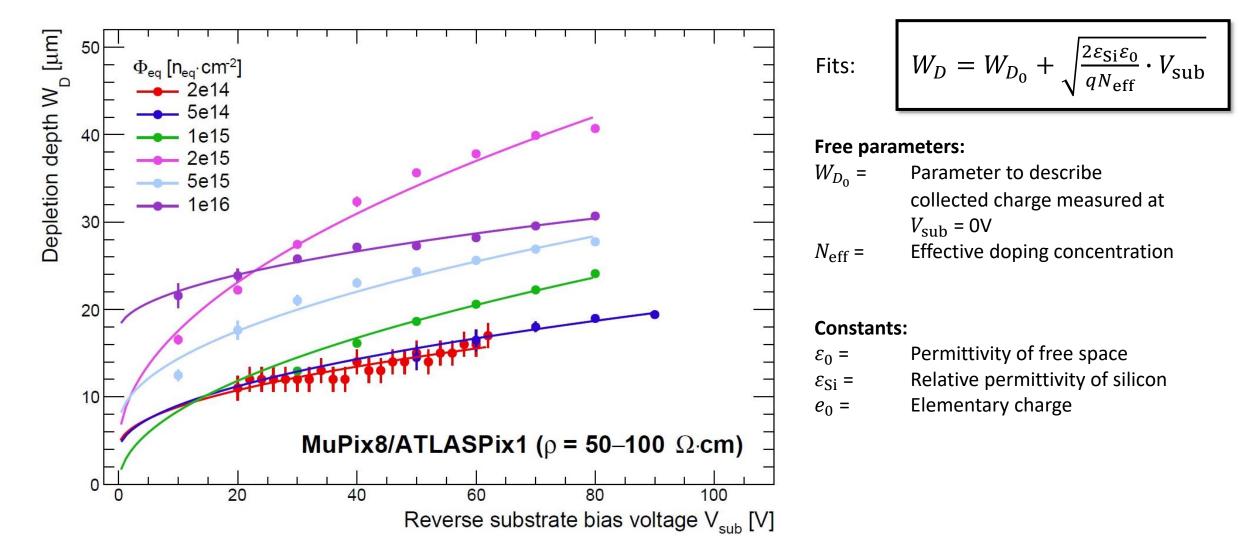
Summary

Introduction

E-TCT

W1 (50–100 Ω·cm)

### MuPix8/ATLASPix1 W1 W<sub>D</sub>(V<sub>sub</sub>) measurements





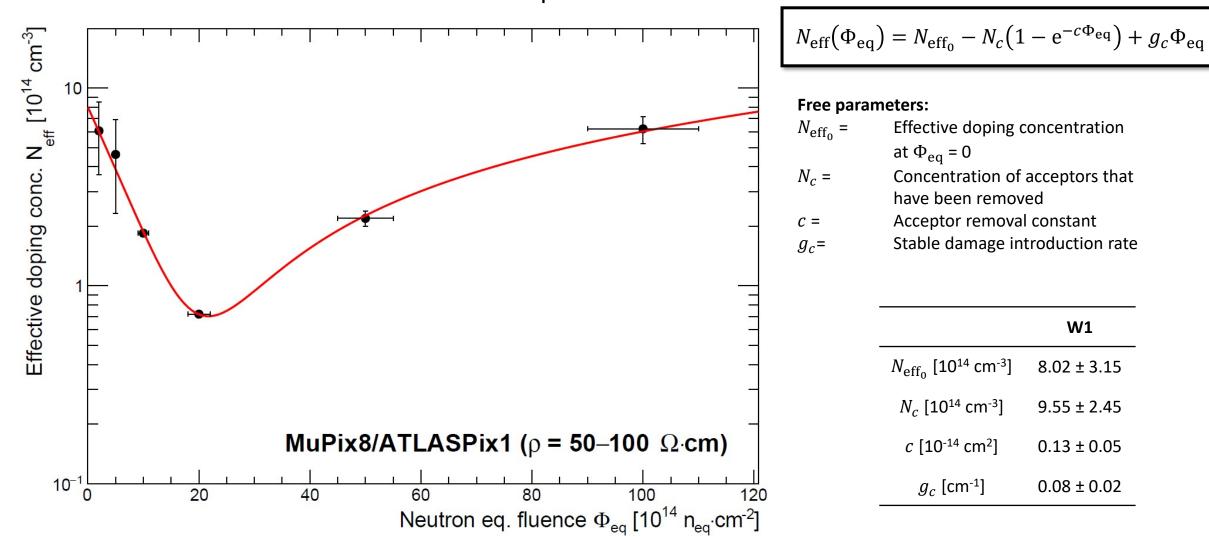


Introduction

E-T<u>CT</u>

W1 (50–100 Ω·cm)

## MuPix8/ATLASPix1 W1 N<sub>eff</sub>( $\Phi_{eq}$ )

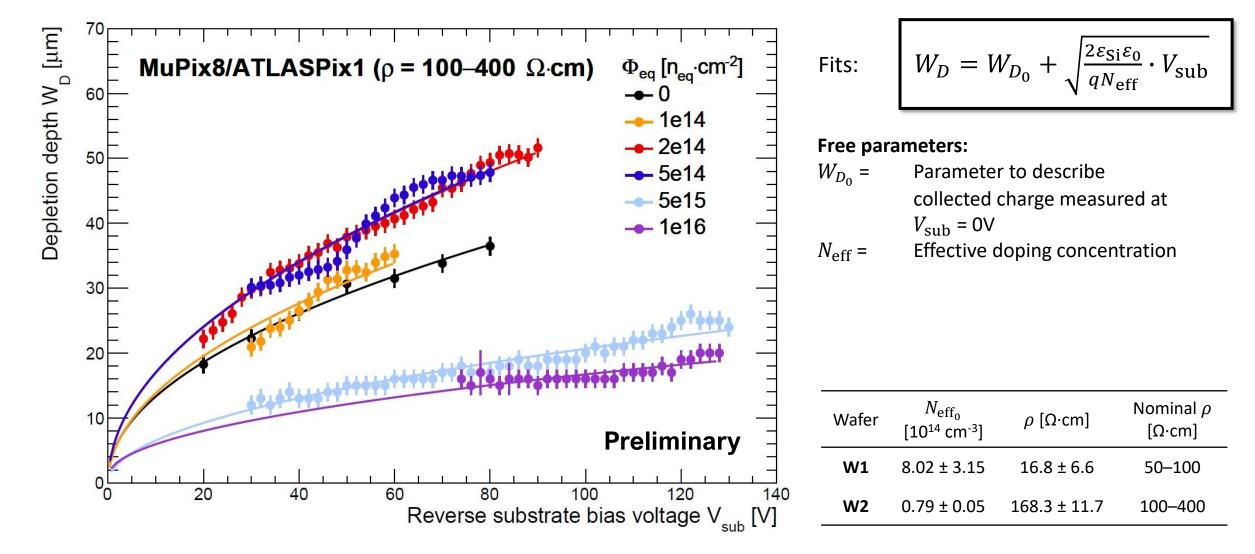




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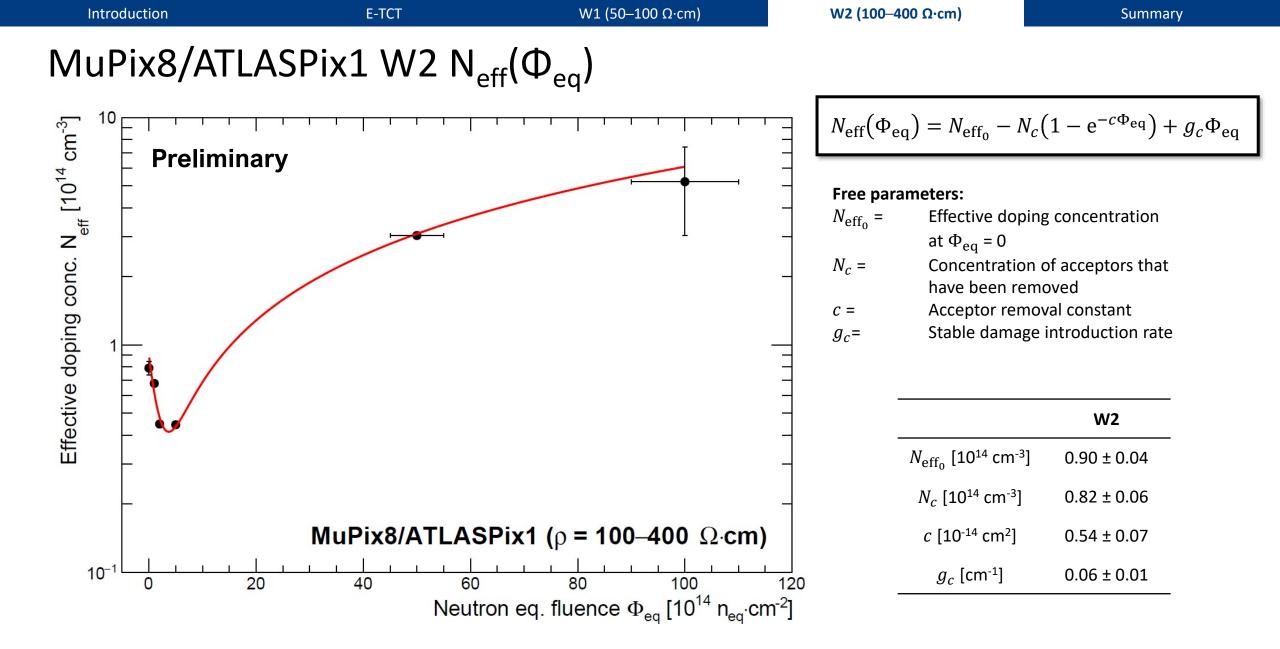
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## MuPix8/ATLASPix1 W2 W<sub>D</sub>(V<sub>sub</sub>)





LIVERPOOL



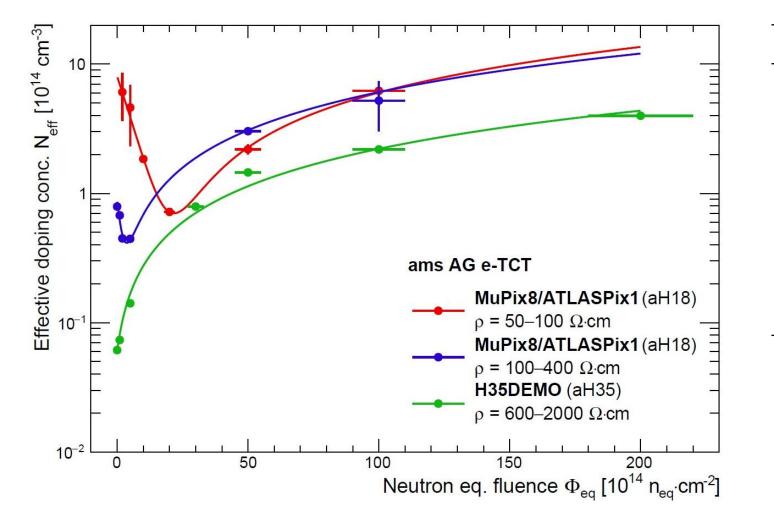
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#### MuPix8/ATLASPix1 e-TCT measurements



	W1	W2	H35DEMO
Nominal ρ [Ω·cm]	50—100	100—400	600—2000
ρ [Ω·cm]	16.8 ± 6.6	168.3 ± 11.7	2212 ± 35
N <sub>effo</sub> [10 <sup>14</sup> cm <sup>-3</sup> ]	8.02 ± 3.15	0.90 ± 0.04	0.060 ± 0.001
<i>N<sub>c</sub></i> [10 <sup>14</sup> cm <sup>-3</sup> ]	9.55 ± 2.45	0.82 ± 0.06	-
<i>С</i> [10 <sup>-14</sup> сm <sup>2</sup> ]	0.13 ± 0.05	0.54 ± 0.07	-
$g_c$ [cm <sup>-1</sup> ]	0.08 ± 0.02	0.06 ± 0.01	0.022 ± 0.001

E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test structures



E-TCT characterisation of neutron irradiated 180 nm HV-CMOS pixel test struc	tures
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#### Summary

Introduction

Aim: Study the radiation tolerance of ams AG aH18 technology

MuPix8/ATLASPix1 W1 (50–100 Ω·cm), topside biased

E-TCT

- □ Irradiated with neutrons at TRIGA (1e14 <  $\Phi$ eq < 1e16 n<sub>eq</sub>·cm<sup>-2</sup>)
- I-V & E-TCT measurements
- Calculated resistivity was lower than the nominal values
- MuPix8/ATLASPix1 W2 (100–400 Ω·cm), backside biased
  - Backside processed wafer
  - □ Irradiated with neutrons at TRIGA (0 <  $\Phi eq$  < 1e16 n<sub>eq</sub>·cm<sup>-2</sup>)
  - I-V & E-TCT measurements
  - Calculated resistivity was within tolerances



W2 (100–400 Ω·cm)