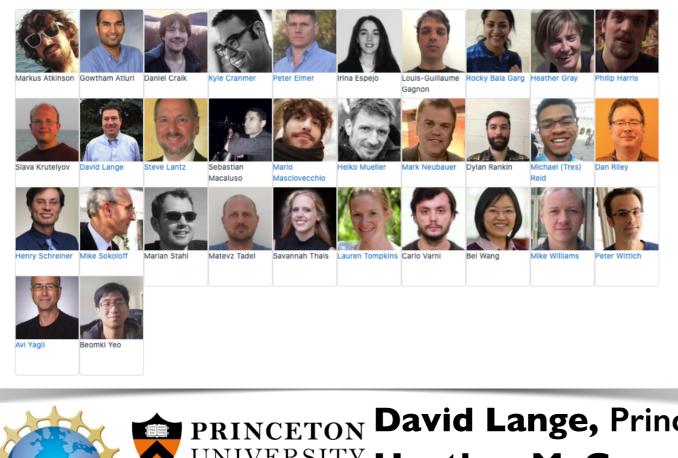
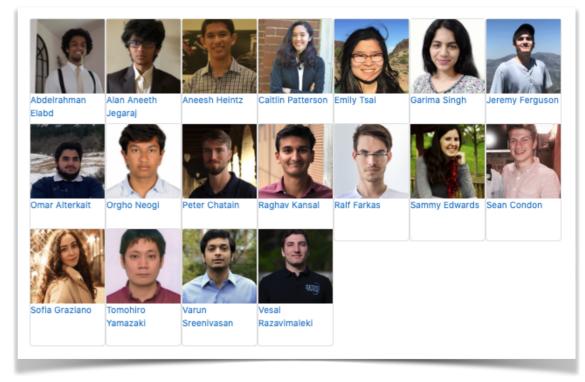


# **Innovative Algorithms**

IA Team



**Current and Former Fellows** 





http://iris-hep.org



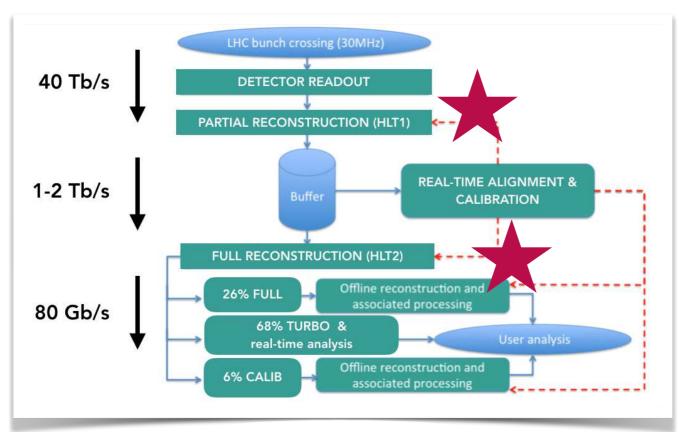
Berkeley

## Overview

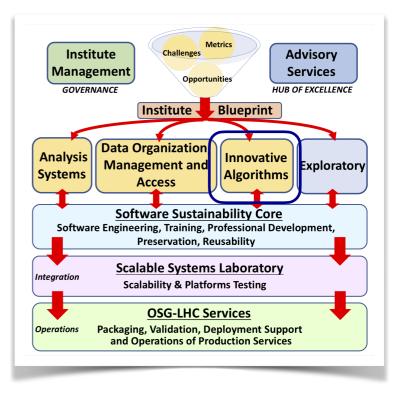
- Innovative Algorithms is one of the three focus areas in IRIS-HEP
- We develop and re-engineer algorithms for some of the most compute-intensive high-energy physics applications

### Software Trigger

Filter events to keep

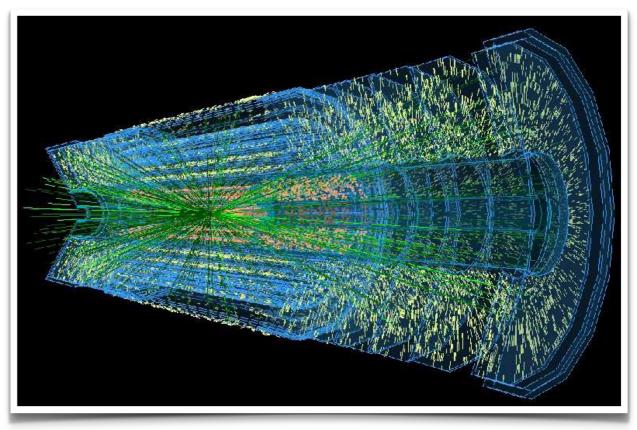


### HLT = High Level Trigger



### **Offline Reconstruction**

Detector output  $\rightarrow$  analysis objects



~100 billion events per year

## Three major themes

### Tracking

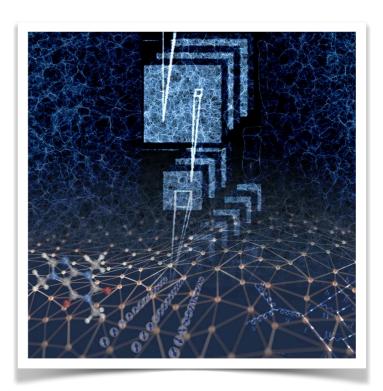
- Requires the largest fraction of CPU during reconstruction
- New algorithms with
  - Better efficiency
  - Reduced computational cost

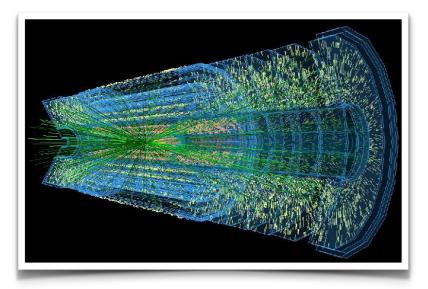
### Accelerators

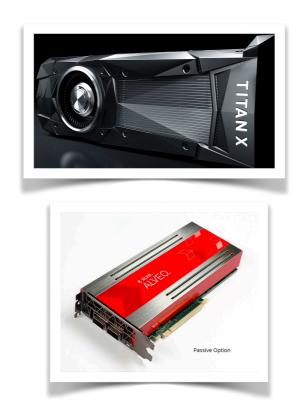
- Rapid growth in terms of power and availability of hardware accelerators
  - Reco applications on accelerators
  - ML on accelerators



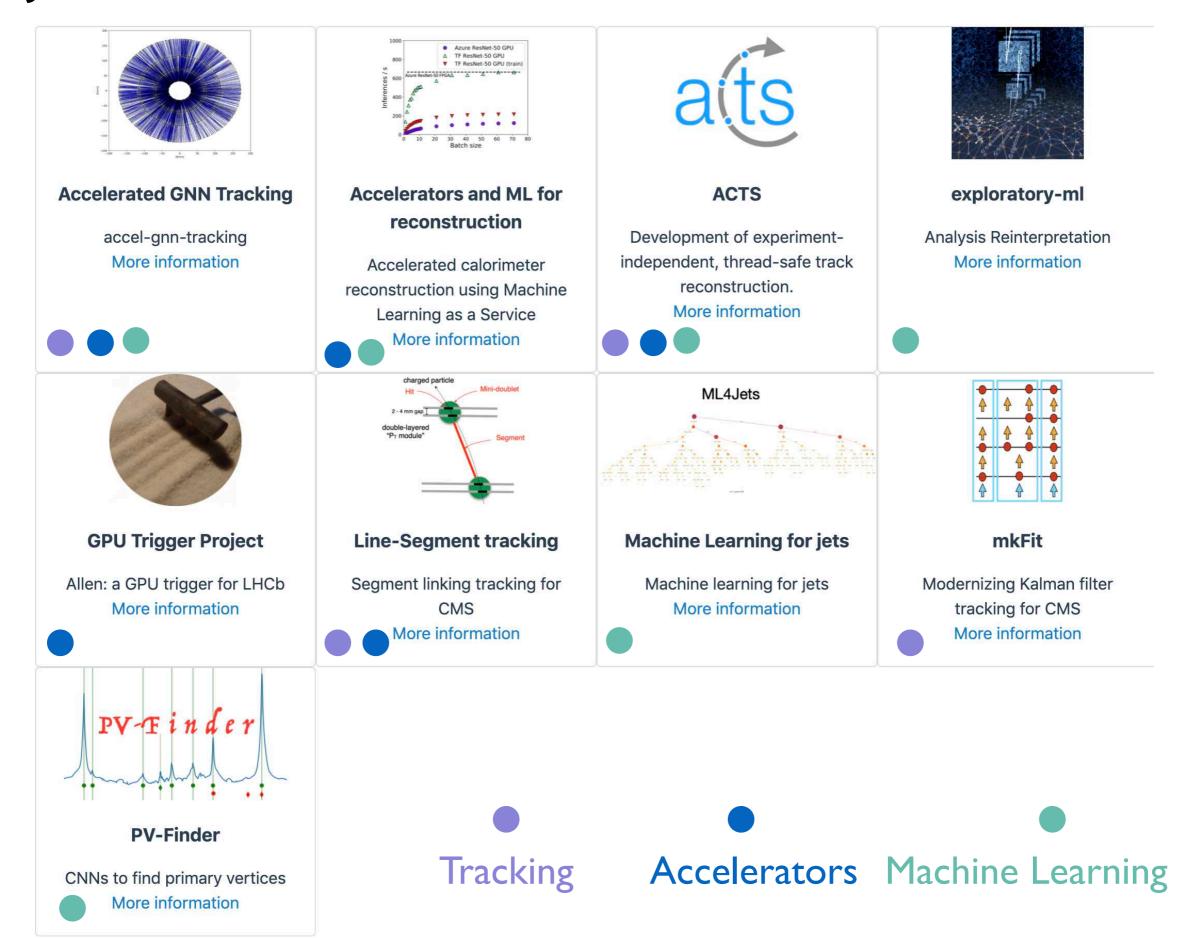
- Exploit major advances in ML
  - Apply new ML techniques to HEP
  - New applications of HEP in ML
  - ML on accelerators





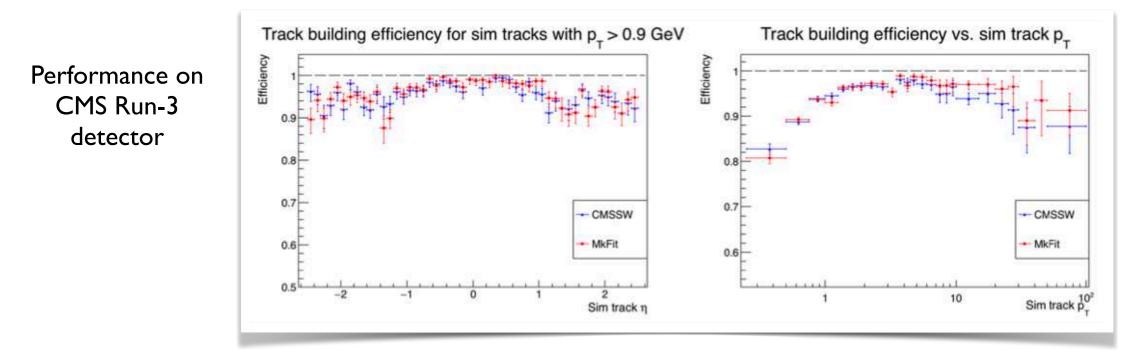


### **Projects and Themes**



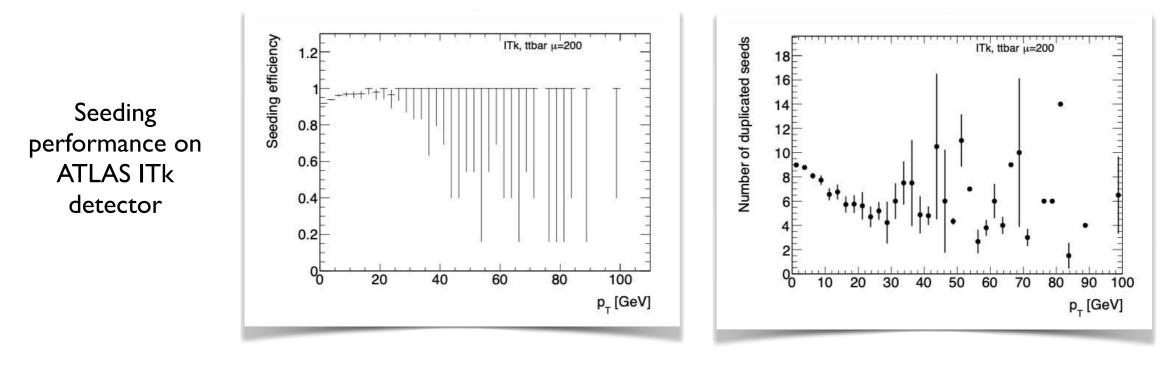
## **Tracking Highlights: Maturing Projects**

**Parallelized** and **vectorized** Kalman Filter tracking



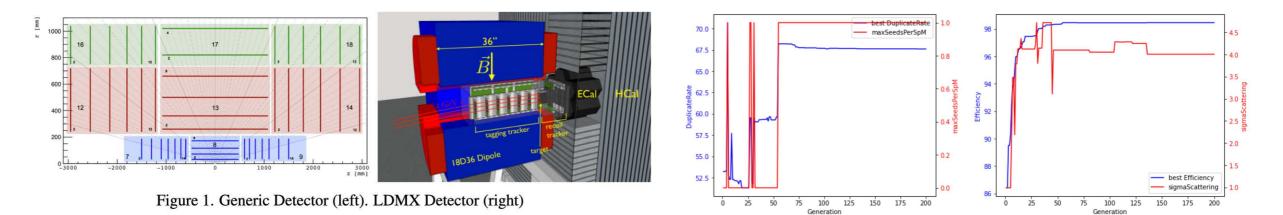
S. Lantz et al, JINST 15 P09030 (2020) (29 May 2020).

#### **Experiment-independent**, thread-safe track reconstruction algorithms

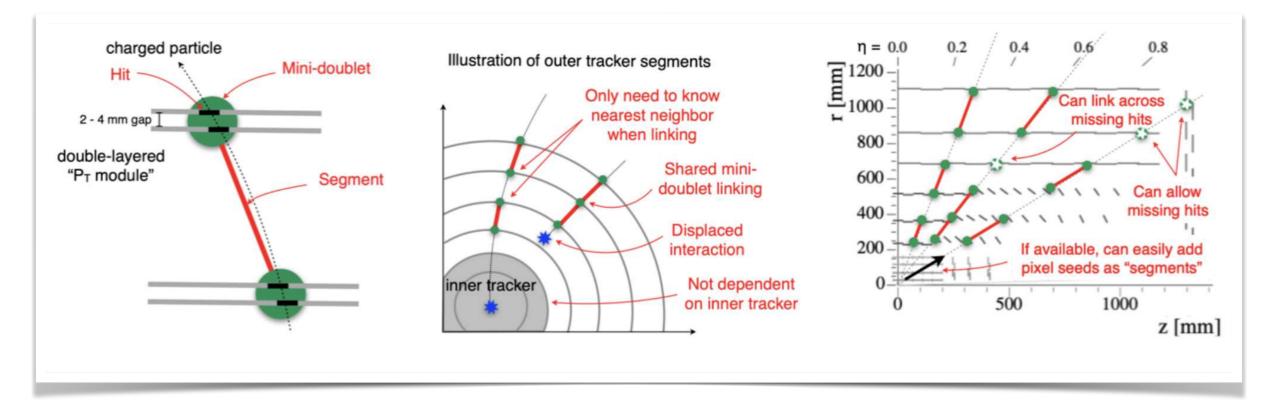


## **Tracking Highlights: Newer projects**

### **ML evolutionary algorithms** to automatically tune algorithm parameters

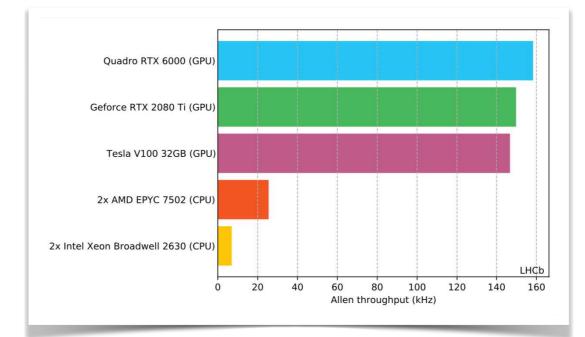


#### **Line-segment** tracking algorithms for parallelization and accelerators



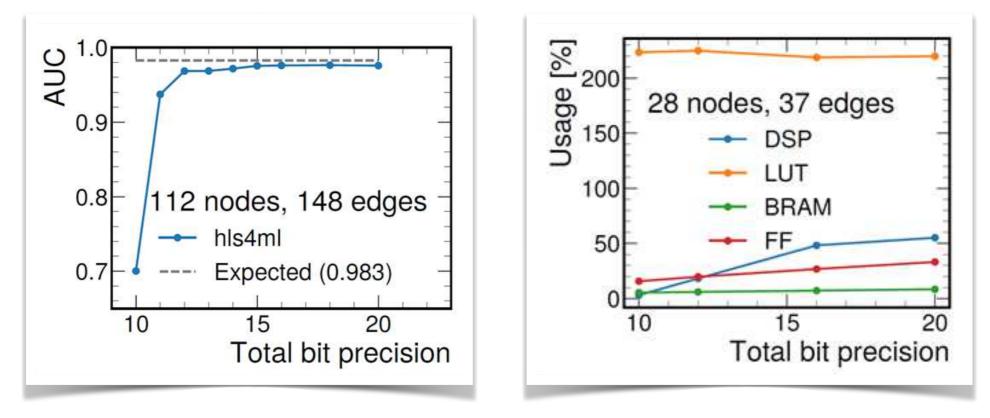
## **Accelerators Highlights**

Full HLT1 trigger reconstruction and selection on GPUs adopted by LHCb in Run-3



Allen: A high level trigger on GPUs for LHCb, R. Aaij et. al., Comput. Softw. Big Sci. <u>4 7 (2020) (19 Dec 2019)</u>

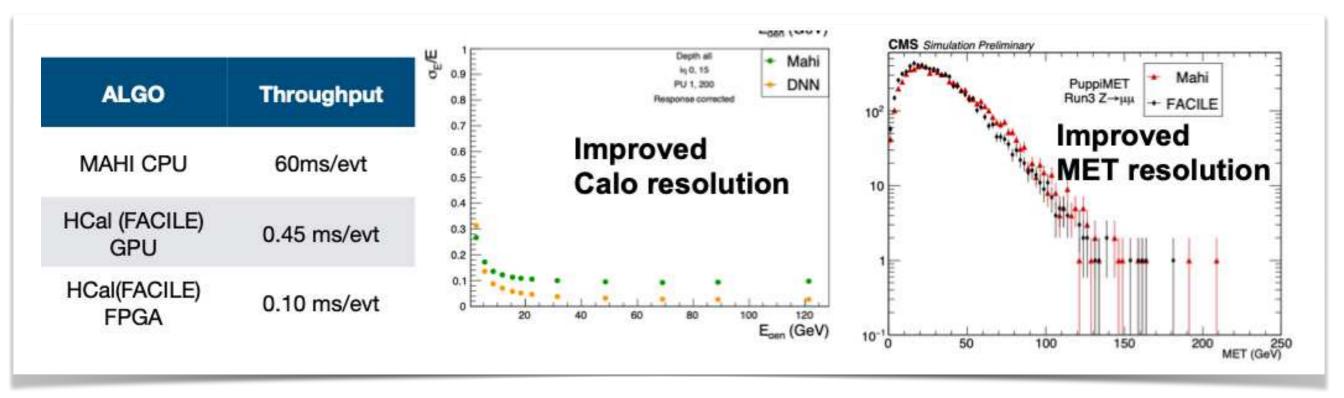
### Graph Neural Network (GNN) tracking algorithms on FPGAs

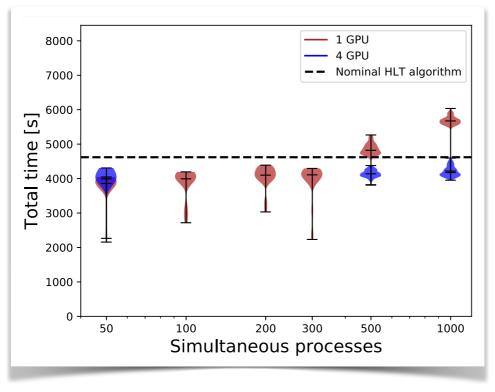


GPU=Graphical Processing Unit, FPGA = Field Programmable Gate Array

## **Accelerators Highlights**

#### CMS hadronic calorimeter with GPU trigger



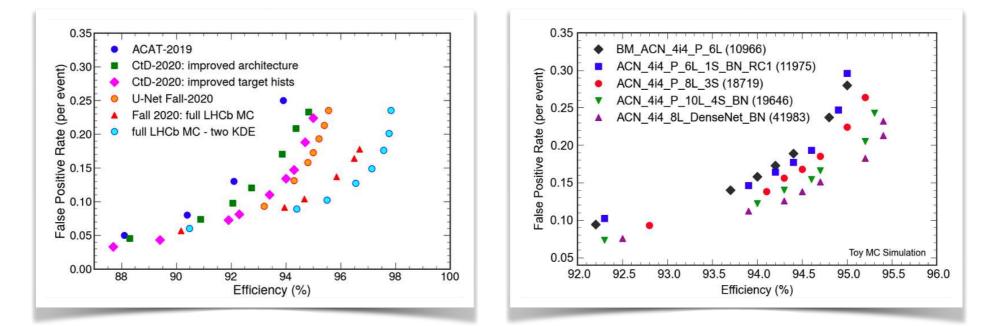


FPGA-accelerated machine learning inference as a service for particle physics computing, J. Duarte et. al., Comput.Softw.Big Sci. 3 13 (2019) (18 Apr 2019)

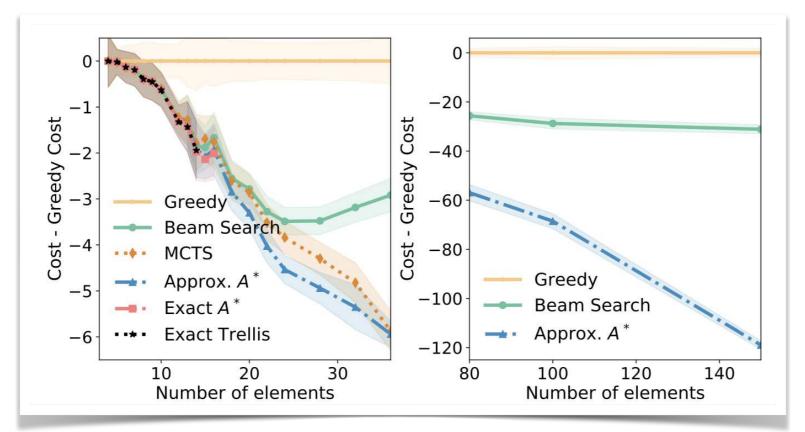
MET=missing energy, HCal = hadronic calorimeter

## **Machine Learning Highlights**

KDEs and DNNs for **primary vertex** reconstruction for LHCb



Probabilistic treatment of hierarchical clustering for jet physics



KDE=Kernel Density Estimator, DNN=Deep Neural Network

## We have lots of IA Fellow projects to follow

#### **Projects finishing up**

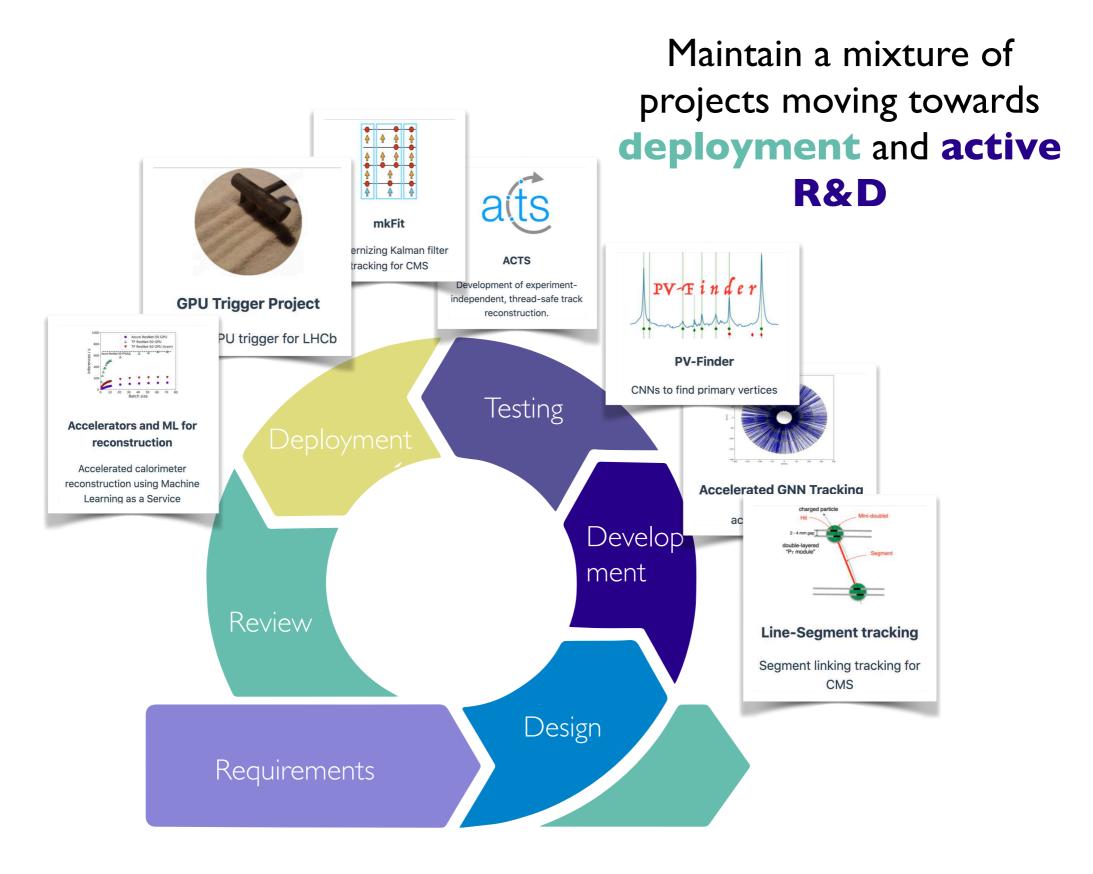
- Creating a Python Front-End for HLS Implementation of GNNs on FPGA (Presentation July 7th)
- Scaling up implementations of GNNs with FPGA co-processors for charged particle track reconstruction (<u>Presentation July 7th</u>)
- Floating Point Error Evaluation With Clad (Presentation June 21st)
- Graph Methods for Particle Tracking (Presentation June 28th)
- Offline Track Selection (Presentation July 7th)
- Machine Learning inference as a Service optimization in neutrino reconstruction
- OpenCL based implementation of graph neural networks on FPGA (<u>Presentation June 28th</u>)

#### **Projects starting up**

- Analyzing Neutrino Interactions
- Developing Symmetric Graph Neural Networks for Charged Particle Tracking
- Graph Methods for Particle Tracking

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### IA Projects in the Software Lifecyle



### Conclusion

- Significant progress in **Innovative Algorithms** over the past year
- Many exciting **results**, many interesting **papers** and **talks**
- Active fellows program is enabling us to broaden participation
- A number of projects are maturing and moving towards deployment
  - e.g. Allen, MkFit, HLS4ML, ACTS
- Other new opportunities for R&D are just getting started
  - e.g. Line-Segment Tracking, ACTS on GPUs

