



HighRR seminar
21.04.2021



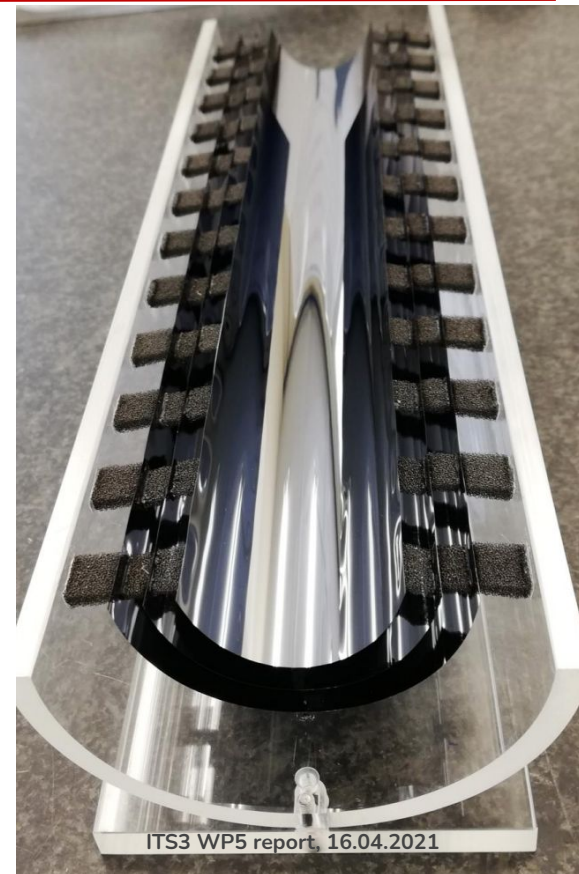
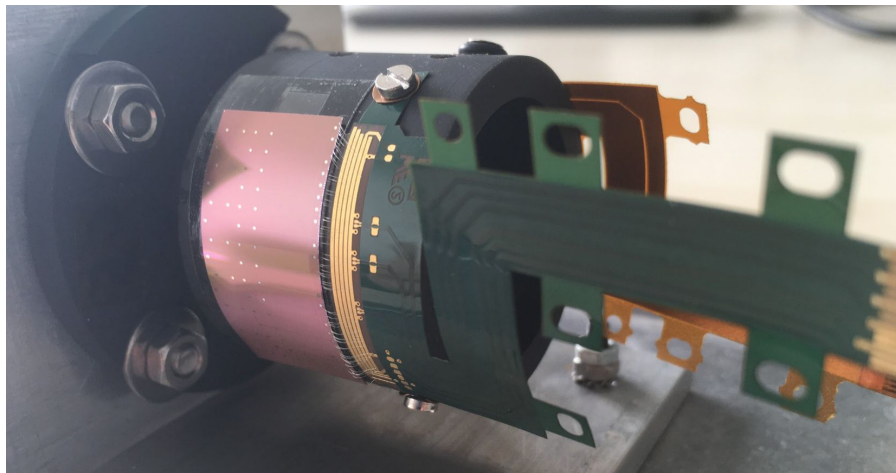
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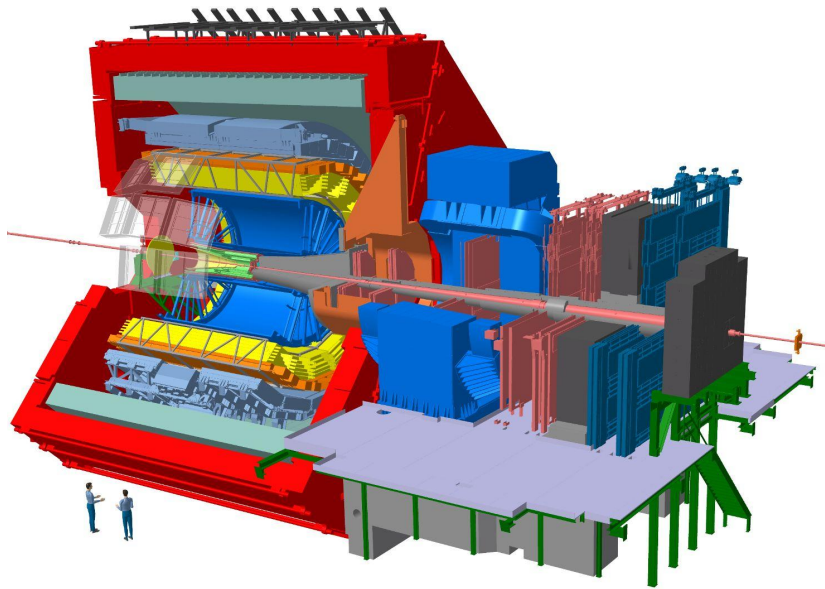
Towards a novel, truly cylindrical, ultra-thin silicon detector for the ALICE Inner Tracker System

Bogdan Mihail BLIDARU

Overview

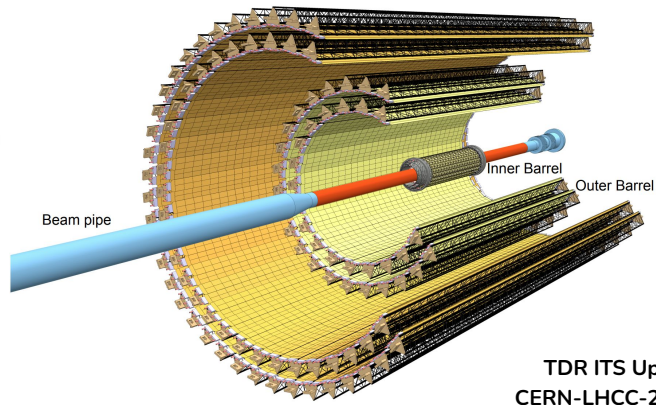
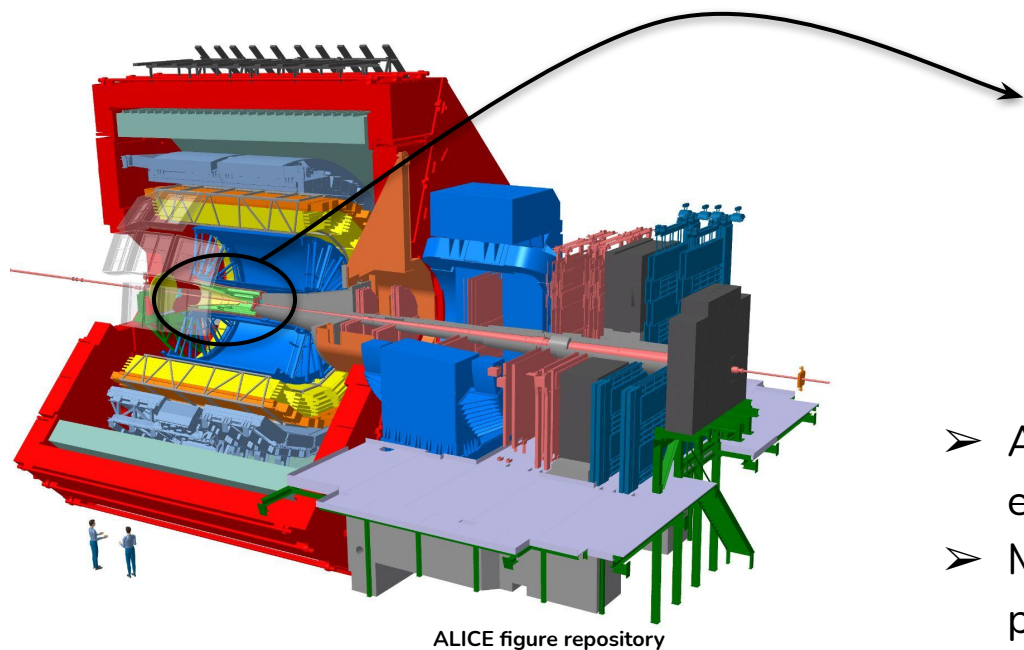
- ALPIDE @ ITS2, ALICE
- Motivation & design for ITS3
- R&D highlights
 - testbeam characterization of bent ALPIDEs





- ALICE is the heavy-ion physics focused experiment at the LHC
- Main goal: study of the quark-gluon plasma in heavy-ion collisions
- Currently LHC LS2 → detector upgrades
- Inner Tracking System 2 (ITS2)

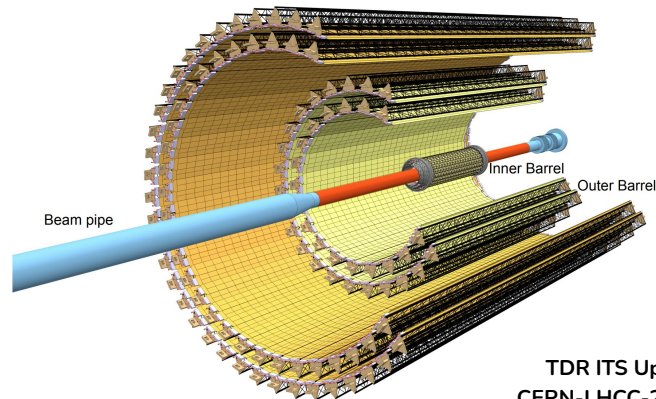
ALICE detector - the Inner Tracking System (ITS2)



- ALICE is the heavy-ion physics focused experiment at the LHC
- Main goal: study of the quark-gluon plasma in heavy-ion collisions
- Currently LHC LS2 → detector upgrades

- ITS2 (LHC LS2, currently under installation)
 - Novel, MAPS based, detector design
 - Seven layers of ALPIDE sensors
 - Increased vertexing and tracking performance with respect to ITS

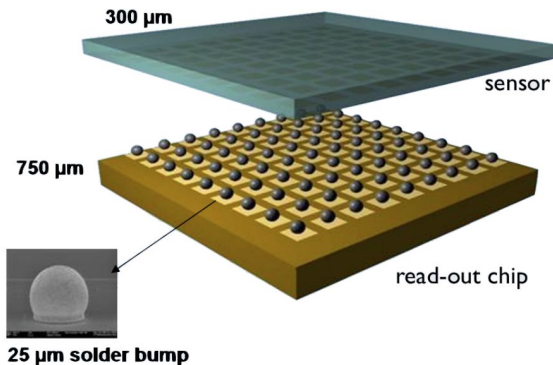
- Layer 0 closer to IP: 39mm \rightarrow 23mm
- Reduced material budget (x/X_0) per layer: 1.14% \rightarrow 0.3%
- Rate capability: 1kHz \rightarrow 100 kHz (Pb-Pb)
- Higher granularity (pixel size): $50 \times 425 \mu\text{m}^2 \rightarrow 29 \times 27 \mu\text{m}^2$



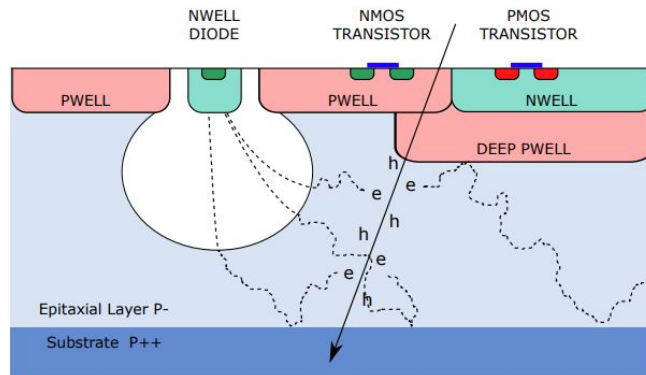
TDR ITS Upgrade
CERN-LHCC-2013-024

Hybrid vs monolithic pixel sensors

ITS



$50 \times 425 \mu\text{m}^2$



ITS2

$29 \times 27 \mu\text{m}^2$

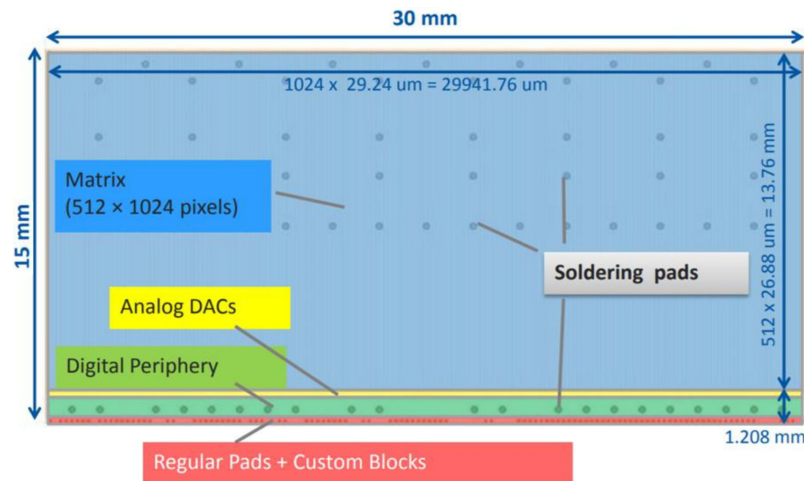


ALPIDE - a closer look

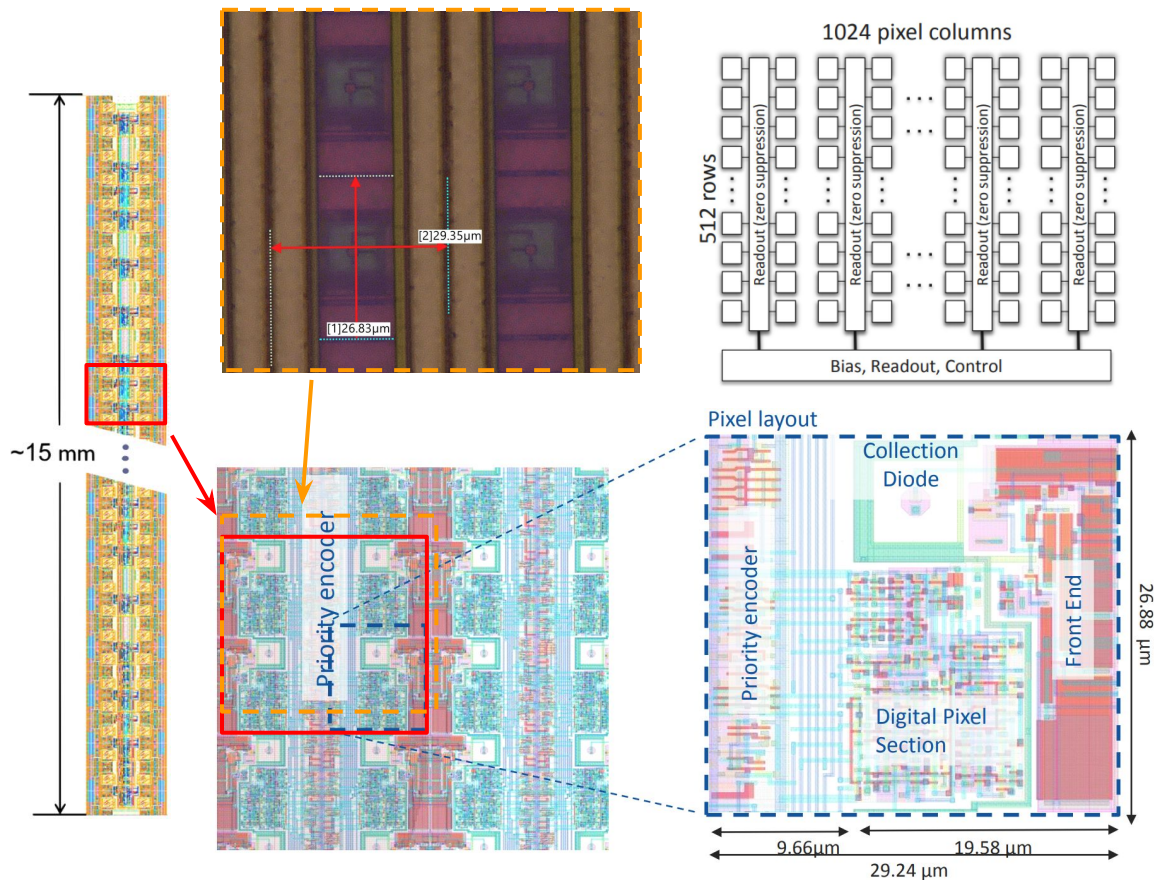


Alpide on carrier card

- MAPS produced in the 180nm TowerJazz CMOS IP
- $30 \times 15 \text{ mm}^2$ matrix of pixels
- Pixel pitch $29.24 \times 26.88 \mu\text{m}^2$ ($1024 \times 512 \text{ px}$)
- Silicon thickness $50 \mu\text{m}$
- Spatial resolution $\sim 5 \mu\text{m}$ (required: $5 \mu\text{m}$)
- Time resolution $\sim 1 \mu\text{s}$
- Detection efficiency $\gg 99\%$ (req: $>99\%$)
- Fake hit rate $\ll \text{req. } 10^{-6} / \text{px} / \text{event}$
- Power consumption (7 mW/cm^2 matrix, 150 mW/cm^2 periphery) $\sim 40 \text{ mW/cm}^2$



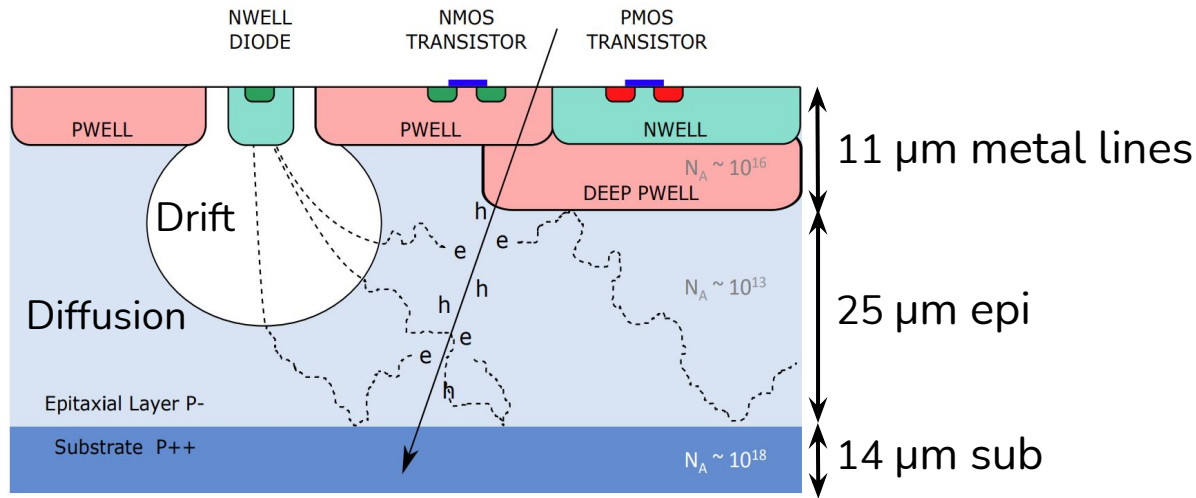
ALPIDE layout features



- Readout of the pixel data from the matrix based on combinatorial readout
- ALPIDE readout is binary (hit / no hit information)
- Data transfer from matrix to periphery is zero suppressed via a priority encoder circuit
- No free running CLK distributed in the matrix, no signaling activity if no hits to read

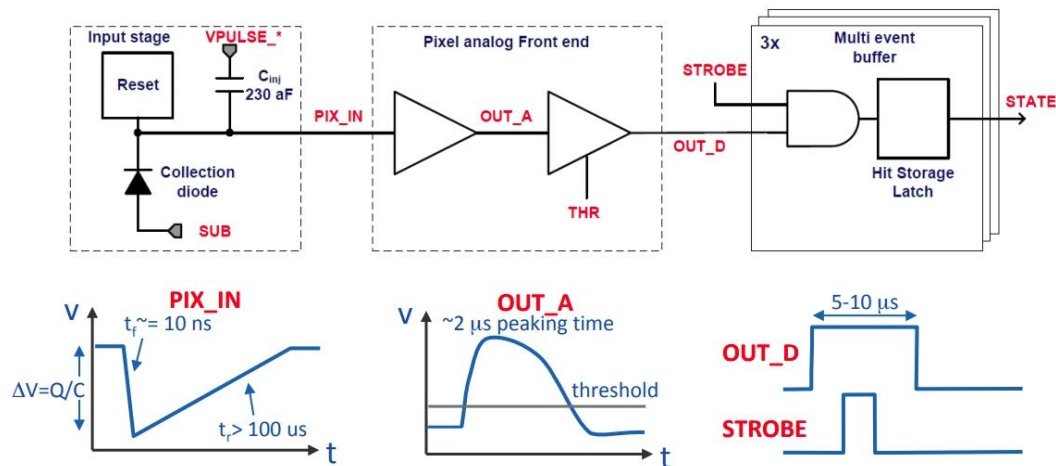
Very low power consumption!

ALPIDE - the pixel



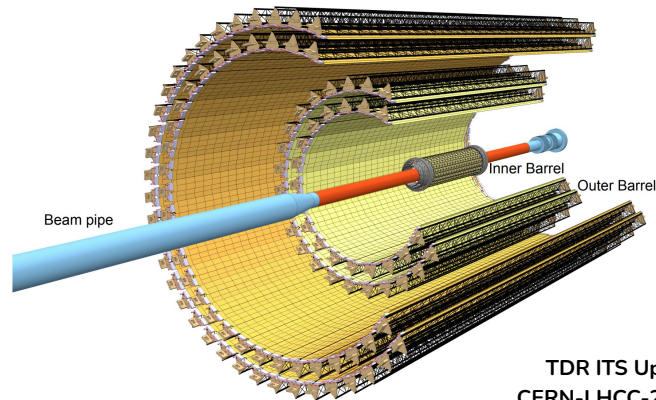
- Charge is created in the high resistivity ($>1\text{k}\Omega\text{cm}$) epitaxial layer (half the sensor thickness)
- Highly doped P substrate and PWELLS act as reflective barriers and contain the charge
- Deep PWELL shields NWELLS, allowing PMOS transistors (full CMOS within active area)
- Small NWELL diode (2 μm diameter) \rightarrow low capacitance (2fF) \rightarrow large S/N
- Reverse bias (-3V) used to increase depletion of the diode

ALPIDE - the pixel; signal processing



- Charge is collected, amplified, discriminated and strobed to in-pixel memory
- The analogue front-end is continuously active (non-linear response); it acts as analogue delay line; rise-time ($< 2\mu$ s) - defines time resolution
- Global shutter (STROBE) latches discriminated hits to memory

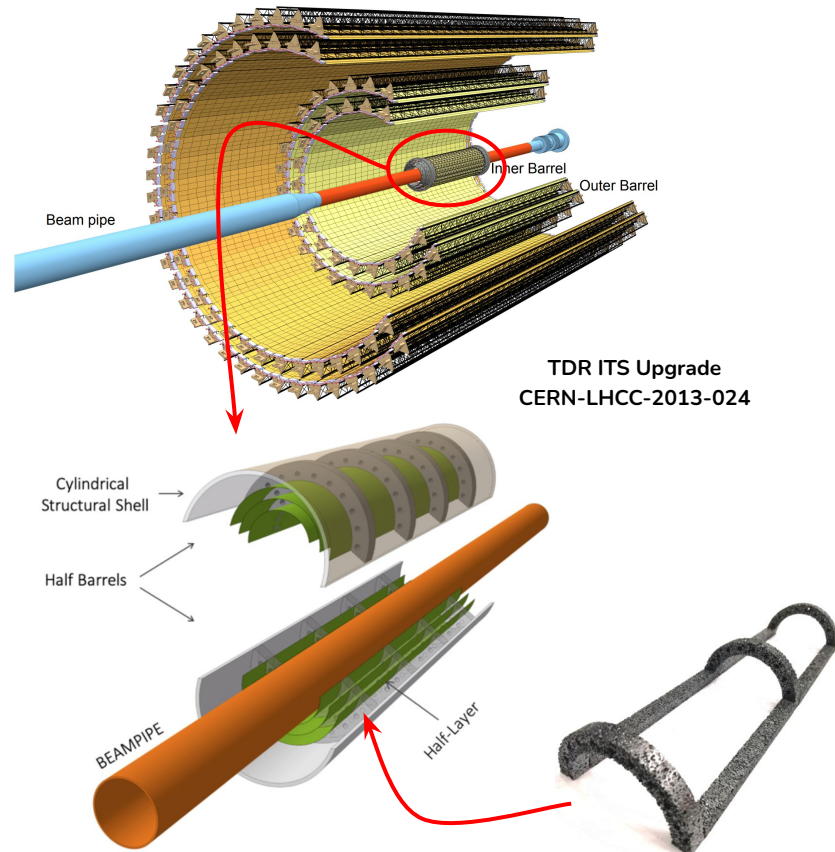
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 - Novel, MAPS based, detector design
 - Seven layers of ALPIDE sensors
 - Increased vertexing and tracking performance with respect to ITS



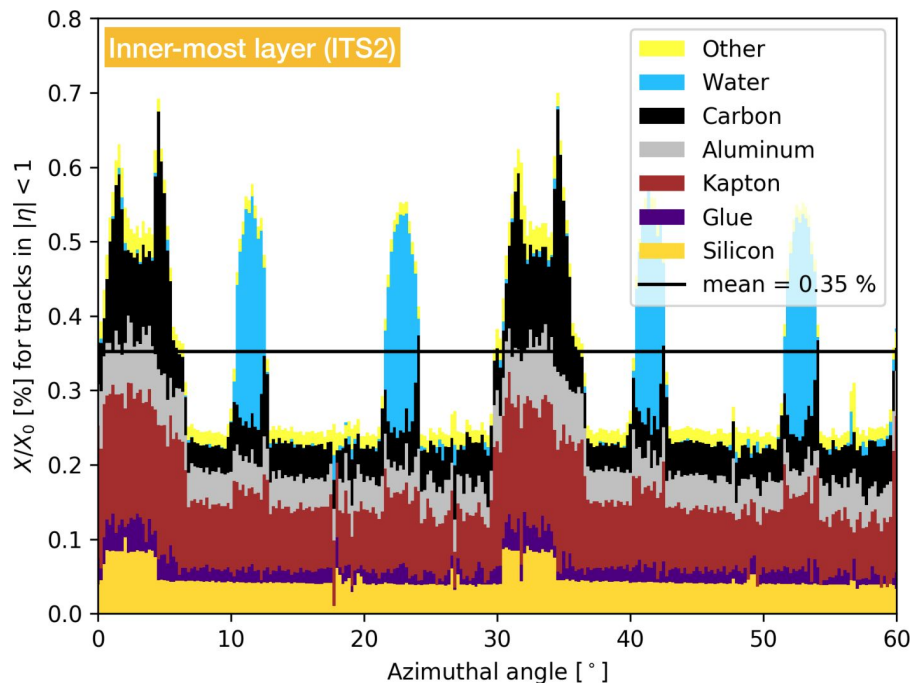
TDR ITS Upgrade
CERN-LHCC-2013-024

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 - Reduced material budget (x/X_0) per layer: 1.14% → 0.3%
 - Rate capability: 1kHz → 100 kHz (Pb-Pb)
 - Higher granularity (pixel size): $50 \times 425 \mu\text{m}^2 \rightarrow 29 \times 27 \mu\text{m}^2$
- ITS2 is a state of the art MAPS detector
 - Further improvements are possible

- ITS2 (LHC LS2, currently under installation)
 - Novel, MAPS based, detector design
 - Seven layers of ALPIDE sensors
 - Increased vertexing and tracking performance with respect to ITS
- ITS3 (LHC LS3, 2025-2027)
 - Aim: replace the three ITS2 IB layers
 - Ultra-light, wafer-scale, curved sensors
 - Currently: R&D based on bent ALPIDE
 - First testbeam results with bent ALPIDE

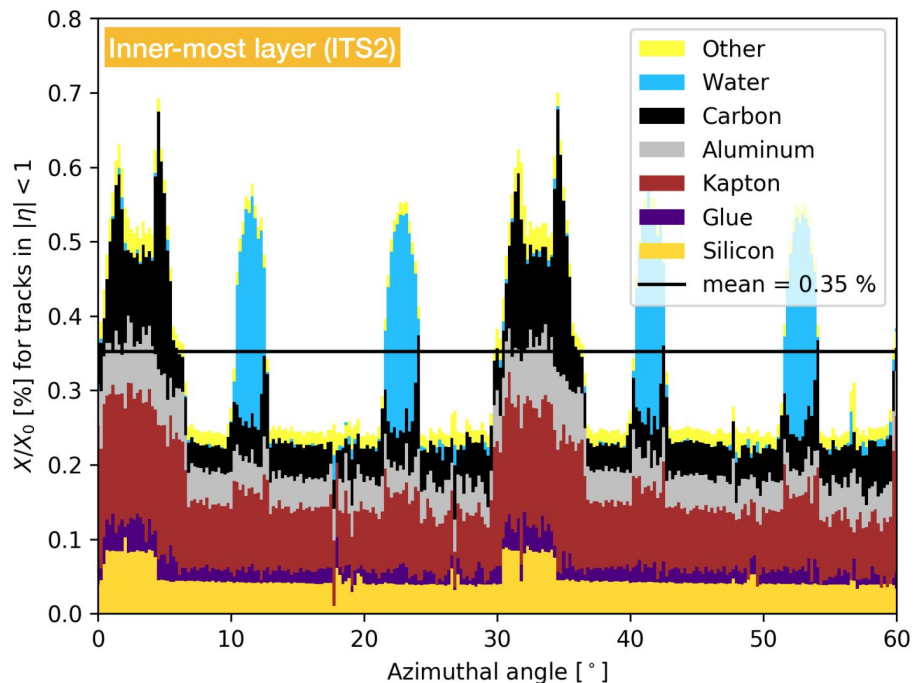


Motivation for the ITS3



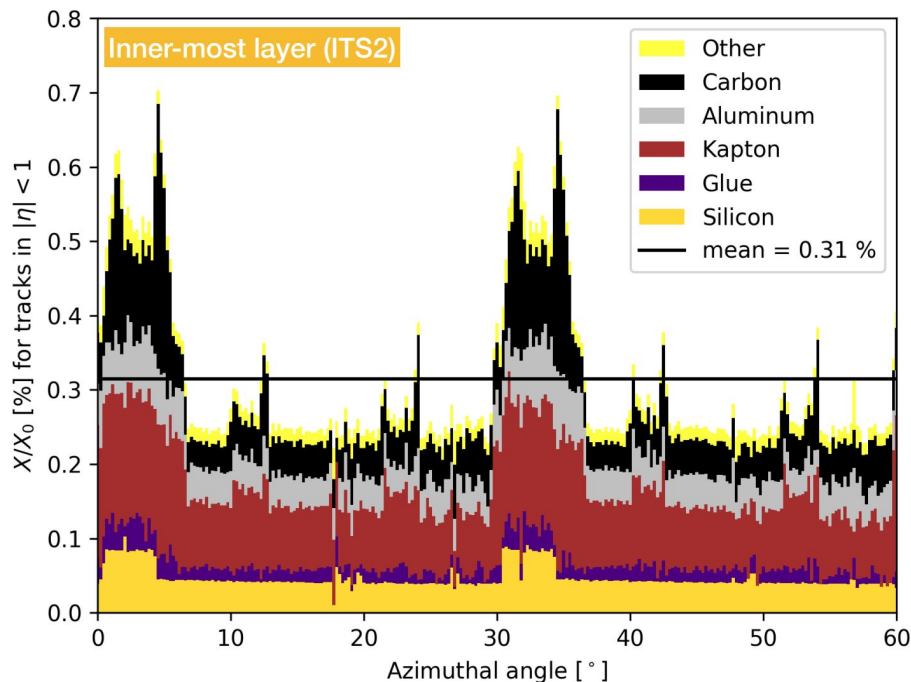
- Si accounts for **1/7th** of the total material (irregularities due to support/cooling)

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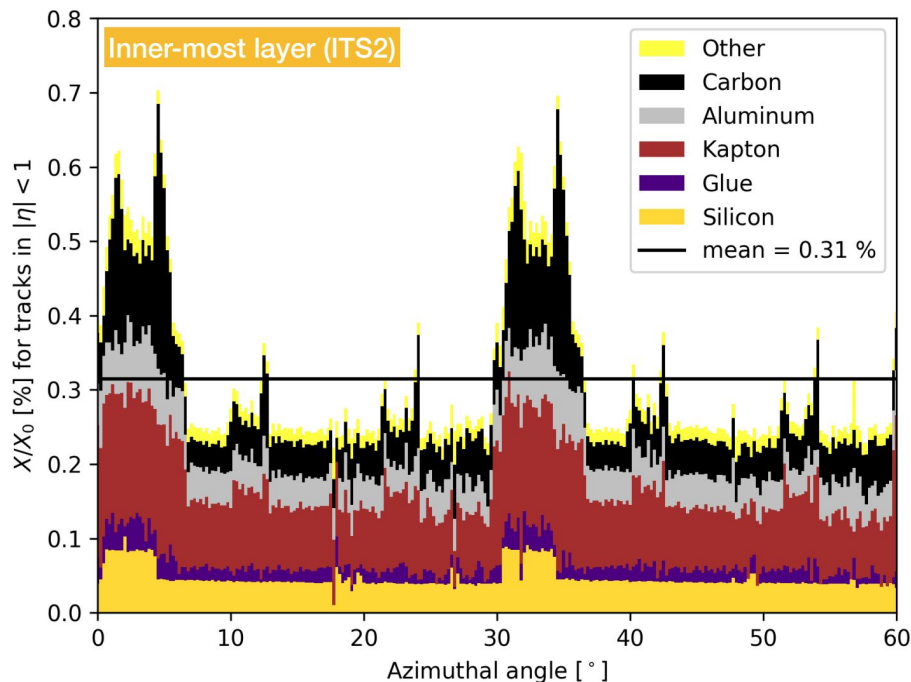
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→ if power consumption < 20 mW/cm²

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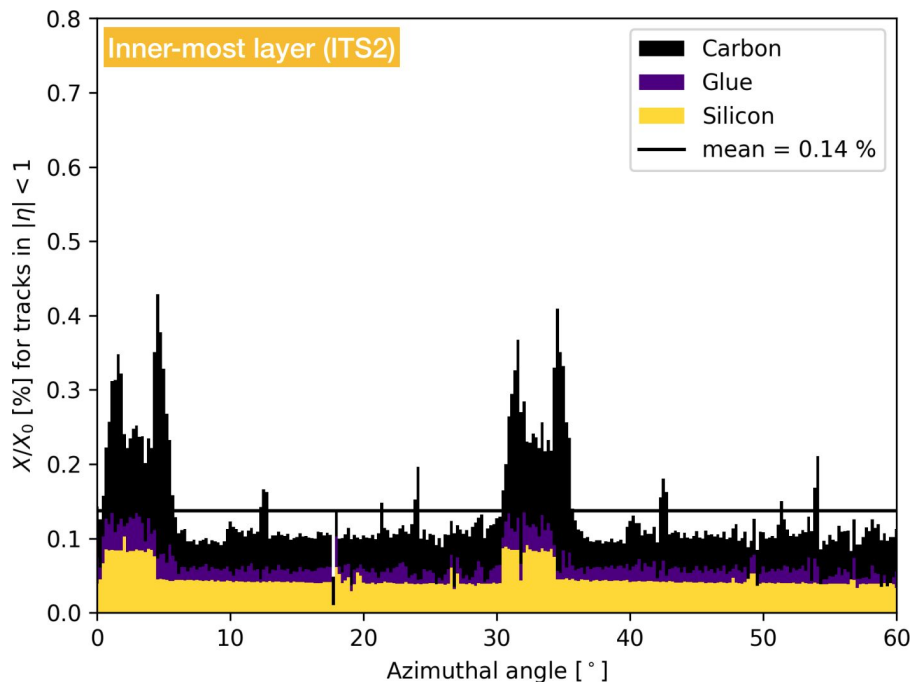
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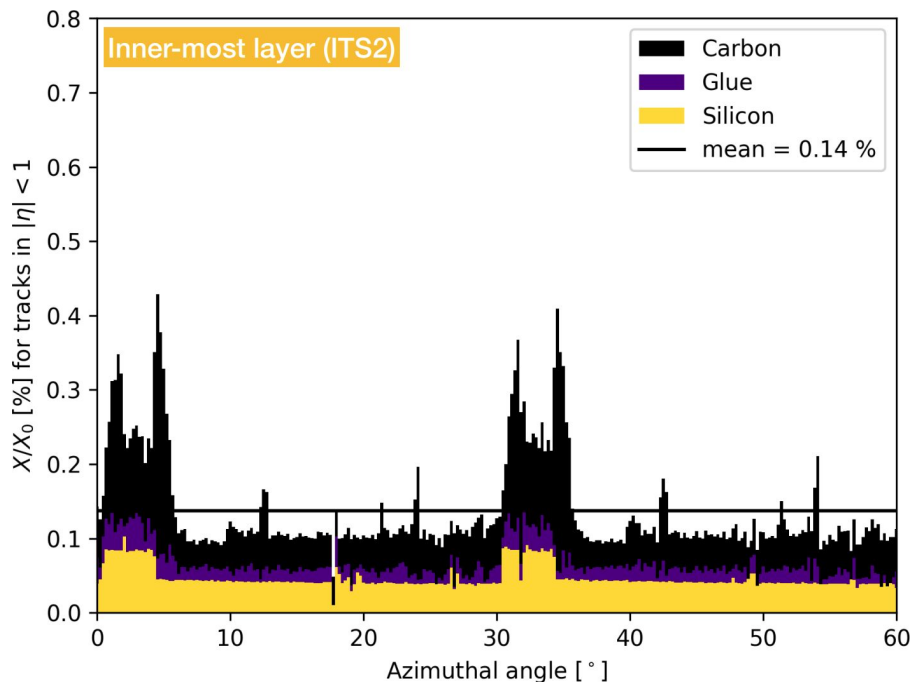


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- Removal of the circuit board for power distribution and data lines possible
→ if integrated on chip (make single large chips, use CMOS metal layers)

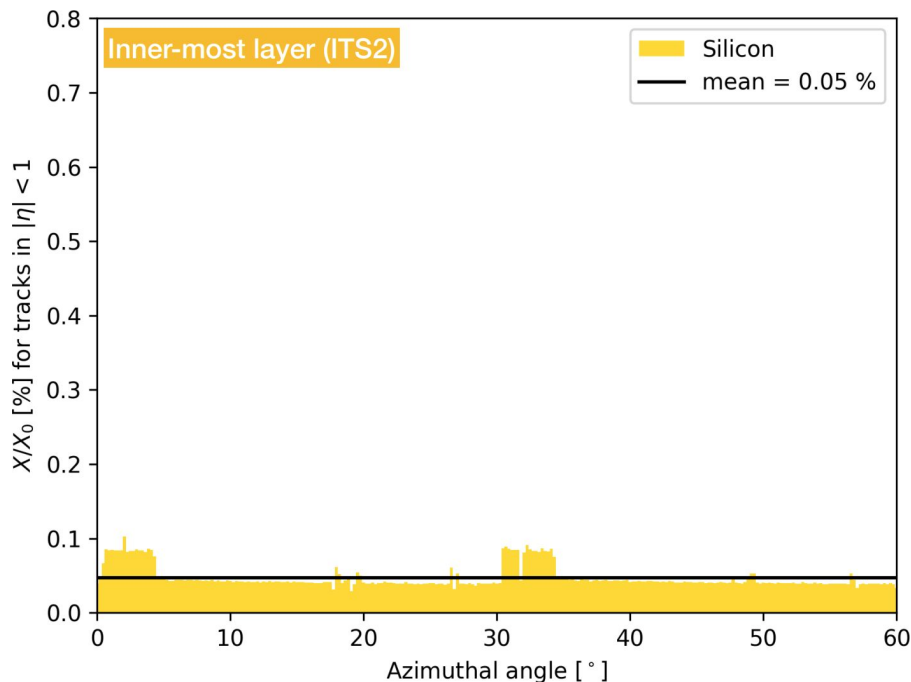
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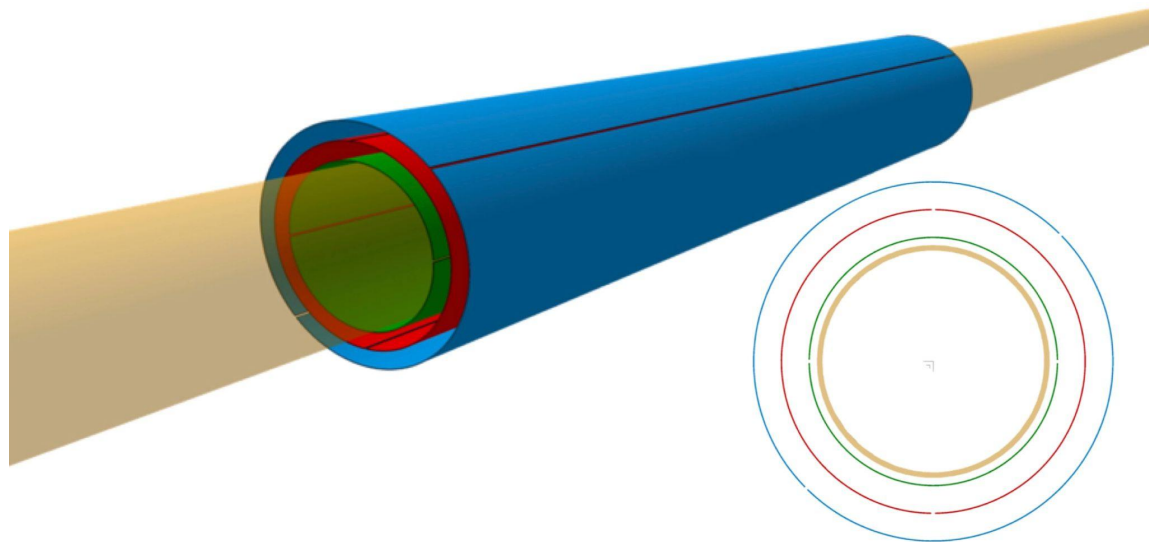


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- Move mechanical support outside acceptance → benefit from bent Si structure (+ ultra-light carbon foam)



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ITS3 detector concept

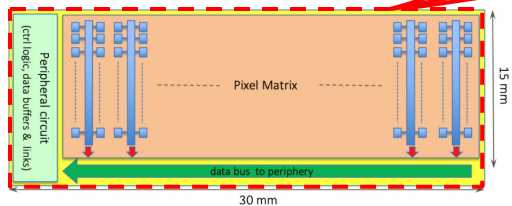


- Ultra thin and closer to the interaction point
 - Layer 0: 23 → 18 mm
 - Layer thickness:
 $0.35 \rightarrow < 0.05\% X_0$
(beampipe: 500 μ m Be 0.14% X_0)
 - Uniformly distributed material (no system. errors)
- Wafer scale ← stitching
- Layout based on air cooling

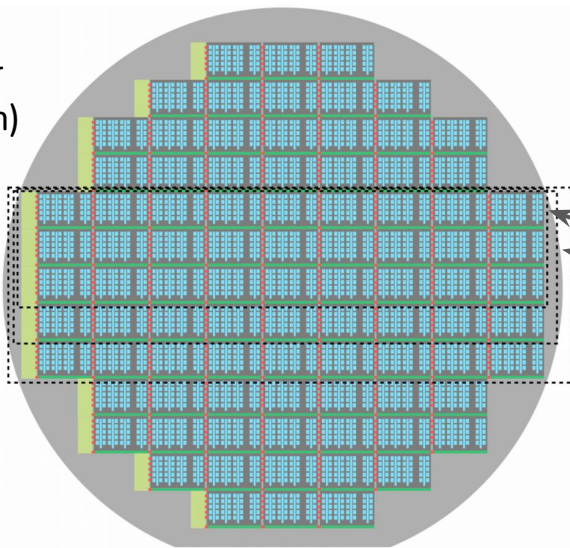
Beam pipe inner/outer radius (mm)	16.0 / 16.5		
	Layer 0	Layer 1	Layer 2
Radial distance from IP (mm)	18	24	30
Pixel sensor dimensions (mm ²)	280×56.5	280×75.5	280×94
Sensitive area length (mm)	300		
Pixel size (μm ²)	O(10×10)		

Architecture & stitching

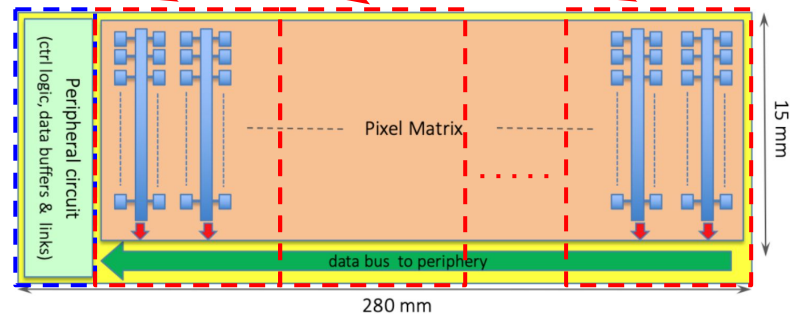
ALPIDE-like
sensor



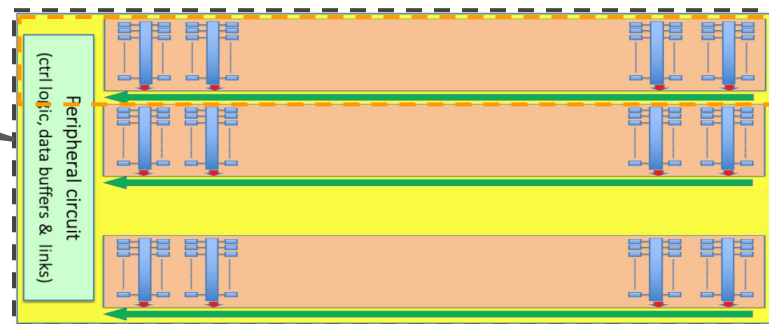
Si wafer
(300mm)



1D stitching

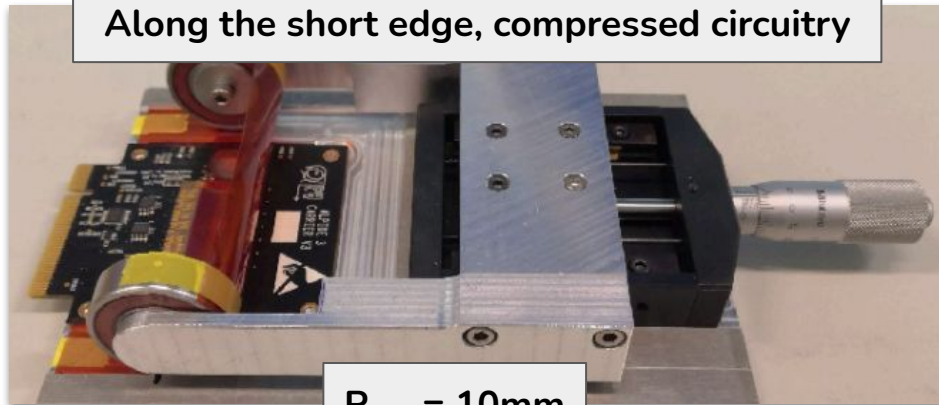


2D stitching



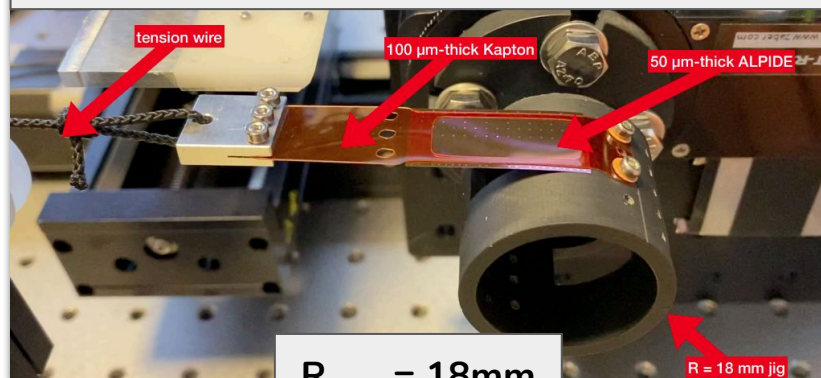
Bending ALPIDEs

Along the short edge, compressed circuitry

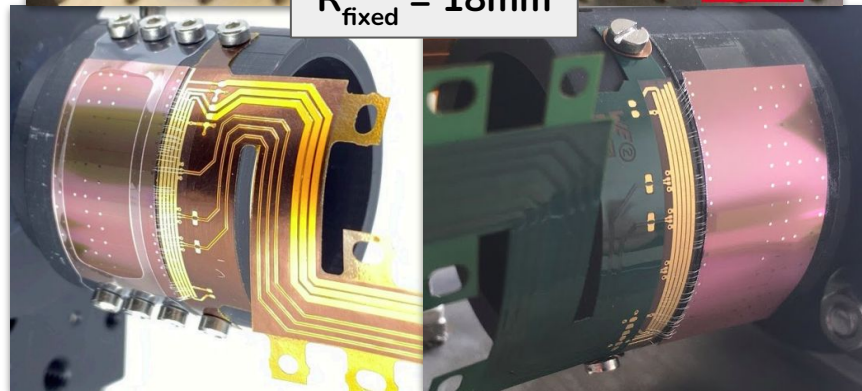
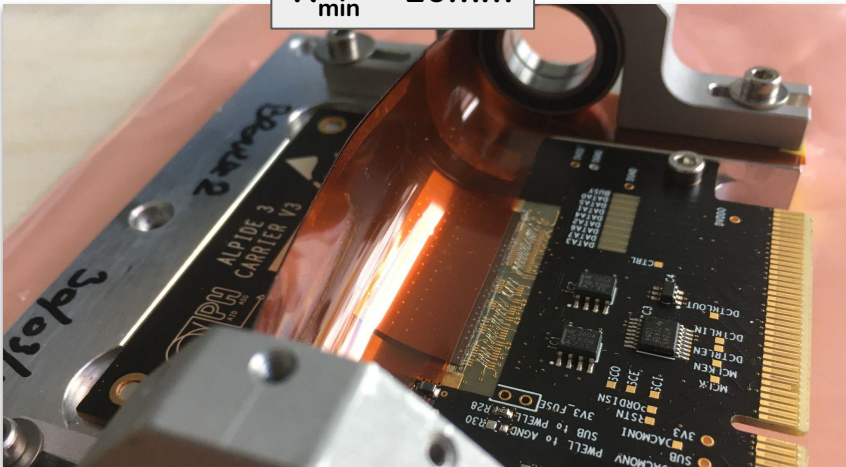


$R_{\min} = 10\text{mm}$

Along the long edge, stretched circuitry

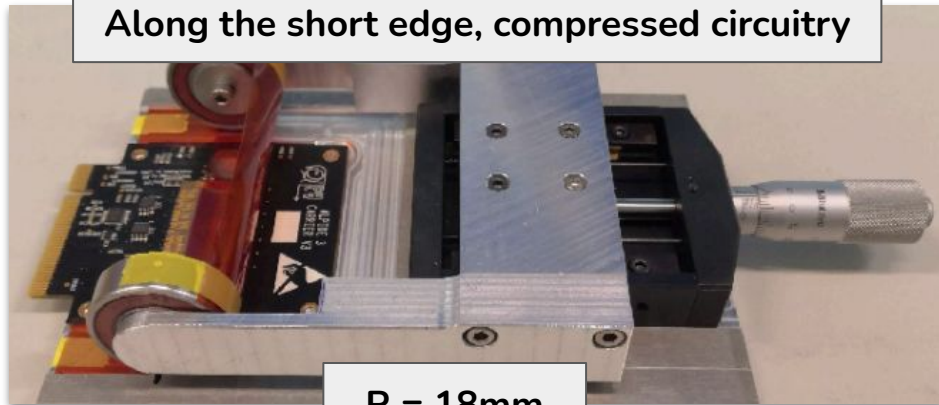


$R_{\text{fixed}} = 18\text{mm}$



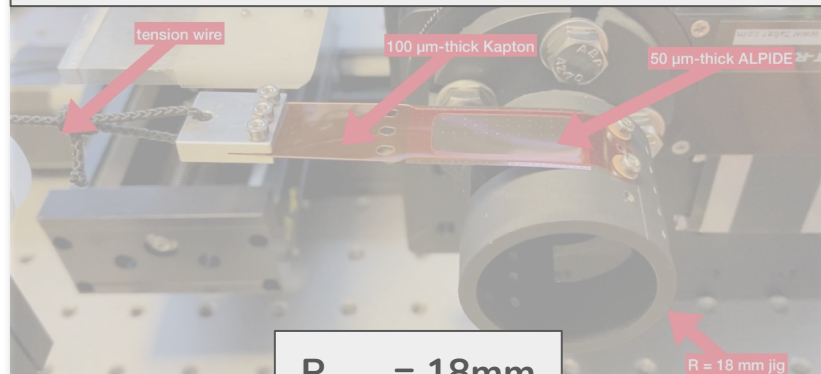
Bending ALPIDEs - part I

Along the short edge, compressed circuitry

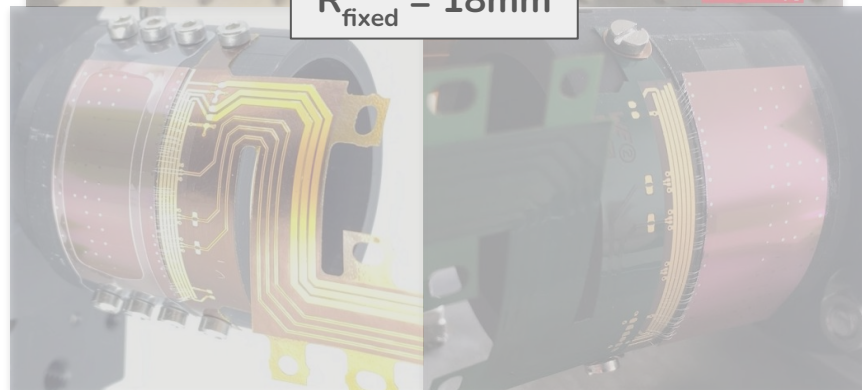
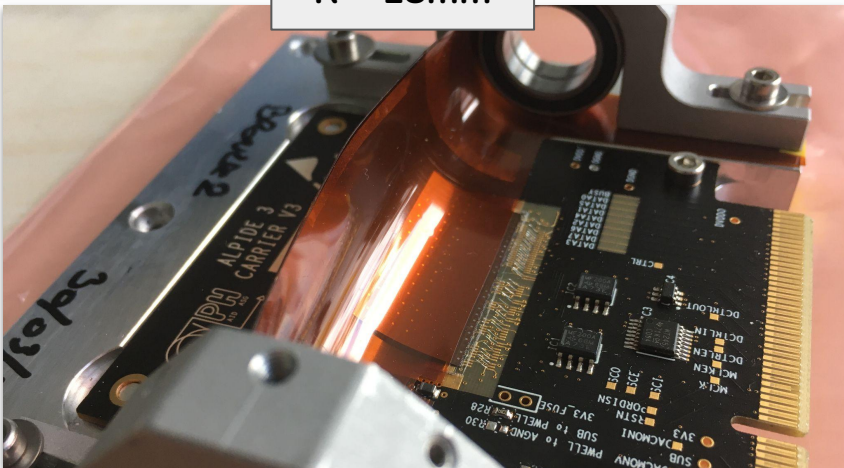


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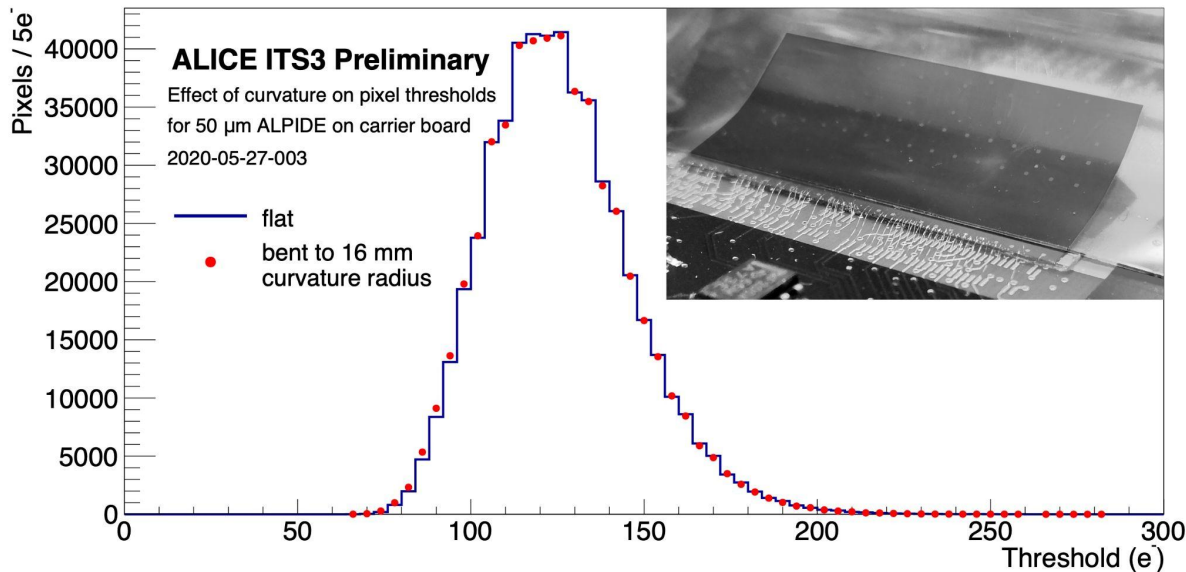
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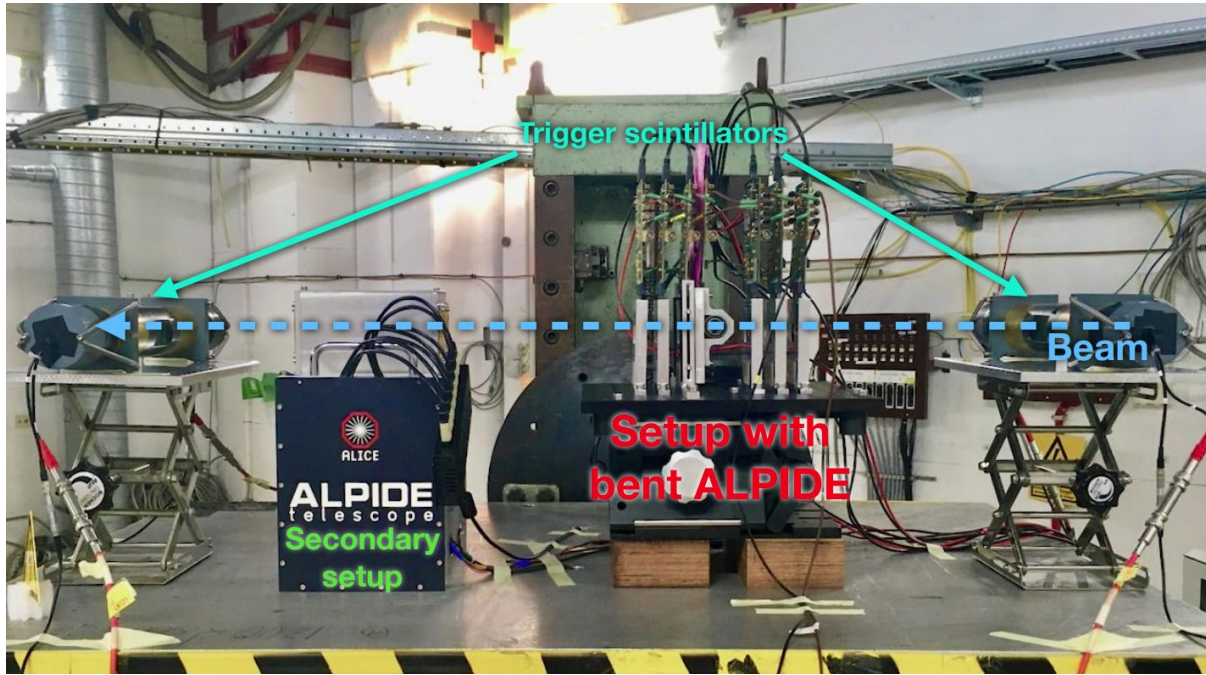


Bent ALPIDE - part I - electrical characterization



- Electrical performance of the chip is unchanged with respect to the flat state
- Threshold and noise unchanged
- No variation in the number of dead pixels

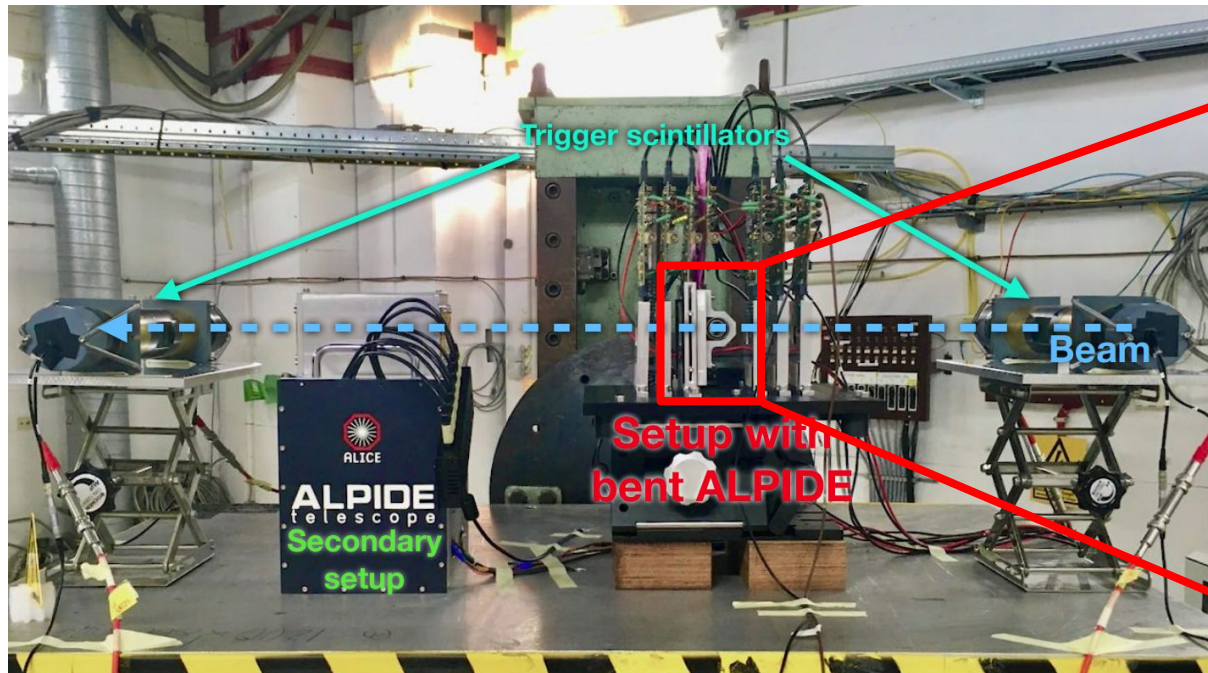
Bent ALPIDE - part I - testbeam



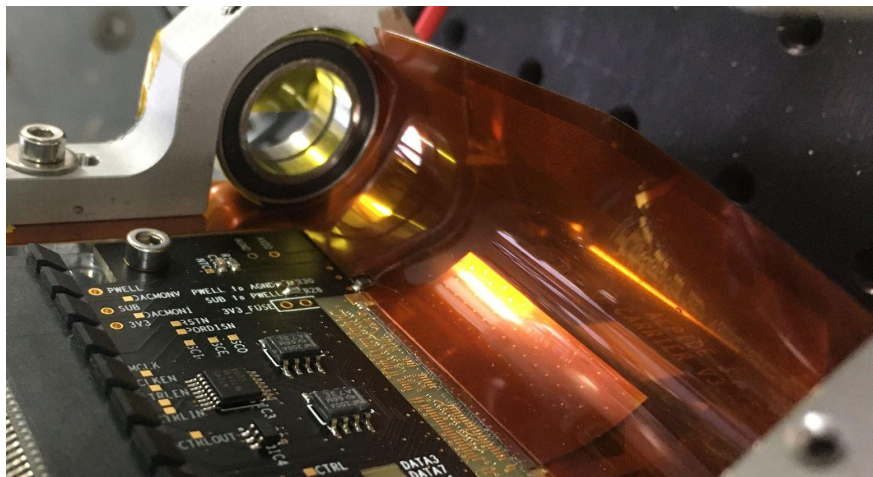
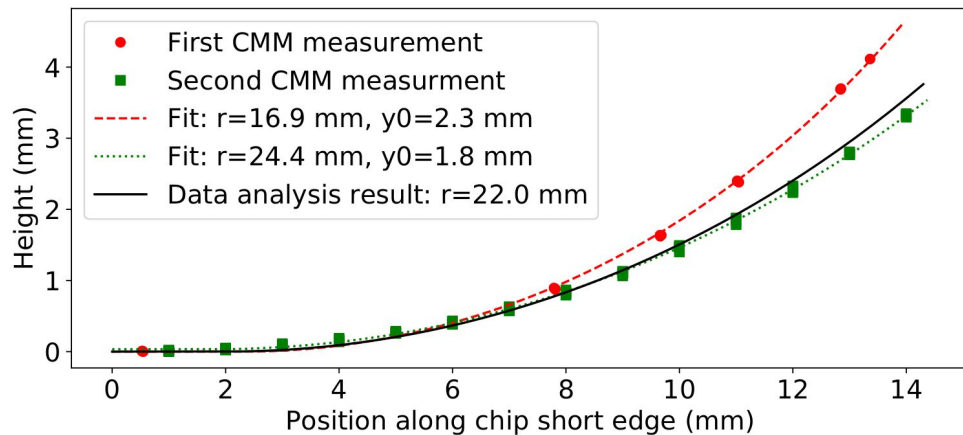
- First ever testbeam with bent silicon pixel sensors
- 5.4 GeV e^- , DESY
- 6 flat ALPIDEs as reference planes (track reconstruction)
- Determine: efficiency of bent sensor, position resolution
- Corryvreckan* used for data analysis

* <https://cern.ch/corryvreckan>

Bent ALPIDE - part I - testbeam

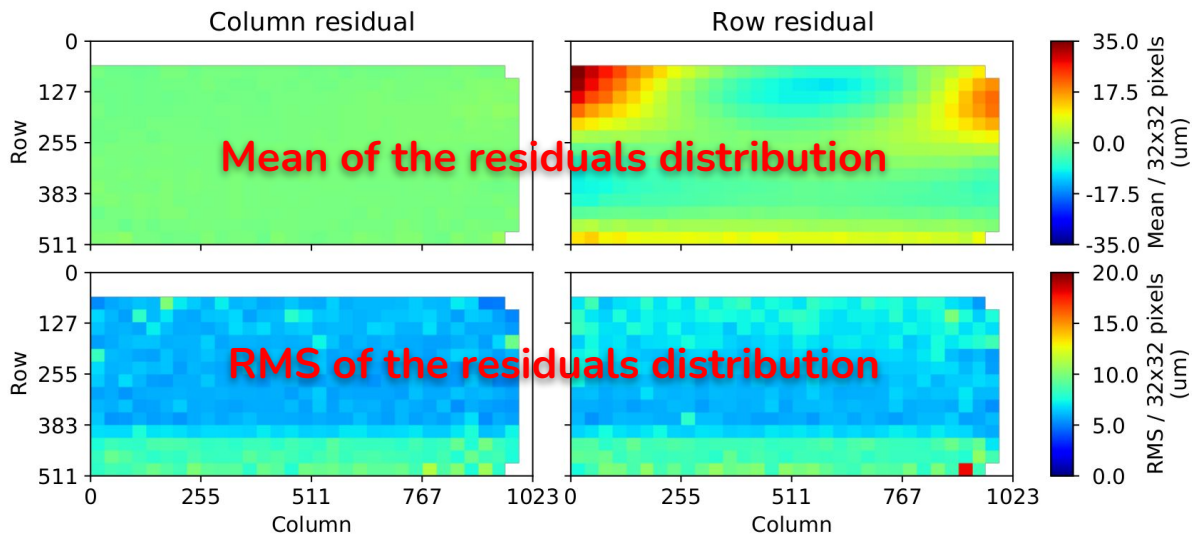
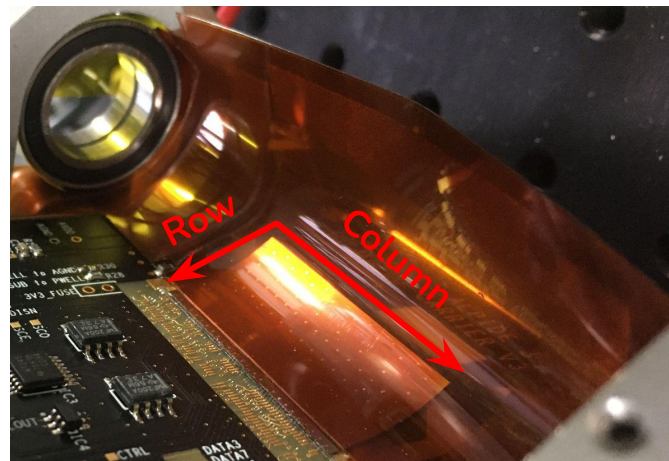


Bent ALPIDE - part I - radius assessment



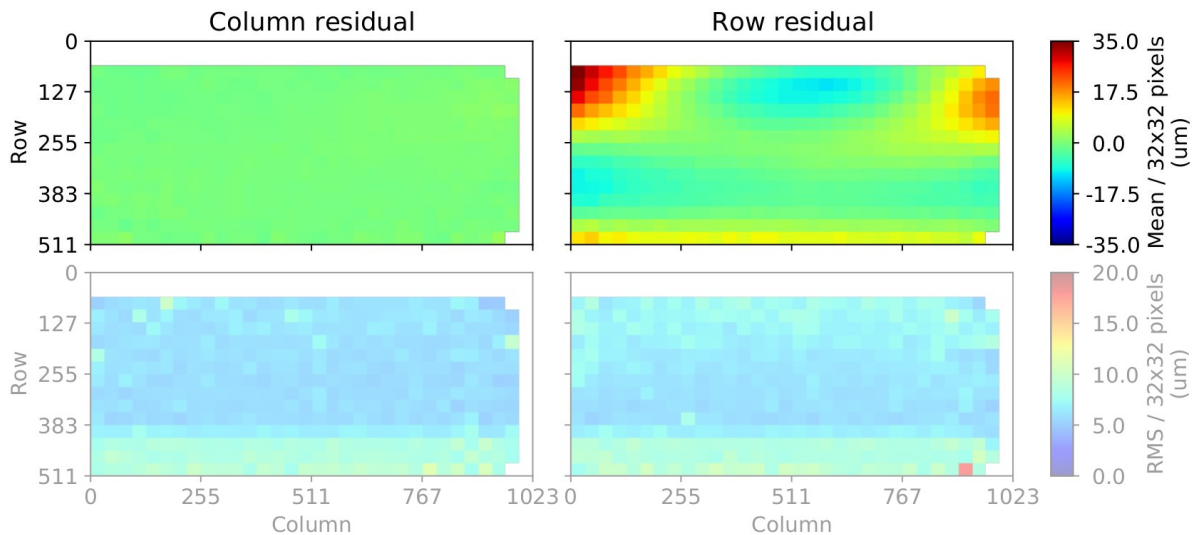
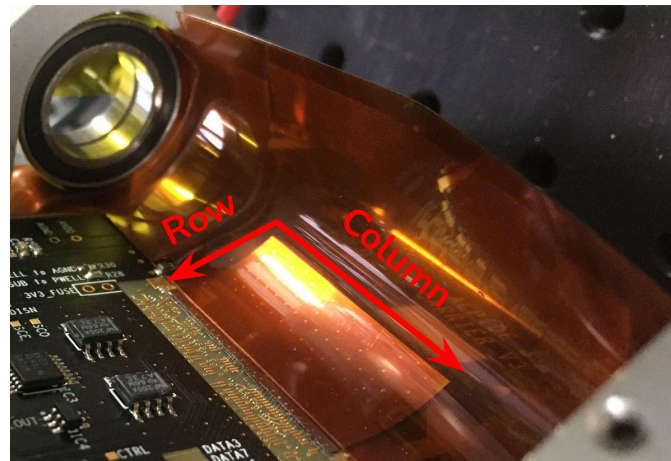
- Sensor bent to 16.9 mm - measured with Coordinate Measurement Machine (CMM) before the testbeam
- Re-measured after testbeam: observed relaxation ($R_{\text{new}}=24.4\text{mm}$)
- Results of the testbeam analysis show a radius of 22 ± 1 mm
- Cylindrical geometry model used to describe the bent chip
- Radius free parameter ← minimization of the residuals

Bent ALPIDE - part I - spatial residuals

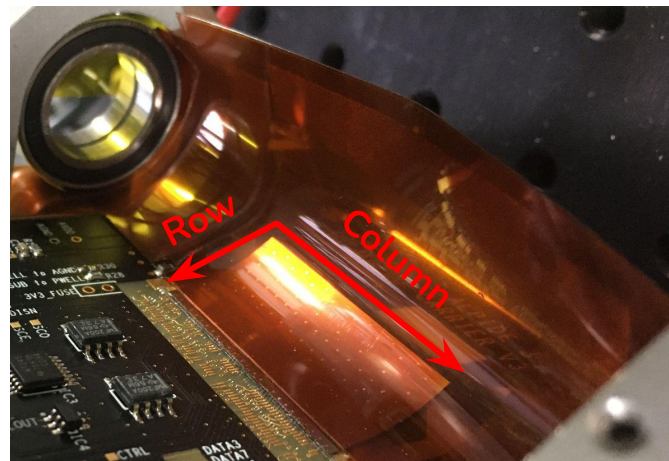


Bent ALPIDE - part I - radius assessment

- **Mean of the residuals distribution**
 - Invariant to rotation around column axis
 - Row direction - compatible with cylindrical geometry model up to $35\ \mu\text{m}$

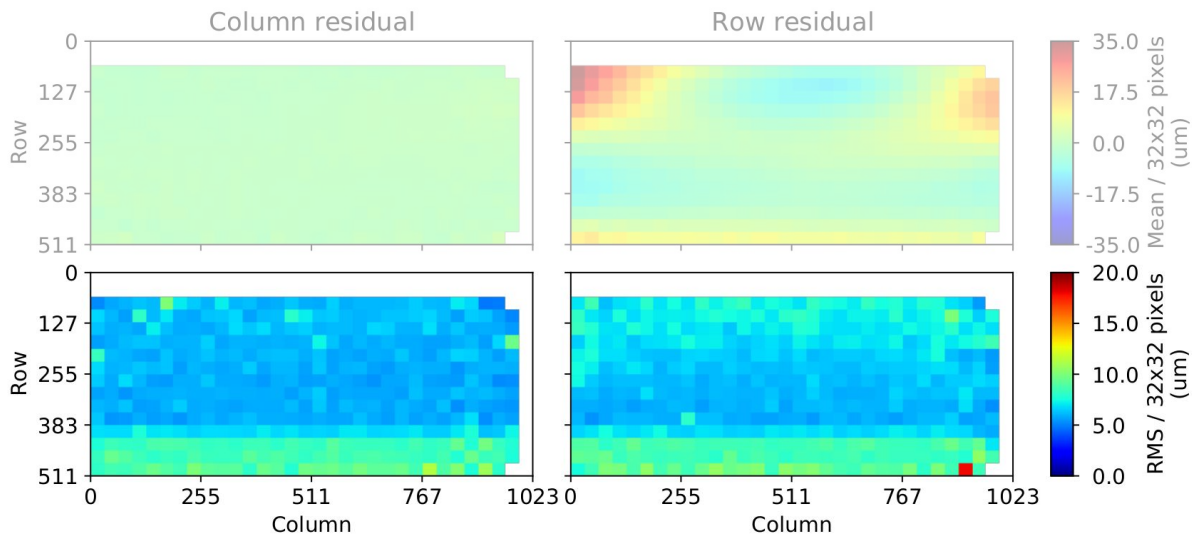


Bent ALPIDE - part I - radius assessment



➤ RMS of the residuals

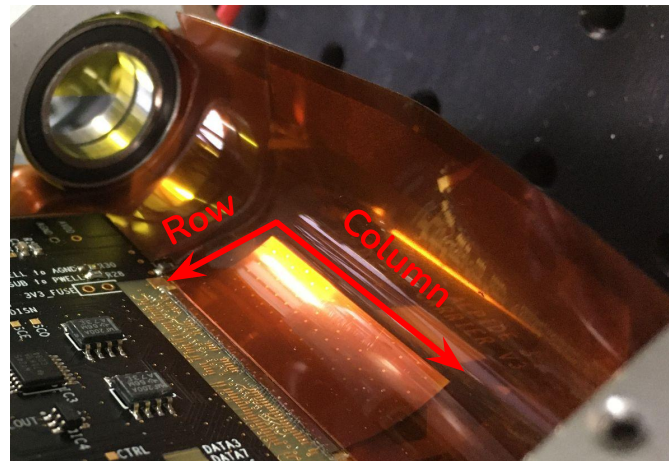
- Larger where the chip is glued to PCB (due to scattering)
- Row direction - increase with the incident angle of the beam



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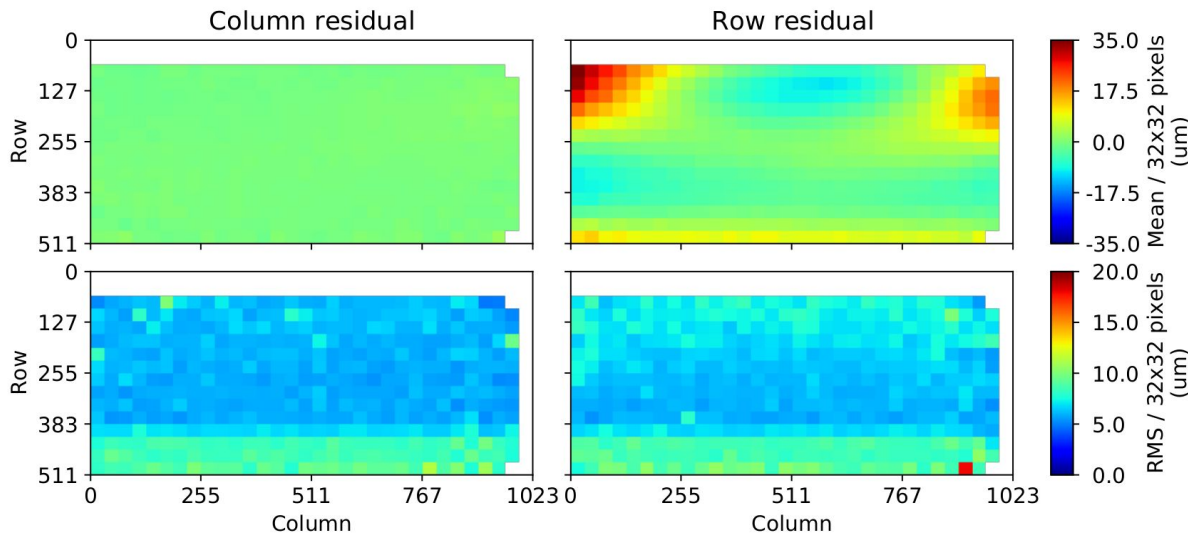
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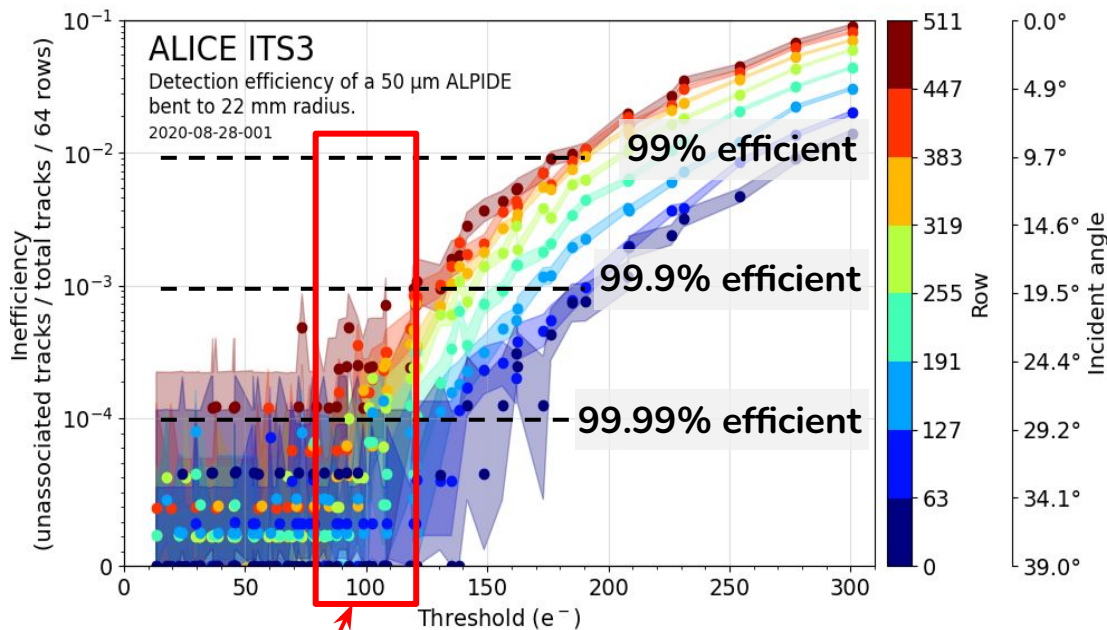
➤ RMS of the residuals

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- Row direction - increase with the incident angle of the beam



Bent ALPIDE - part I - efficiency of bent sensor

- **Inefficiency** as a function of pixel threshold for various chip rows (= incident angles)
- Linear-logarithmic plot



- Only 0V back-bias!
- More than 8000 tracks per data point
- Below a threshold of 100 e^- , only 1 in 10^4 tracks is inefficient
- Above 100 e^- , the inefficiency increases with decreasing beam incident angle (increasing row number)
- **Excellent detection performance retained!**
- Publication under internal review

Nominal operating point

Bending ALPIDEs - part II

Along the short edge, compressed circuitry

$R = 18\text{mm}$

Along the long edge, stretched circuitry

tension wire

100 μm -thick Kapton

50 μm -thick ALPIDE

$R_{\text{fixed}} = 18\text{mm}$

$R = 18\text{mm}$ jig

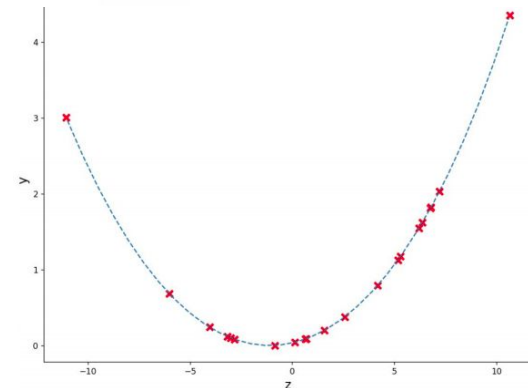
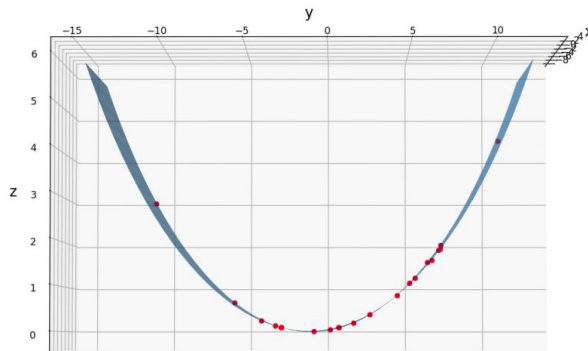
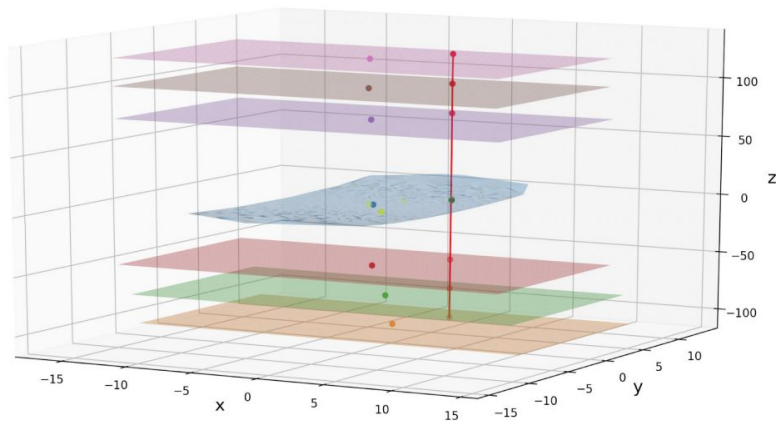
Bending ALPIDEs - part II

- More rigid construction
- Bent along the long side (decompressed circuitry) → piezoresistive effect (change in electrical resistivity)
- Connection to DAQ board done via flex-print-cable (FPC)
- Bonded after bending
- Multiple radii (18,24,30 mm), corresponding to ITS3 layers



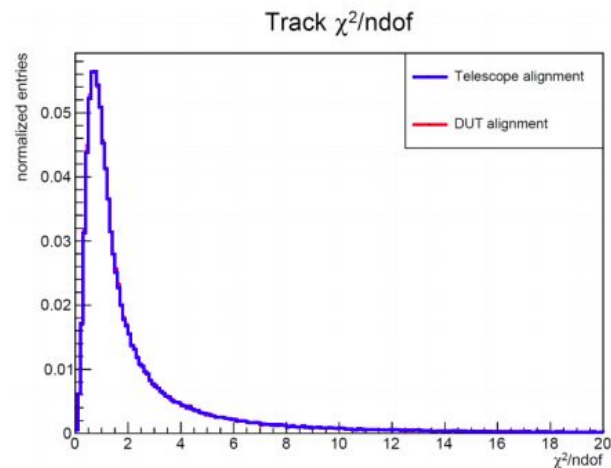
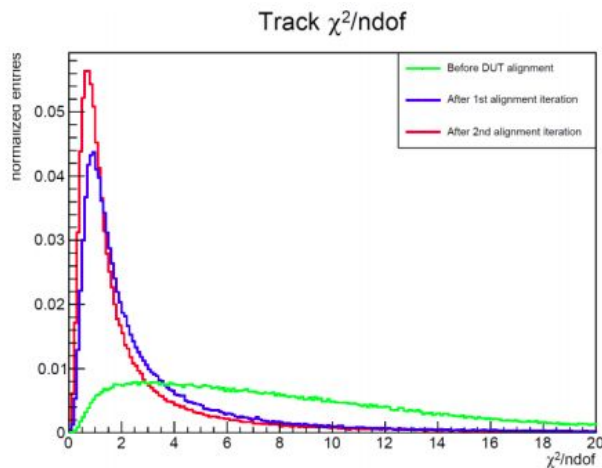
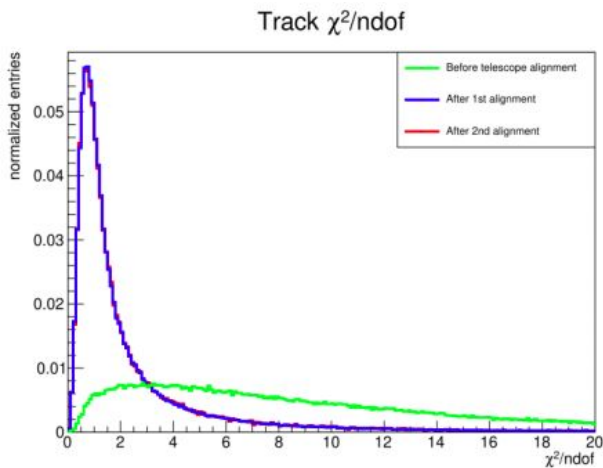
Bending ALPIDEs - part II - new geometry

- Include cylindrical geometry in code
- Account for change of coordinate transformation (local \rightarrow global)
- Correct for different z track intercept (no curved detectors previously studied)
- Ensure all DOFs are accounted for
- Check that tracks/clusters lie on the same plane



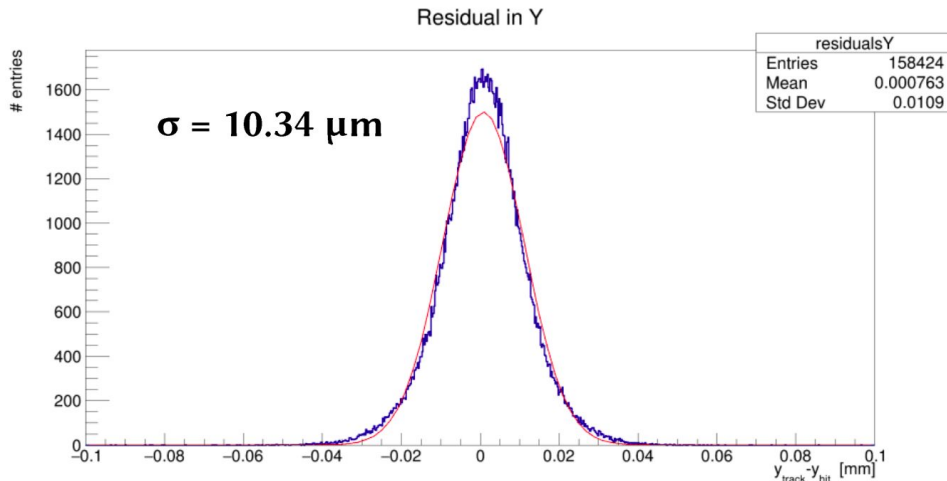
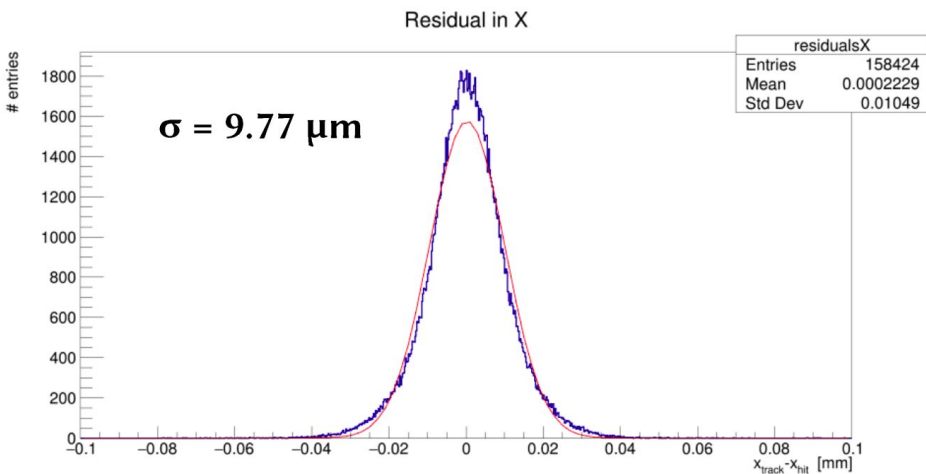
Bending ALPIDEs - part II - tracking

- Align telescope planes with tracks from the reference planes only
- Include and align the DUT with respect to the telescope
- Straight tracks only (not accounting for scattering)



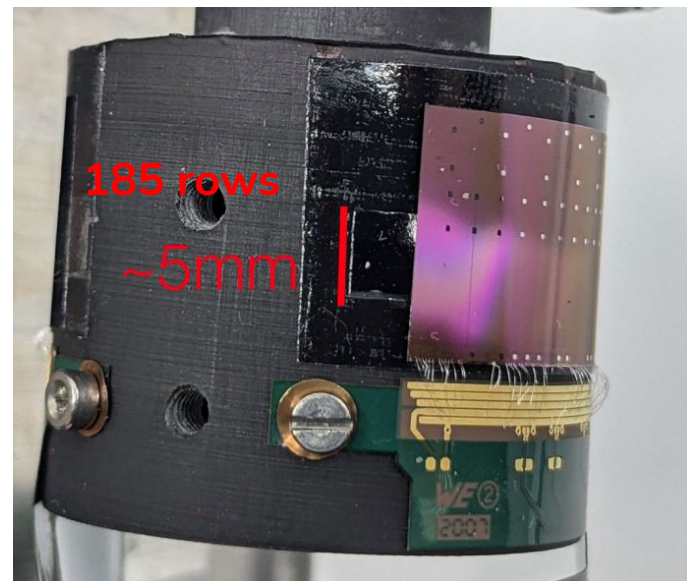
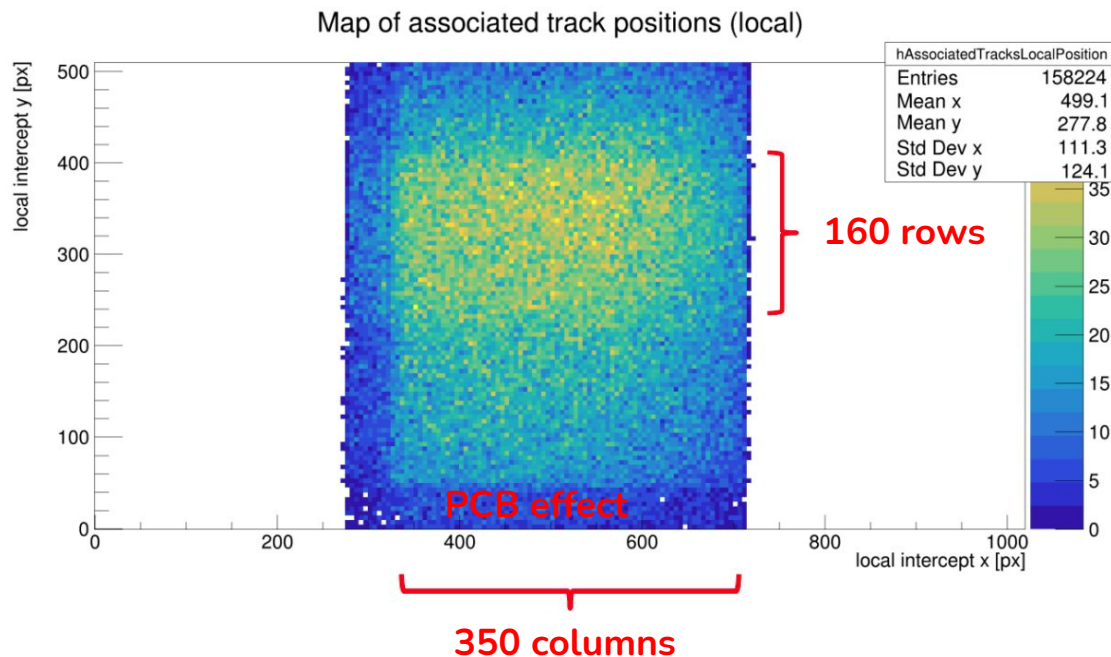
Bending ALPIDEs - part II - spatial residuals

- Broad residuals
- Convolution of effects?



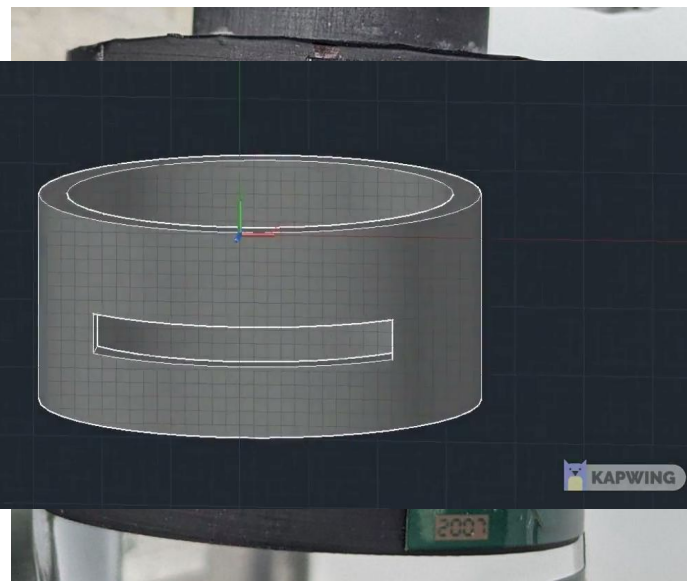
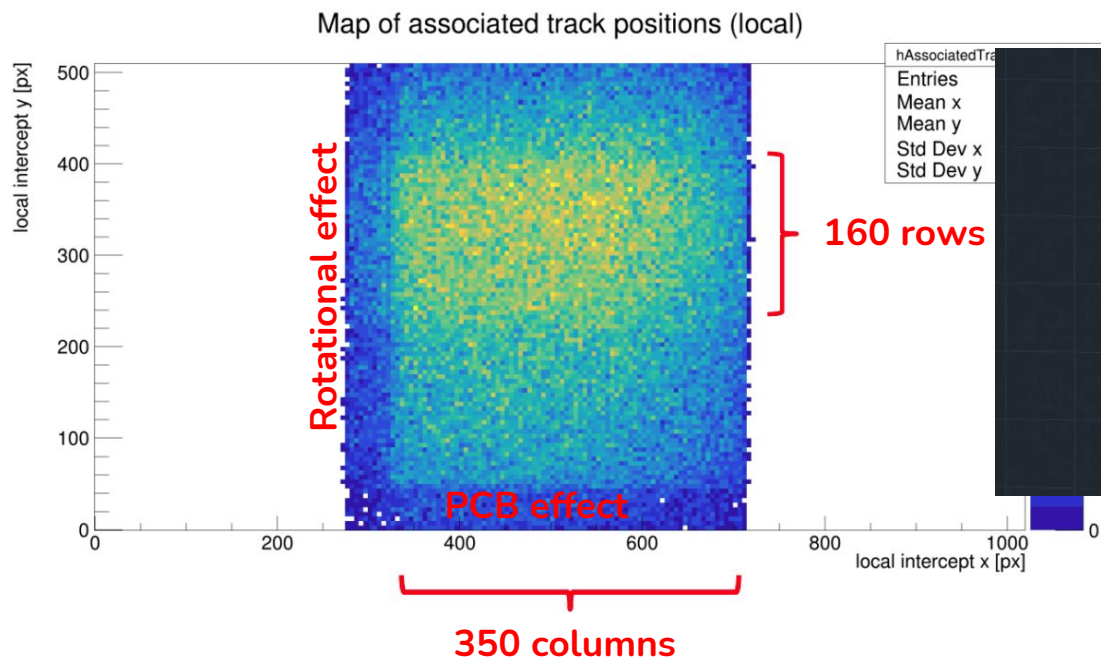
Bending ALPIDEs - part II - ROI definition

- Tracks associated to clusters on DUT
- Region of interest (ROI) selection - chose part of the sensor to characterize

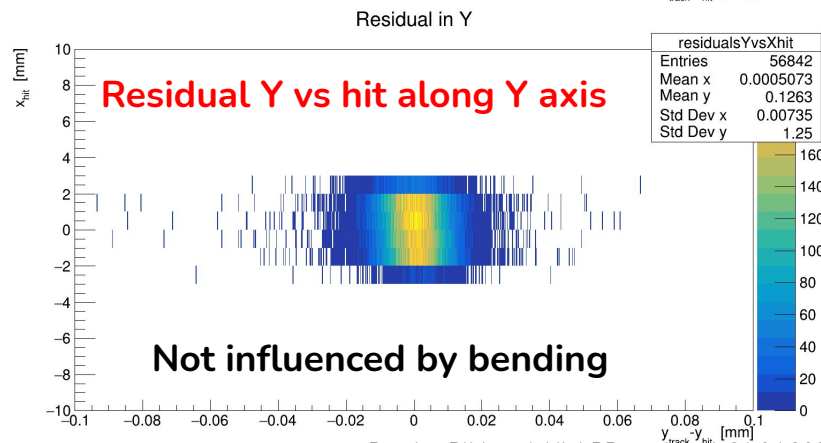
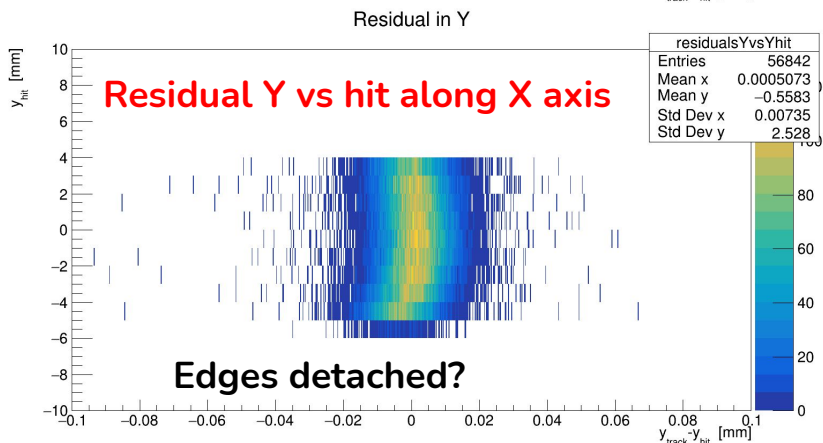
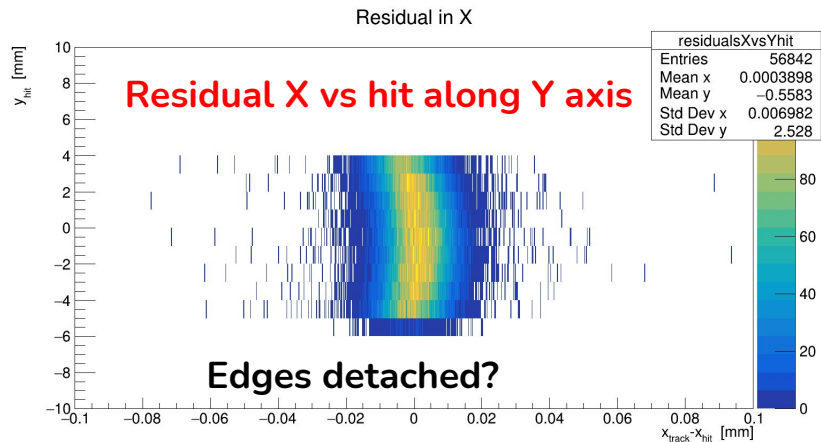
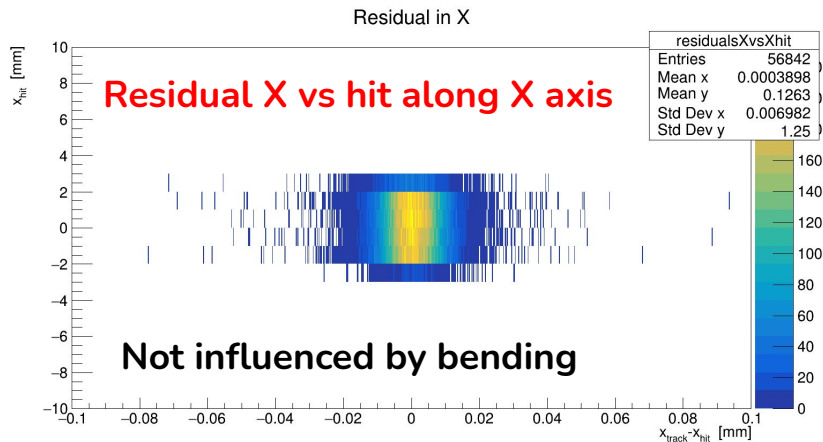


Bending ALPIDEs - part II - ROI definition

- Tracks associated to clusters on DUT
- Region of interest selection - chose part of the sensor to characterize

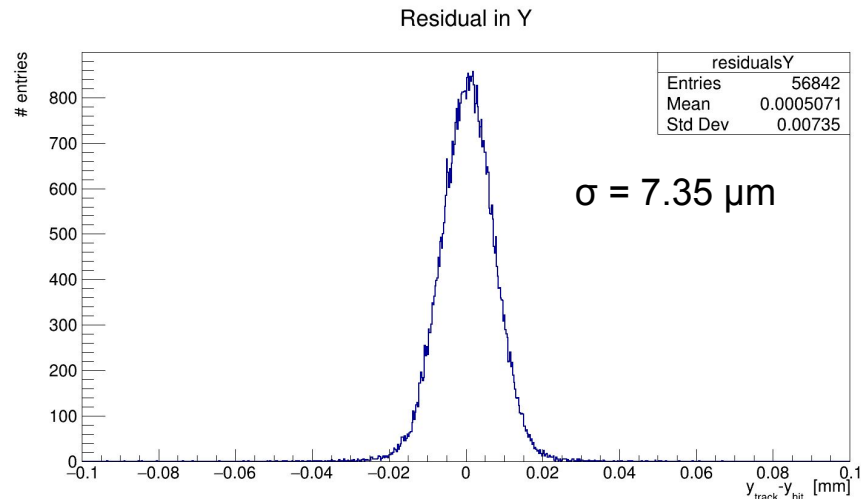
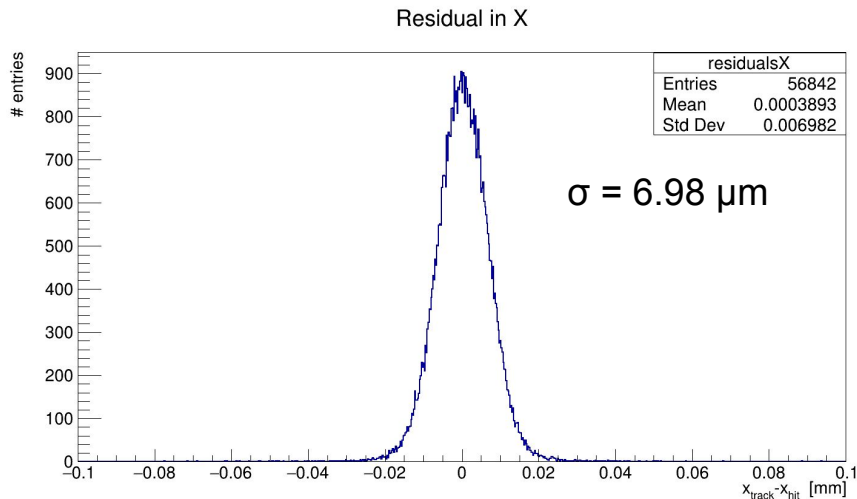


Bending ALPIDEs - part II - residuals



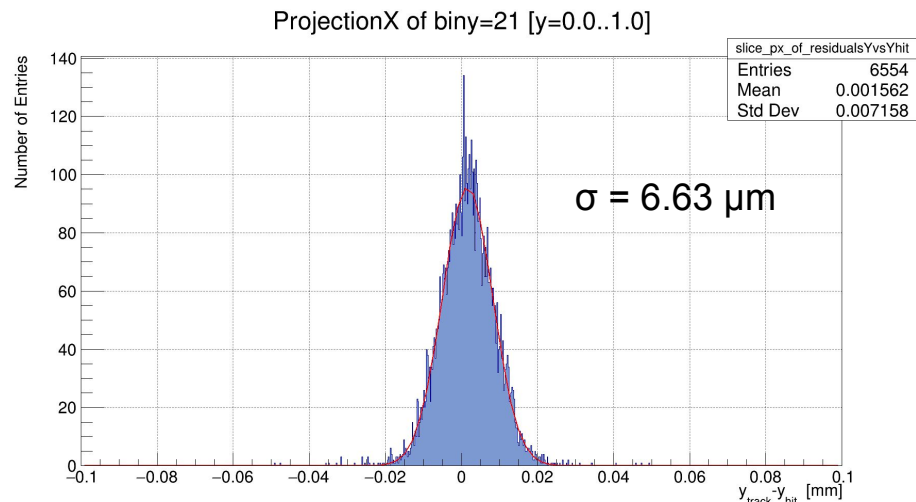
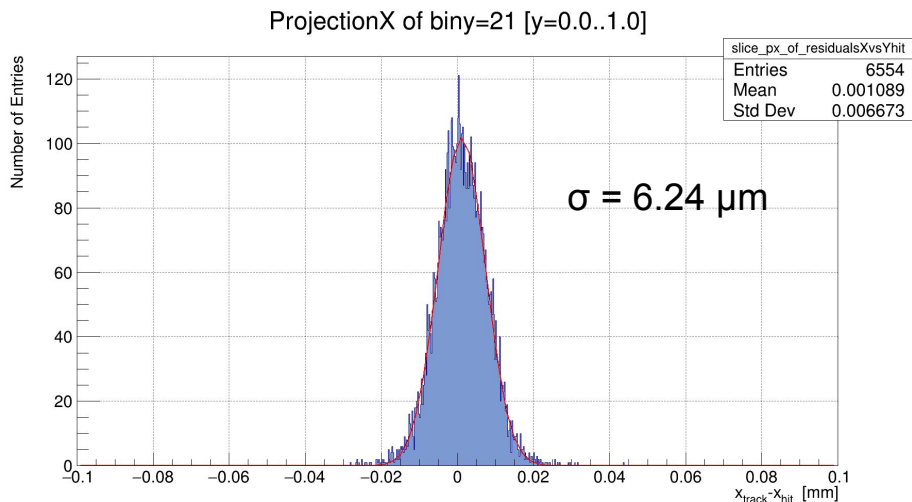
Bending ALPIDEs - part II - residuals

- Residuals are still a convolution of Gaussians, due to curvature effect



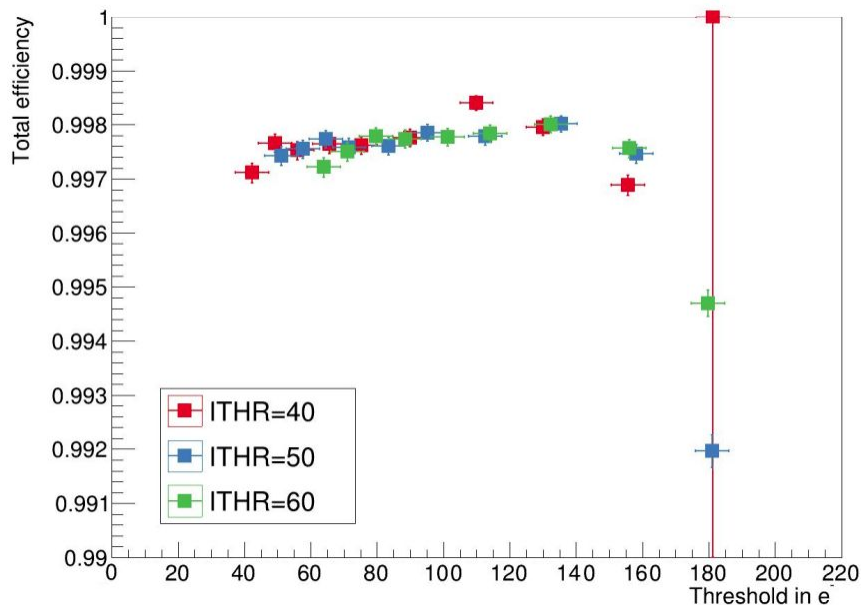
Bending ALPIDEs - part II - residuals

- Residuals are still a convolution of Gaussians, due to curvature effect
- A single bin, deconvoluted $\sim 6\text{-}7\text{ }\mu\text{m}$
- Need to subtract telescope resolution (tracking) \rightarrow resolution $\sim 5\text{ }\mu\text{m}$

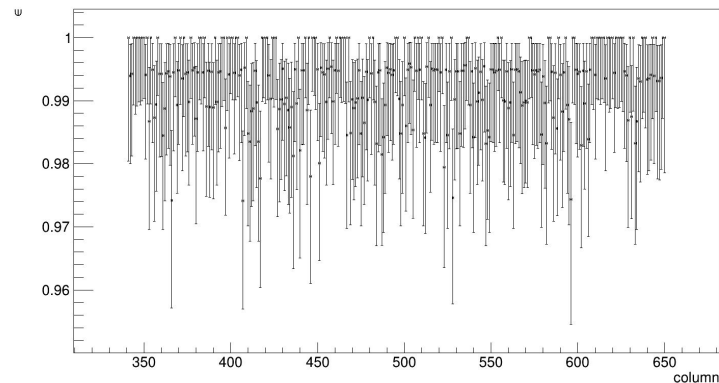


Bending ALPIDEs - part II - efficiency

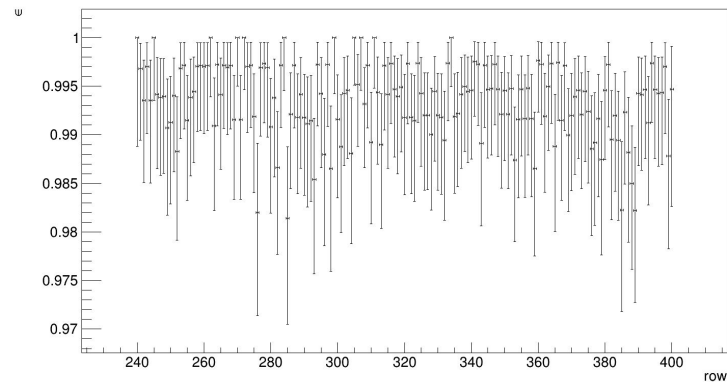
- Still highly efficient (@ 0V back bias)
- Efficiency decreases as a fc. of threshold (increasing number of inefficient pixels)



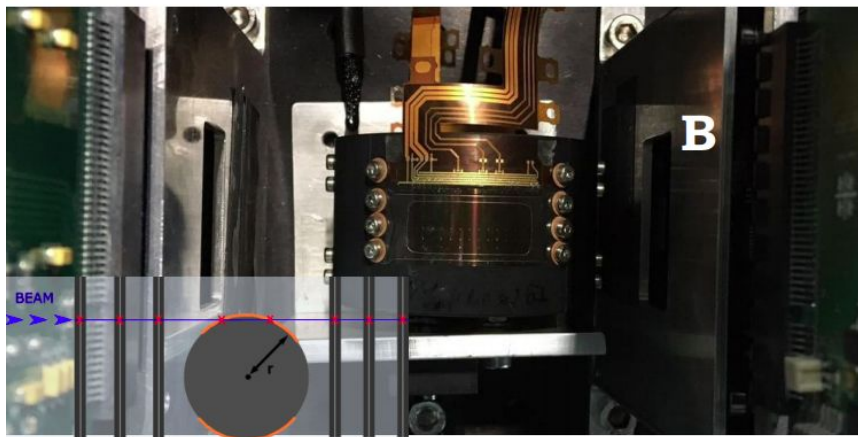
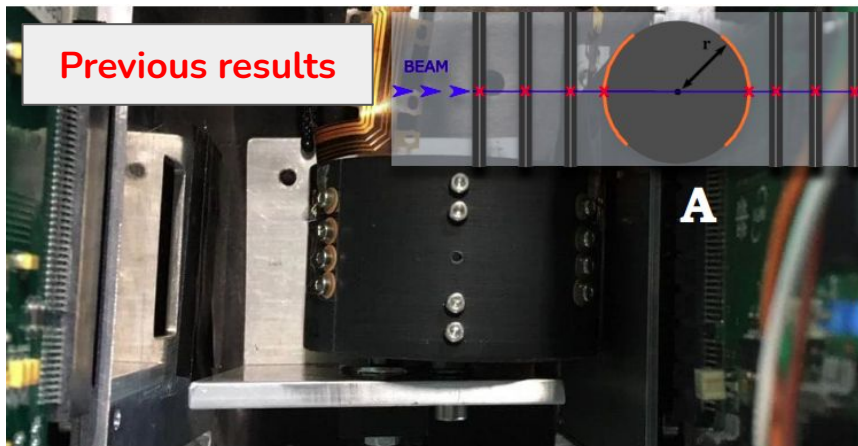
Efficiency vs. column number



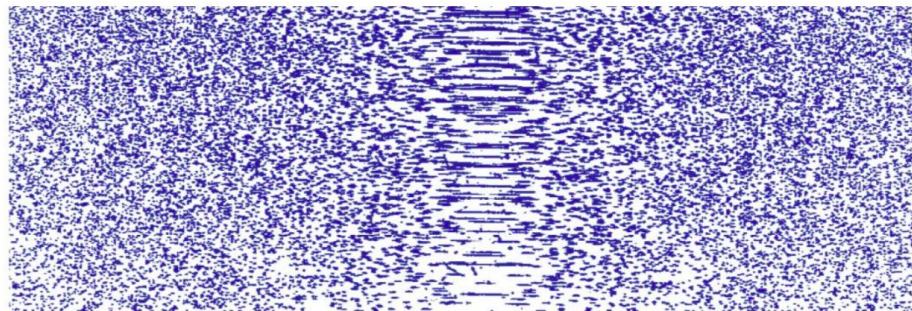
Efficiency vs. row number



Bending ALPIDEs - part II - configurations



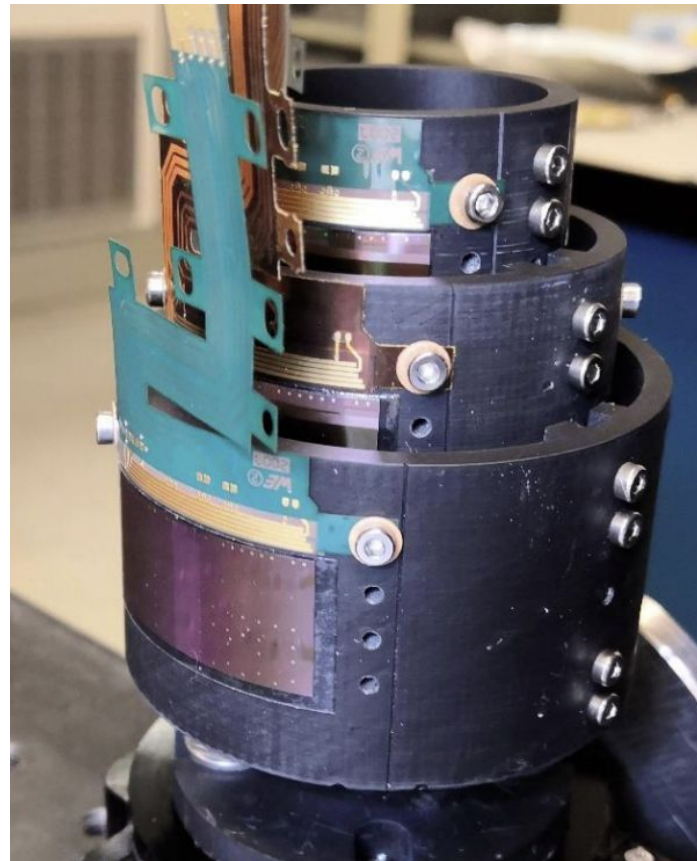
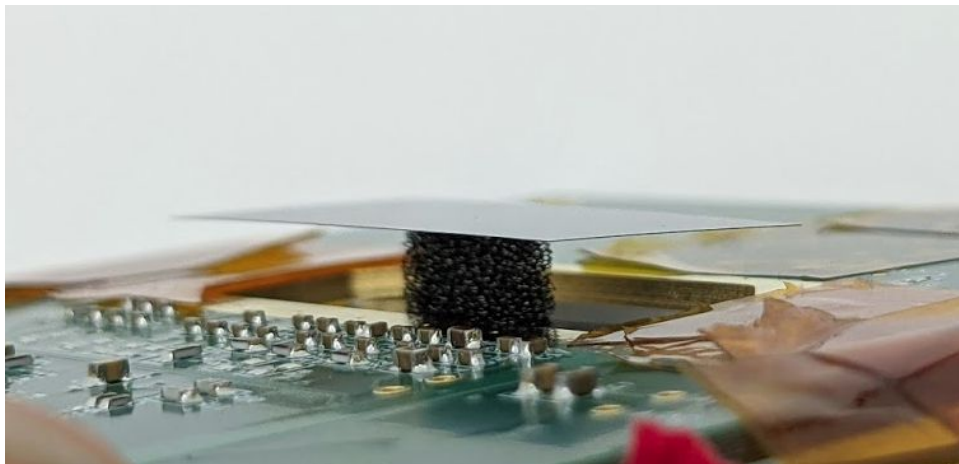
- More than one configuration possible
 - A - single crossing per sensor
 - B - double crossing / grazing
- Example hitmap - grazing events
- Can probe epitaxial layer



- ALICE proposes to build a next-generation Inner Tracking System in LHC LS3
- Based on 300mm wafer scale, ultra-thin bent MAPS → ITS3 will push the technology even further, approaching a massless detector
- R&D started with bending current ALPIDE sensors and testing them in beam
- Laboratory and testbeam results show **no performance deterioration** of the sensors after bending

Outlook - now!

- μ ITS3
 - mimics ITS3 - same radii (18, 24, 30 mm)
 - 4 out of 6 layers bonded, operational
- Carbon foam X_0 measurement
 - measure scattering on foam, determine X_0



Outlook - further out

- MLR1 (Multi Layer Reticle 1)
 - chip submitted in 65 nm technology node
 - back end of May, then testing starts

- Super ALPIDE

- 18 ALPIDE dies/ superchip
- not stitched, but interconnected

