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## TOTEM GEM tracker readout & new SRS developments

*RD51 mini-week*

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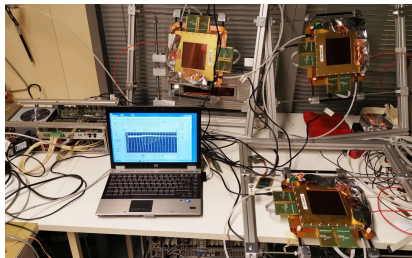
February 15-19, 2021

TOTEM-PPS Collaboration in need for a **telescope** for its **R&D programme**:

- characterisation of **detectors efficiency and uniformity** (+ timing precision) at CERN SPS-H8
- wide range of DUT technologies: scintillator tiles, scCVD sensors, LGADs, ...

## Basic requirements

- ① ~1 kHz **counting rate**
- ② **Spatial resolution** from 10  $\mu\text{m}$  – 100  $\mu\text{m}$
- ③ **Flexibility** for improvements/additions
  - e.g. event builder integration of MCP-PMT for timing applications
- ④ Portability
- ⑤ Use of already developed **DAQ**
- ⑥ Use of already existing **tracking reconstruction**
- ⑦ Reasonable cost



## Pros

- $10 \times 10 \text{ cm}^2$  area coverage
- **Spatial resolution**  $\sim 60 \mu\text{m}$
- DAQ/tracking reconstruction exist
  - ✗ DATE/AMORE software chain **not maintained anymore**
- Hardware available at CERN store, APV25 hybrids, plenty of support from RD51
- Low cost

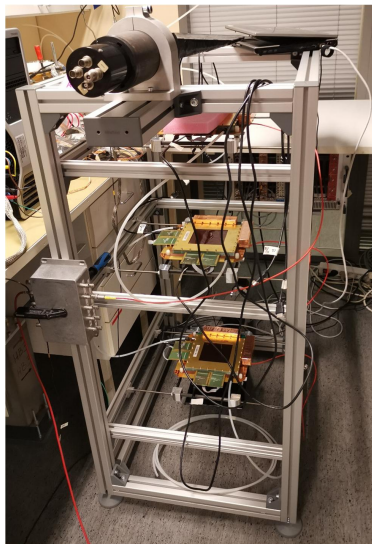
## Cons

- **Requires gas** ArCO<sub>2</sub> (70/30)
- Not easy to **transport**
  - ✓ for “permanent” installation in TOTEM-PPS experimental area at SPS-H8

Built in Helsinki, ready to be shipped to CERN

- **mechanical integration** of XY planes, muon counters, and overall frame ready
- SRS FEC and APV25 hybrids **electronics tests** successfully passed

**Timeline for first operations:**  
mid-March




F. Garcia

Given the retirement of ALICE's DATE, developed a library handling all parts of UDP communication with SRS module, and object-oriented definition of all registers.

- “Standard stack”, C++14 & CMake, designed for **portability**
  - core code relies on UNIX socket API and requires **no external dependency**
  - single shared object + headers library, **easily imported** elsewhere, heavily **documented**
- Own implementation of the `slow_control` executable with more debugging tools, dry-run mode, ...
  - library has full parsing/writing control over `.txt` configuration files, can write its own (e.g. to store per-run configuration)
- (Boost) **Python binding** for quick scripting
  - slow control implemented, readout to follow

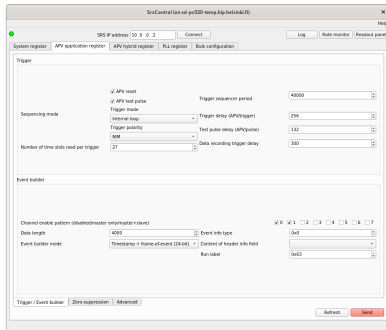
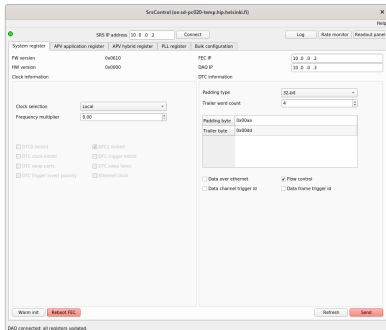
```
import pysrsdriver as srs


sc = srs.SlowControl('10.0.0.2')
apvapp = sc.readApvAppRegister()
apvapp.triggerDelay = 0x100 # BCKL_TRGDELAY
apvapp.triggerSeqPeriod = 40000 # BCKL_FREQ
# (...)
```

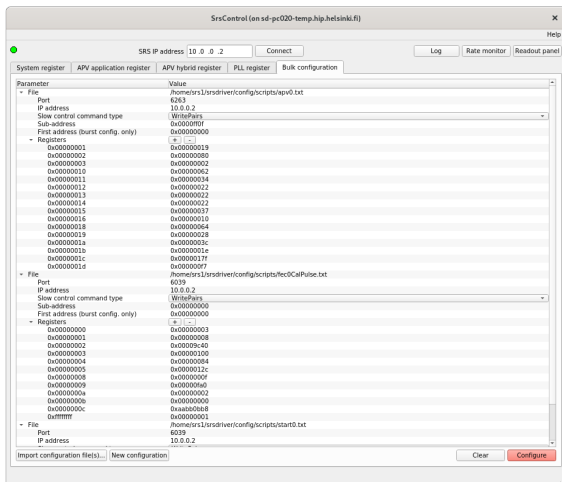
- Currently supported readout mode: APV25, zero-suppressed mode development ongoing
  - unpacking part factorised from slow control, framework to **define other data formats**
- Released under **GPLv3**, currently accessible with a CERN NICE account:
  -  /lforthom/srsdriver

## Qt5 graphical user interface with the SRS driver

- developed for the **testing and validation** of this latter
- all communication/readout is operated through the driver, only Qt as external requirement
- potential candidate for the **slow control operations** in TOTEM test beam control room



-  /lforthom/srscontrol (users with CERN NICE account)

Trivial **editor** developed to load/edit/upload external `.txt` **configuration files**


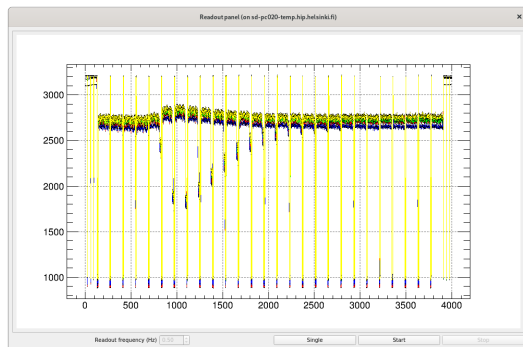
SrsControl (on sd-pc020-temp.hip.helsinki.fi)

SRS IP address: 10.0.0.2

System register	APV application register	APV hybrid register	PLL register	Bulk configuration
Parameter	Value			
- File	/home/srs1/srdriver/config/scripts/apv0.txt			
Port	6263			
IP address	10.0.0.2			
Slow control command type	WritePairs			
Sub-address	0x000000f1			
First address (burst config. only)	0x00000000			
- Registers	+ -			
0x00000001	0x00000019			
0x00000002	0x00000080			
0x00000003	0x00000002			
0x00000010	0x00000062			
0x00000011	0x00000034			
0x00000012	0x00000022			
0x00000013	0x00000022			
0x00000014	0x00000022			
0x00000015	0x00000037			
0x00000016	0x00000010			
0x00000018	0x00000064			
0x00000019	0x00000028			
0x0000001a	0x0000003c			
0x0000001b	0x0000001e			
0x0000001c	0x0000017f			
0x0000001d	0x0000007f			
- File	/home/srs1/srdriver/config/scripts/fec0CalPulse.txt			
Port	6039			
IP address	10.0.0.2			
Slow control command type	WritePairs			
Sub-address	0x00000000			
First address (burst config. only)	0x00000000			
- Registers	+ -			
0x00000000	0x00000003			
0x00000001	0x00000008			
0x00000002	0x00009c40			
0x00000003	0x00000100			
0x00000004	0x00000064			
0x00000005	0x0000012c			
0x00000008	0x0000000f			
0x00000009	0x00000008			
0x0000000a	0x00000002			
0x0000000b	0x00000000			
0x0000000c	0xaab000b8			
0x0000000e	0x00000001			
- File	/home/srs1/srdriver/config/scripts/start0.txt			
Port	6039			
IP address	10.0.0.2			

- **sequential storage** for later usage to be implemented (`.tar?`)

Easy debugging of configuration through **monitoring** of full frames and rates



- earlier is **ROOT-based**, can be easily extended with more advanced online features (fits, pedestal subtraction, ...)
- many **more rate monitors** to be added, when more unpacking modes are handled



## Slow control and data acquisition

SRS driver flexibility, one single library required for slow control and readout components

- already integrated readout part into a producer for EUDAQ2
  - optionally steer slow control through input of sequenced `.txt` configuration files, or rely on SRS control GUI for prior configuration
  - currently limited monitoring tool, access to full frame, coarse APV unpacking, still a work in progress
- possibly write a wrapper for CMS' XDAQ, or others
  - again, full documentation of API is available for users applications

## Tracking and event reconstruction

- given its ongoing development effort, Corryvreckan is a good candidate:
  - EUDAQ2 unpacking already validated,
  - only requires to define APV25 data format and GEMXY + DUT geometries
  - alignment/clustering/tracking (incl. GBL) algorithms already implemented, steering configurations to be implemented

Parallel effort ongoing for the construction, characterisation, and operation of a new triple-GEM tracker for TOTEM test beam area at H8

- **sensors** already characterised, **full readout chain** to be tested with SRS
- new implementation of a **software toolbox** to ease communication with SRS motherboard (slow control and fast readout)
- new **graphical tool** to help setting up a **run configuration**
- implementation of the **readout part** into e.g. EUDAQ2 as a proof of concept for operations at H8
- **future developments** foreseen for the reconstruction package and commissioning tools (alignment/efficiency mapping/...)

Backup

Calibration Pulse:

Injection of four different charge amplitude to four different APV25.

